# ONERA

THE FRENCH AEROSPACE LAB

www.onera.fr

# Simulation Software Ecosystem Low-Level Architecture





ALU.xml



Backend

```
?xml version="1.0" encoding="UTF-8"?>
<!-- Fichier de configuration local des points de connexion disponible pour le modele par le système Thetys -->
  <ArithmeticLogicUnit uuid="1000" alias="Foo" lang="cxx">
                            <Slot uuid="1" alias="test boolean" type="bool" unit="SU" description="">false</Slot>
                           <Slot uuid="2" alias="test_char" type="char" unit="SU" description="">0</Slot>
                           <Slot uuid="3" alias="test octet" type="octet" unit="SU" description="">0</Slot>

<Slot uuid="3" alias="test_octet" type="octet" unit="SU" description="">o</Slot>

<Slot uuid="4" alias="test_short" type="short" unit="SU" description="">o</Slot>

<Slot uuid="5" alias="test_unsigned_short" type="ushort" unit="SU" description="">o</Slot>

<Slot uuid="6" alias="test_long" type="long" unit="SU" description="">o</Slot>

<Slot uuid="7" alias="test_unsigned_long" type="ulong" unit="SU" description="">o</Slot>

<Slot uuid="8" alias="test_long_long" type="longlong" unit="SU" description="">o</Slot>

<Slot uuid="9" alias="test_unsigned_long_long" type="ulonglong" unit="SU" description="">o</Slot>

<Slot uuid="10" alias="test_float" type="float" unit="SU" description="">o</Slot>

<Slot uuid="11" alias="test_float" type="float" unit="SU" description="">o</Slot>

<Slot uuid="11" alias="test_float" type="float" unit="SU" description="">o</Slot>

<Slot uuid="11" alias="test_float" type="float" unit="SU" description="">o</Slot>

</slot unit="12" alias="test_float" unit="SU" description="">o</slot>

</slot unit="12" alias="test_float" type="float" unit="SU" description="">o</slot>

</slot unit="12" alias="test_float" type="float" unit="SU" description="">o</slot</slot>

</slot unit="12" alias="test_float" type="float" unit="SU" description="">o</slot</slot</slot</slot</slot</slot</slot</slot</slot</slot</slot</slot</slot</td>
</slot</tr>
</slot</table>
                            <Slot uuid="12" alias="test_string" type="string" unit="SU" description="">test</Slot>
                           <$lot uuid="13" alias="test_vec2f" type="vec2f" unit="SU" description="">0.0 0.1
                           <$lot uuid="14" alias="test vec3f" type="vec3f" unit="SU" description="">0.0 0.1 0.2</$lot>
                           <$lot uuid="15" alias="test vec4f" type="vec4f" unit="SU" description="">0.0 0.1 0.2 0.3
                           <Slot uuid="16" alias="test_matrixf" type="matrixf" type="mat

<Slot uuid="17" alias="test_vec2d" type="vec2d" unit="SU" description="">0.0 0.1</Slot>

<Slot uuid="18" alias="test_vec3d" type="vec2d" unit="SU" description="">0.0 0.1 0.2</Slot>

<Slot uuid="18" alias="test_vec4d" type="vec4d" unit="SU" description="">0.0 0.1 0.2 0.3</Slot>

<Slot uuid="20" alias="test_matrixd" type="matrixd" unit="SU" description="">0.0 0.1 0.2 0.3</Slot>
</slot uuid="20" alias="test_matrixd" type="matrixd" unit="SU" description="">0.0 0.1 0.2 0.3 1.0 1.1 1.2 1.3 2.0 2.1 2.2 2.3 3.0 3.1 3.2 3.3</Slot>

             </Input>
                            <Slot uuid="21" alias="test_boolean" type="bool" unit="SU" description="">false</Slot>
                            <$lot uuid="22" alias="test char" type="char" unit="SU" description="">0</$lot>
                            <Slot uuid="23" alias="test octet" type="octet" unit="SU" description="">0</Slot>
                            <Slot uuid="24" alias="test_short" type="short" unit="SU" description="">0</Slot>
                           <Slot uuid="25" alias="test_unsigned_short" type="ushort" unit="SU" description="">0</Slot>
                        <Slot uuid="25" alias="test_unsigned_short" type="ushort" unit="SU" description="">0</Slot>
<Slot uuid="26" alias="test_long" type="long" unit="SU" description="">0</Slot>
<Slot uuid="27" alias="test_long_long" type="ulong" unit="SU" description="">0</Slot>
<Slot uuid="28" alias="test_long_long" type="ulonglong" unit="SU" description="">0</Slot>
<Slot uuid="29" alias="test_long_long_long" type="ulonglong" unit="SU" description="">0</Slot>
<Slot uuid="30" alias="test_float" type="float" unit="SU" description="">0</Slot>
<Slot uuid="31" alias="test_float" type="float" unit="SU" description="">0</Slot>
<Slot uuid="31" alias="test_string" type="string" unit="SU" description="">0</Slot>
<Slot uuid="32" alias="test_vec2f" type="vec2f" unit="SU" description="">0</Slot>
<Slot uuid="34" alias="test_vec3f" type="vec2f" unit="SU" description="">0</Slot>
<Slot uuid="35" alias="test_vec4f" type="vec2f" unit="SU" description="">0</Slot>
<Slot uuid="35" alias="test_vec4f" type="vec4f" unit="SU" description="">0</Slot>
<Slot unid="36" alias="test_vec4f" type="vec4f" unit="SU" description=">0</Slot>
<Slot unid="36" alias="test_vec4f" type="vec4f" unit="SU" description=">0</Slot>
<Slot unid="36" alias="test_vec4f" type="ter3f" unit="SU" description=">0</Slot</Slot>
<Slot unid="36" alias="test_vec4f" type="ter3f" unit="SU" description=">0</Slot</Slot>
<Slot unid="36" alias="test_vec4f" type="ter3f" unit="SU" description=">0</Slot</Slot>
<Slot unid="36" alias="test_v
                           <slot uuid="36" alias="test_matrixf" type="matrixf" unit="SU" description="">0.0 0.1 0.2 0.3 1.0 1.1 1.2 1.3 2.0 2.1 2.2 2.3 3.0 3.1 3.2 3.4/$lot>
                           <$lot uuid="37" alias="test vec2d" type="vec2d" unit="SU" description="">0.0 0.1
                           <$lot uuid="38" alias="test vec3d" type="vec3d" unit="SU" description="">0.0 0.1 0.2</$lot>
                           <Slot uuid="39" alias="test_vec4d" type="vec4d" unit="SU" description="">0.0 0.1 0.2 0.3
                           <Slot uuid="40" alias="test matrixd" type="matrixd" unit="SU" description="">0.0 0.1 0.2 0.3 1.0 1.1 1.2 1.3 2.0 2.1 2.2 2.3 3.0 3.1 3.2 3.3</Slot>
    /ArithmeticLogicUnit>
```

ALU.xml the I/O model definition file

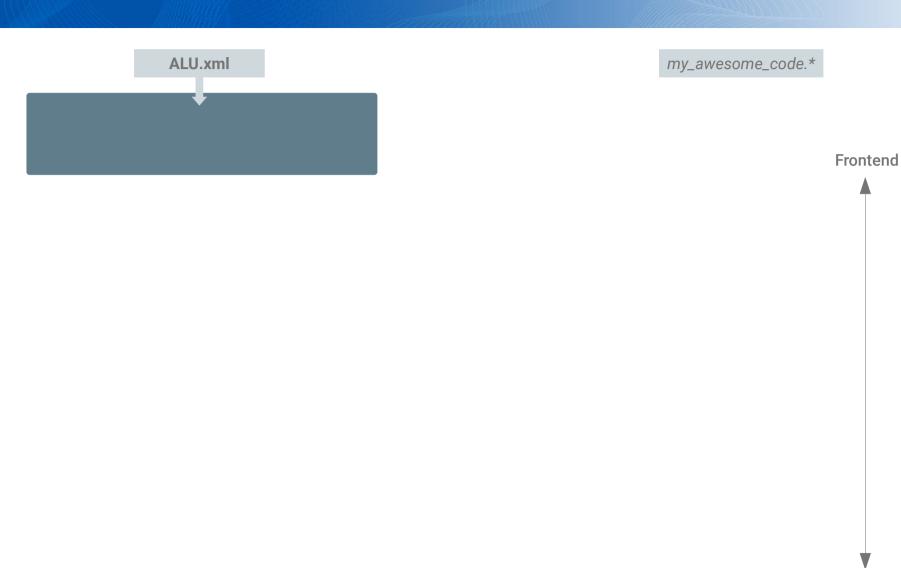


ALU.xml

my\_awesome\_code.\*

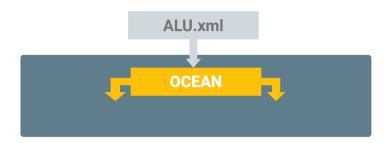
Frontend







Backend

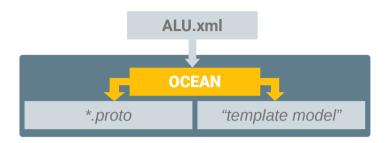


my\_awesome\_code.\*







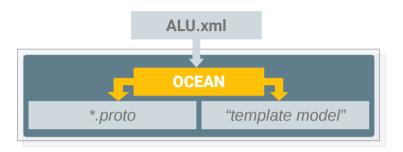


my\_awesome\_code.\*







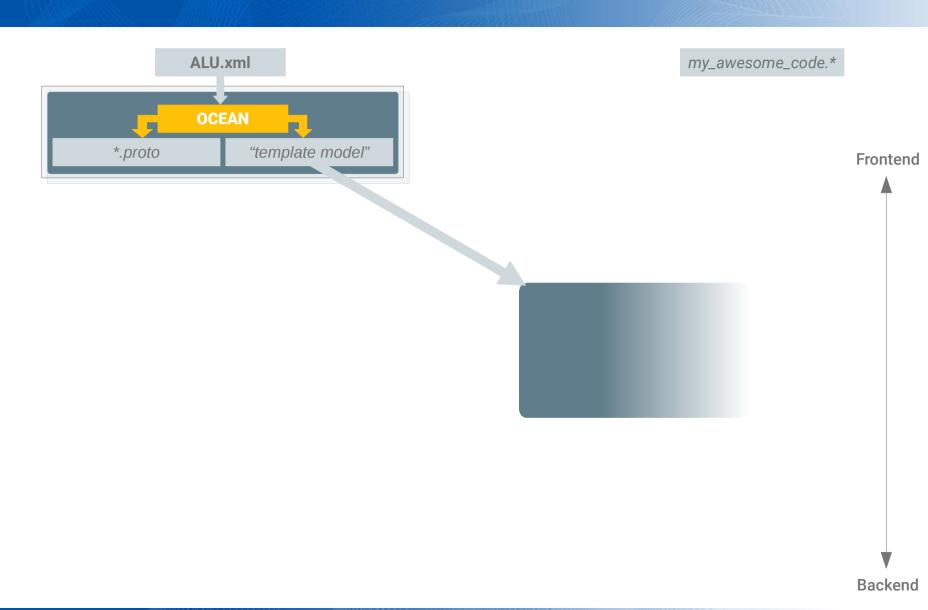


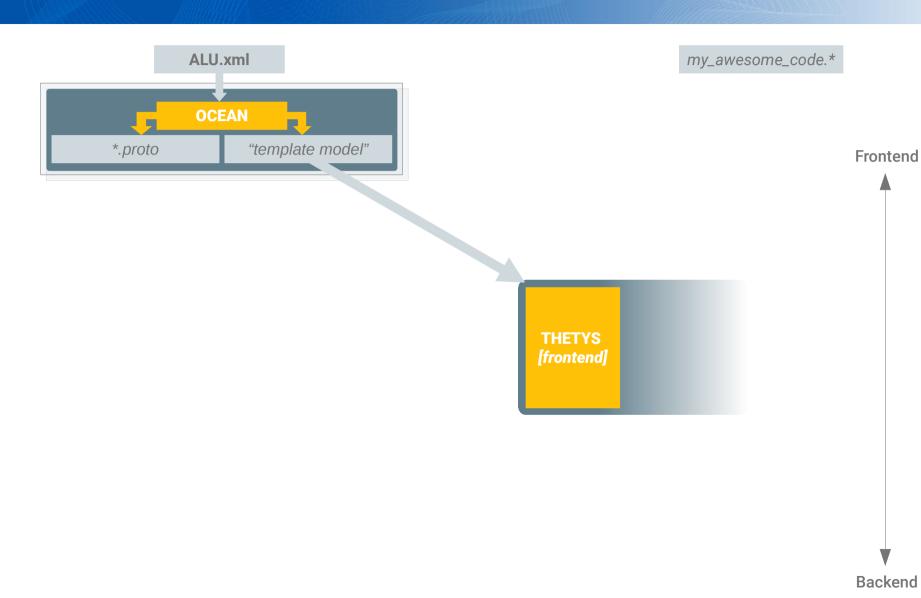
my\_awesome\_code.\*

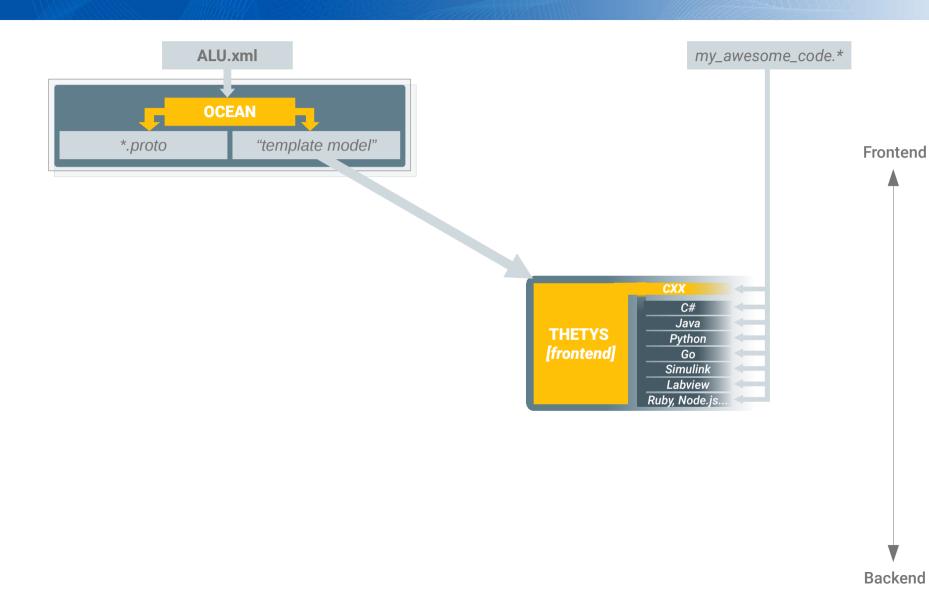
Frontend



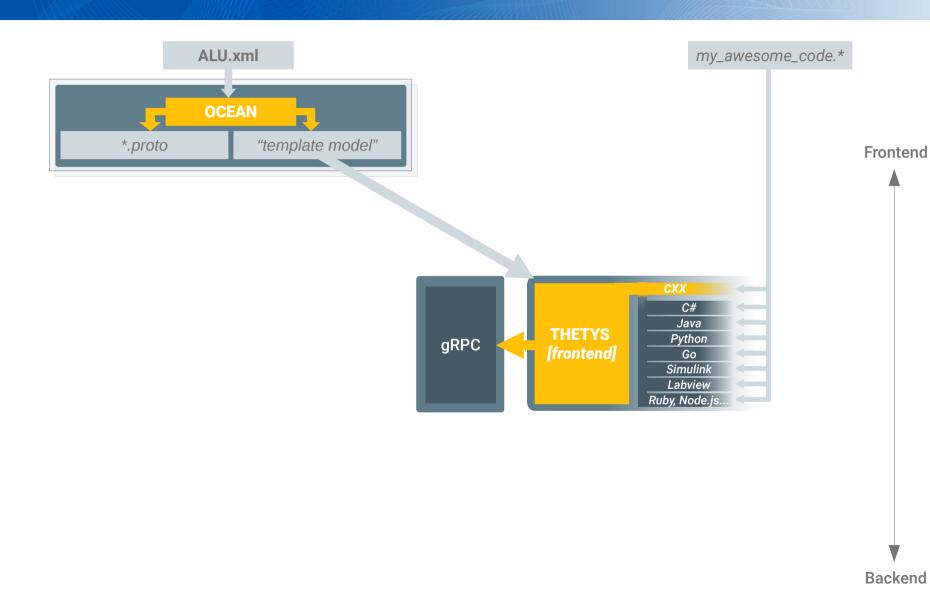


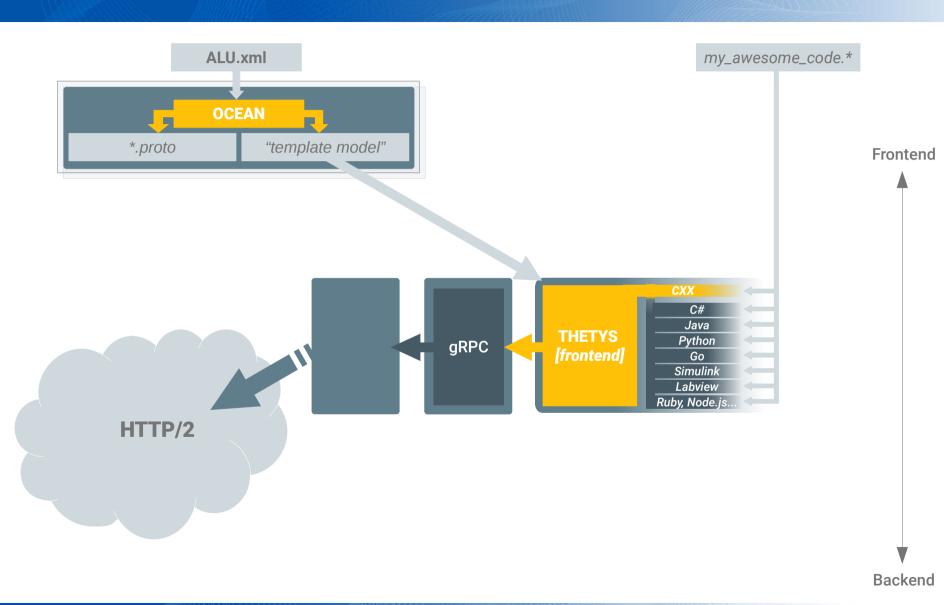




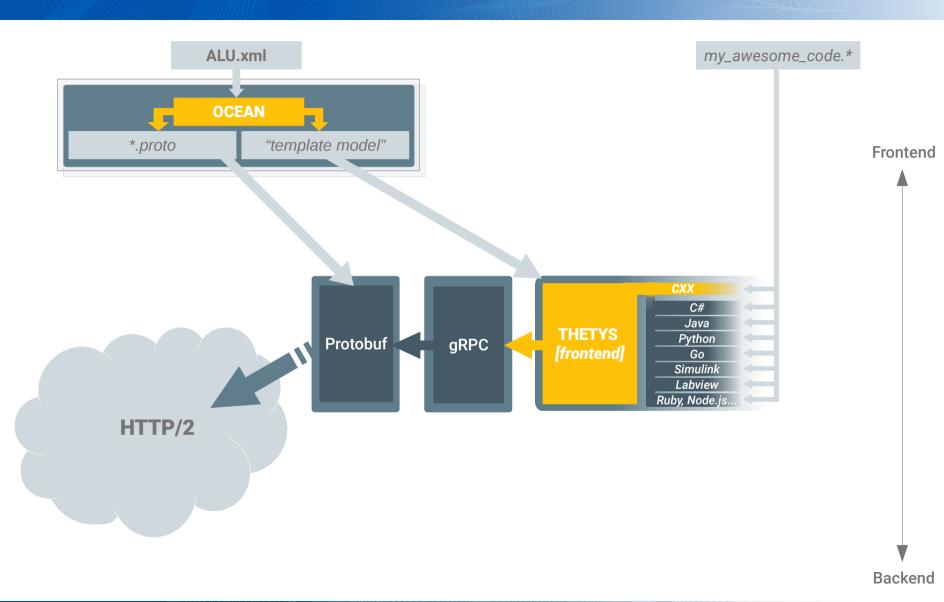




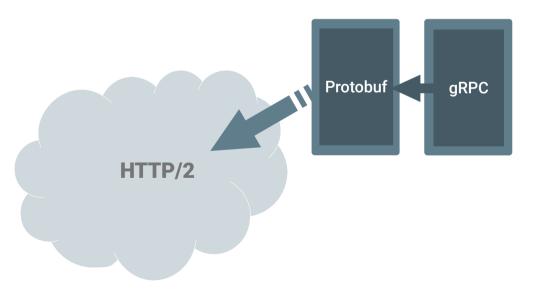










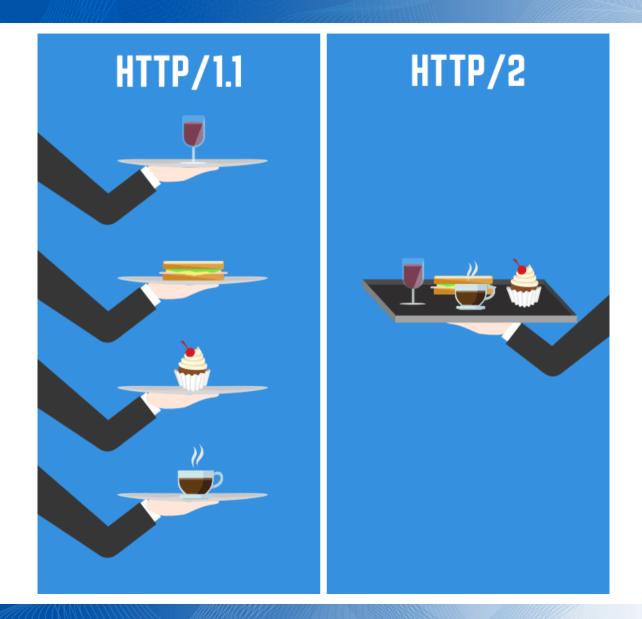




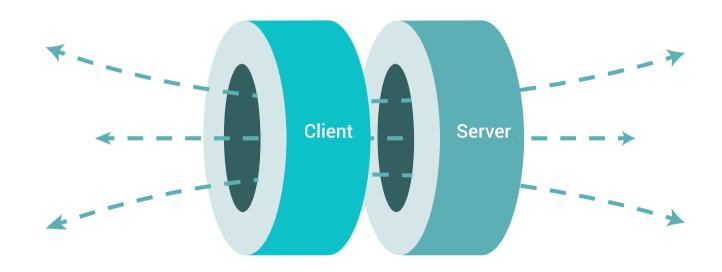


gRPC is an open source *remote procedure call* (RPC) system initially developed at Google. It uses *HTTP/2* for transport, *Protocol Buffers* as the interface description language, and provides features such as authentication, bidirectional streaming and flow control, blocking or nonblocking bindings, and cancellation and timeouts. It generates cross-platform client and server bindings for many languages.

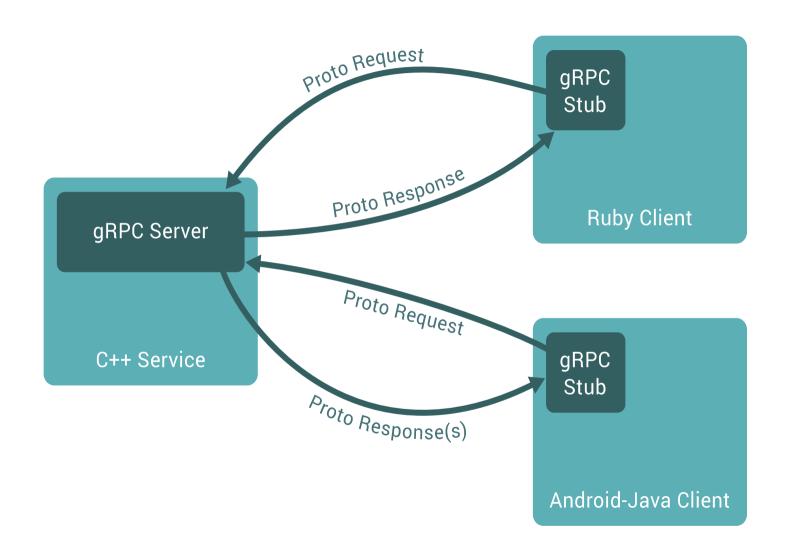




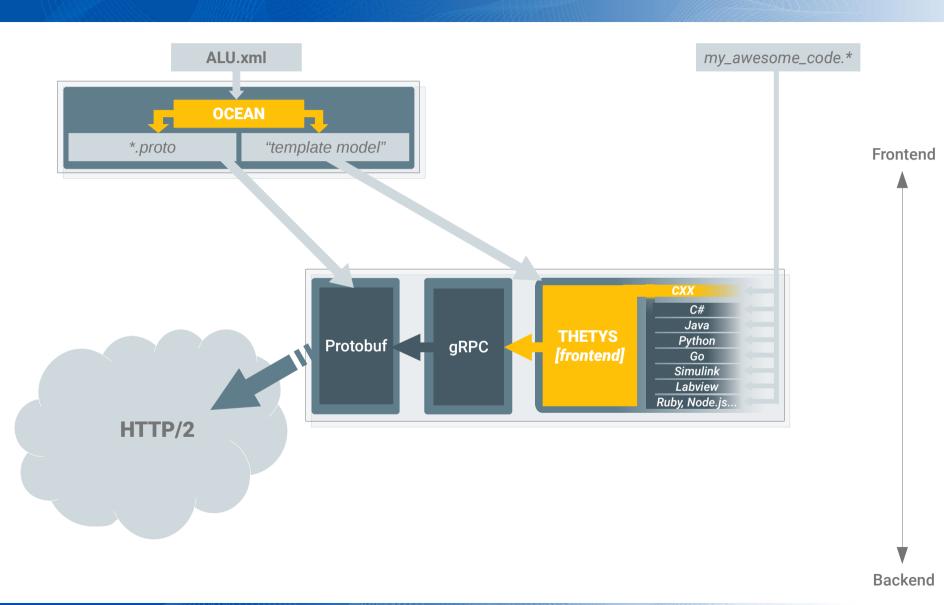




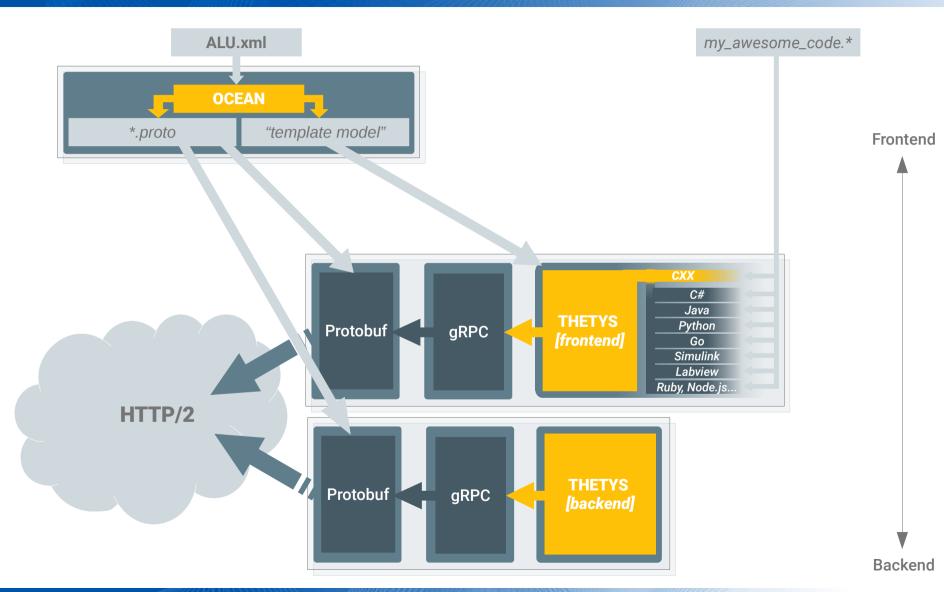


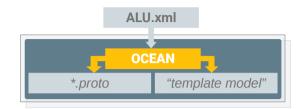










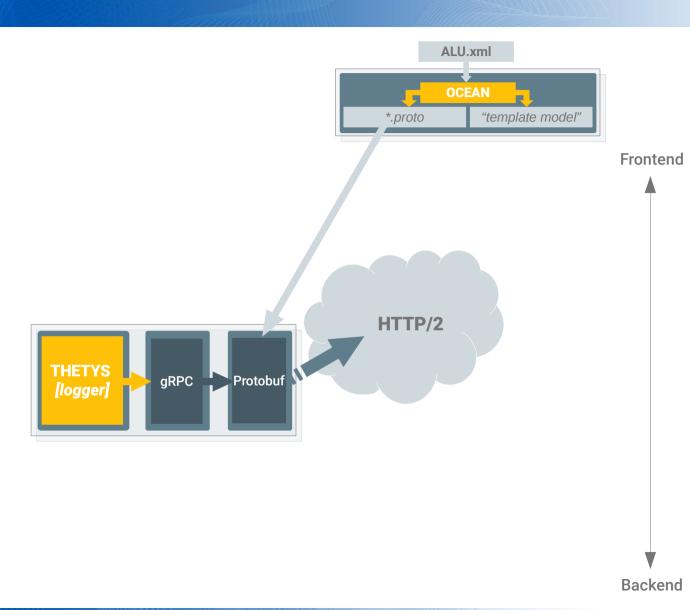




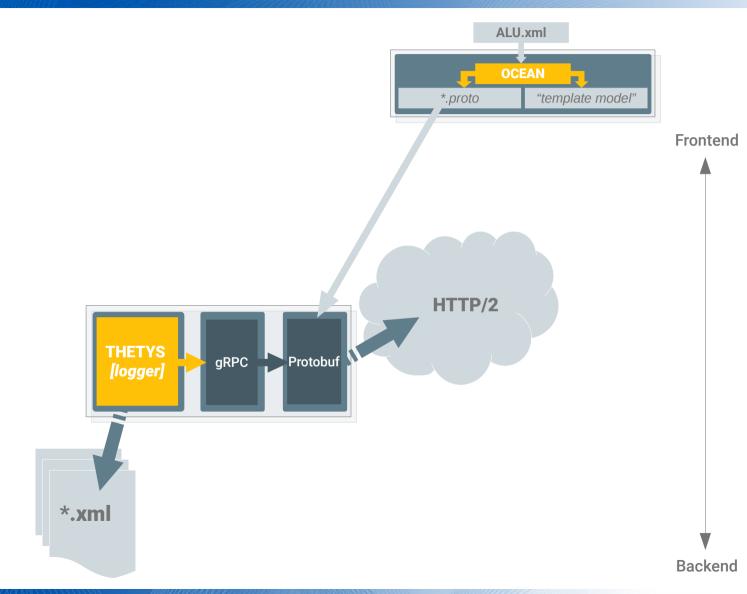


Frontend

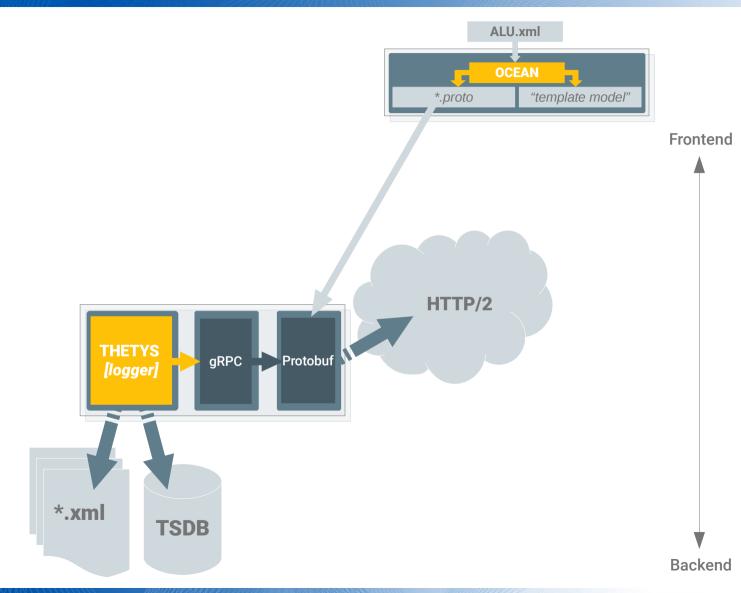




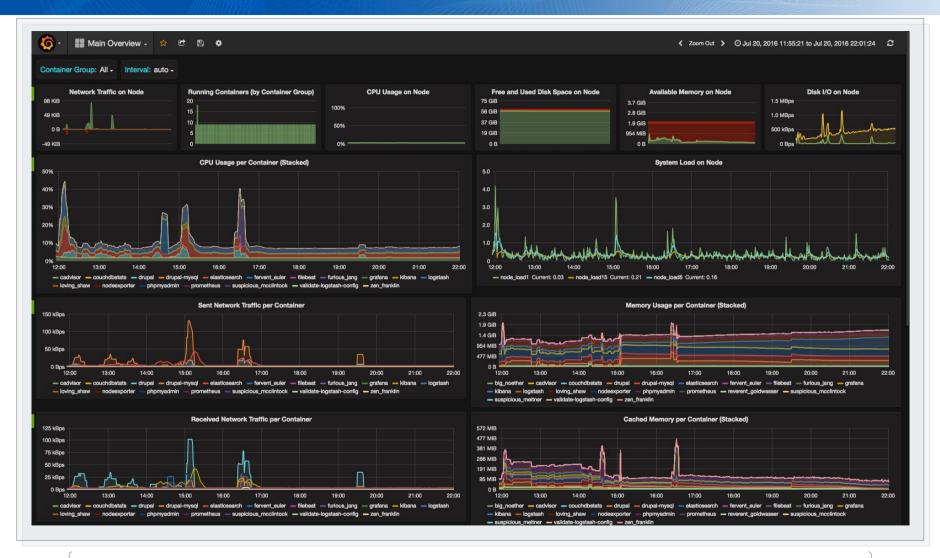






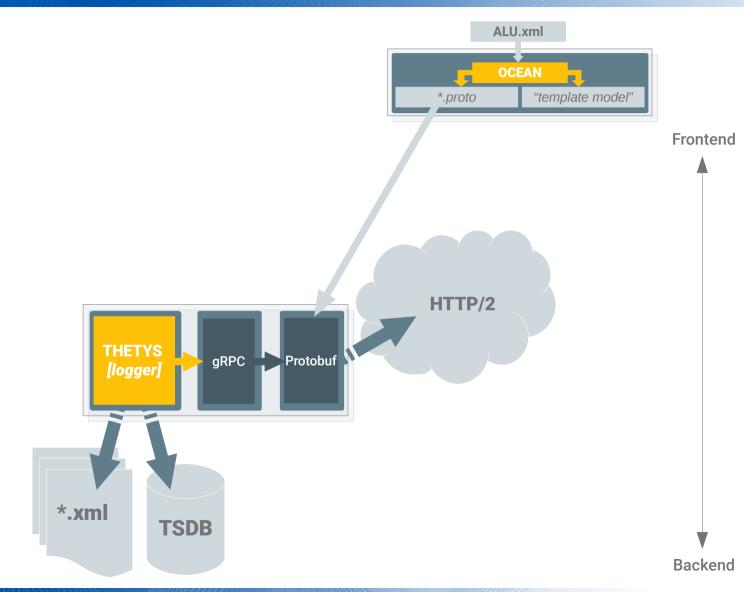




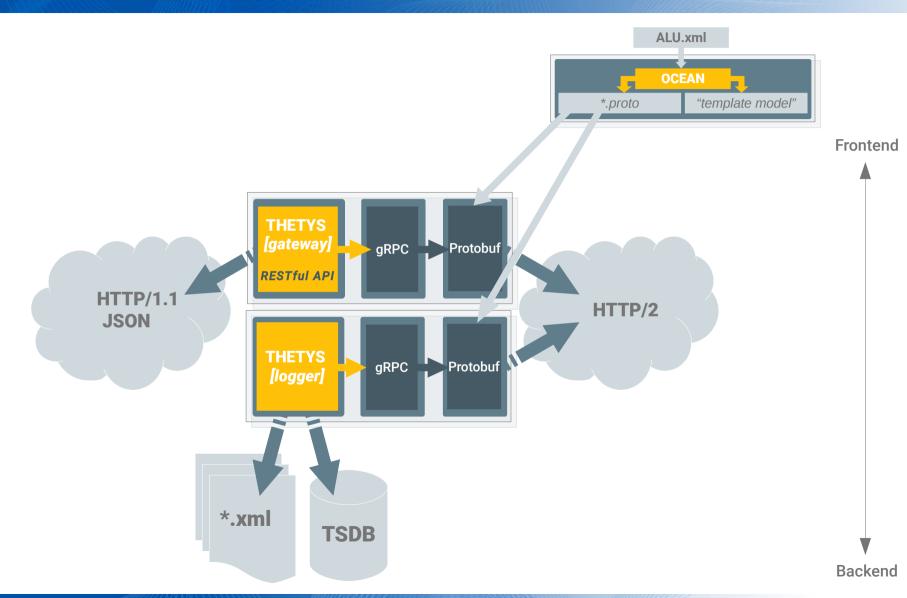


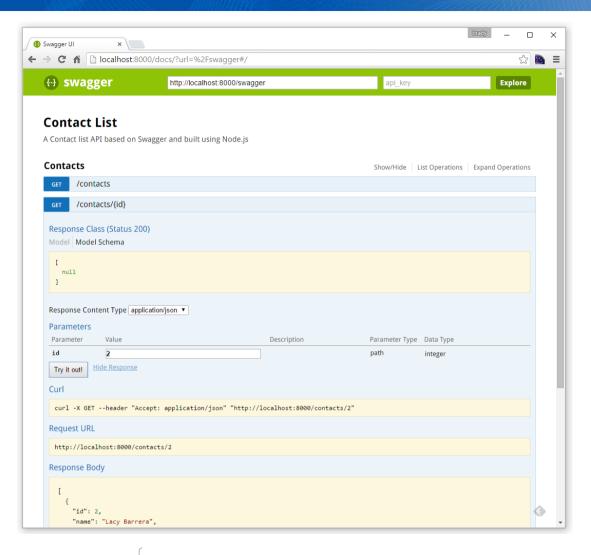
TSDB (Time Series DataBase) Toolchain → [InfluxDB + Graphana + Graphite (Web GUI)]









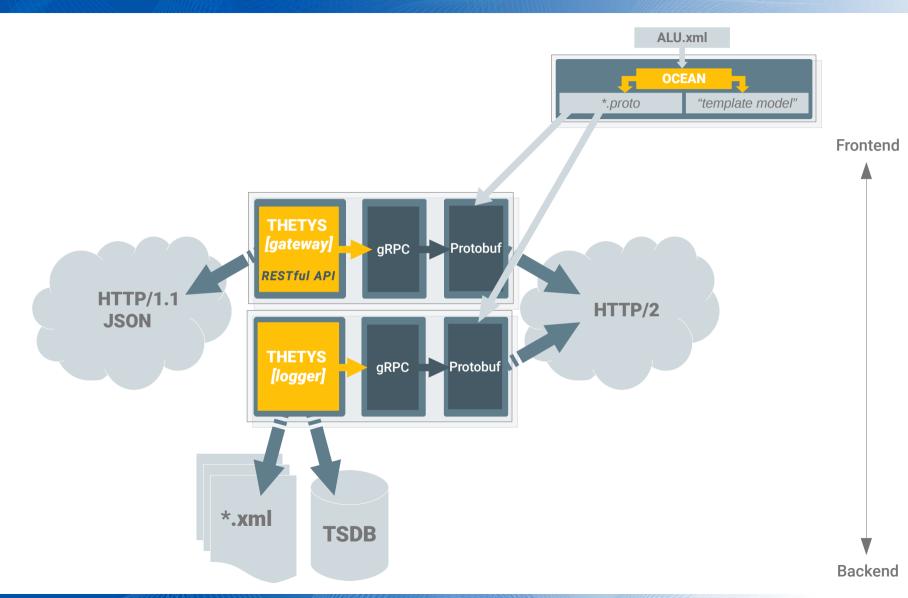


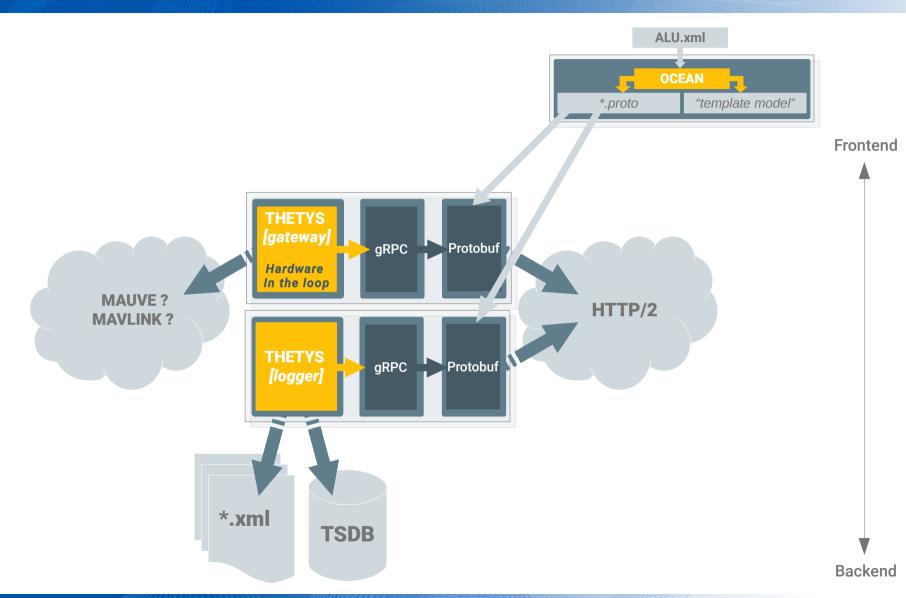


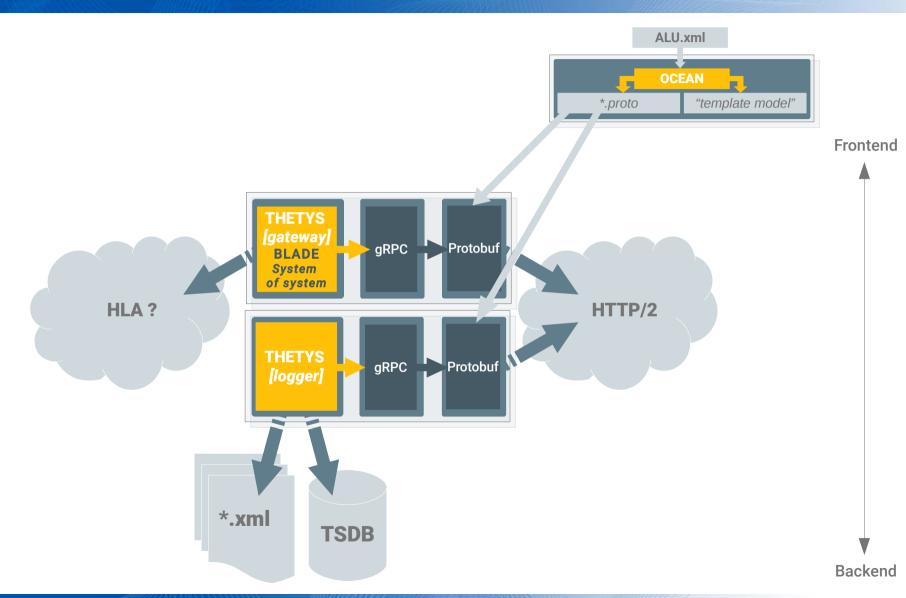


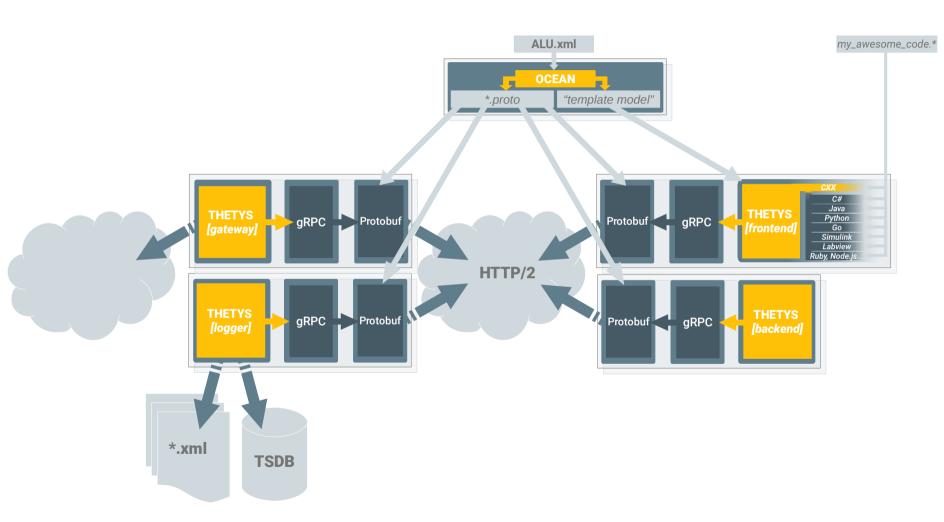
Supervisor Toolchain → [ OpenAPI + SwaggerUI (Web GUI) ]

















ISU.xml SLU.xml

HCU.xml





```
<?xml version="1.0" encoding="utf-8"?>
   <!-- Fichier de configuration de l'ensemble des points de connexion disponible pour chacun des modeles par le système KronosDDS -->
4 < InstructionSetUnit>
        <ArithmeticLogicUnit uuid="1000" alias="Foo" lang="cxx">
                <Slot uuid="1" alias="test boolean" type="bool" unit="SU" description=""></Slot>
8
9
10
11
12
13
14
15
16
17
18
19
20
21
                <Slot uuid="2" alias="test char" type="char" unit="SU" description=""></Slot>
                <Slot uuid="3" alias="test octet" type="octet" unit="SU" description=""></Slot>
                <Slot uuid="4" alias="test short" type="short" unit="SU" description=""></Slot>
            </Input>
            <Output>
                <Slot uuid="11" alias="test boolean" type="bool" unit="SU" description=""></Slot>
                <Slot uuid="12" alias="test char" type="char" unit="SU" description=""></Slot>
                <Slot uuid="13" alias="test octet" type="octet" unit="SU" description=""></Slot>
                <Slot uuid="14" alias="test short" type="short" unit="SU" description=""></Slot>
           </Output>
        </ArithmeticLogicUnit>
        <ArithmeticLogicUnit uuid="1000" alias="Bar" lang="python">
                <Slot uuid="21" alias="test_boolean" type="bool" unit="SU" description=""></Slot>
23
24
25
26
27
28
29⊜
                <Slot uuid="22" alias="test char" type="char" unit="SU" description=""></Slot>
                <Slot uuid="23" alias="test_octet" type="octet" unit="SU" description=""></Slot>
                <Slot uuid="24" alias="test short" type="short" unit="SU" description=""></Slot>
           </Input>
            <Output>
                <Slot uuid="31" alias="test boolean" type="bool" unit="SU" description=""></Slot>
31
                <Slot uuid="32" alias="test_char" type="char" unit="SU" description=""></Slot>
                <Slot uuid="33" alias="test octet" type="octet" unit="50" description=""></Slot>
33
34
                <Slot uuid="34" alias="test short" type="short" unit="50" description=""></Slot>
                [...]
           </Output>
       </ArithmeticLogicUnit>
   </InstructionSetUnit>
```

ISU.xml the simulation logical I/O file



```
<?xml version="1.0" encoding="UTF-8"?>
   <!-- Fichier de configuration du cablage des liaisons exécuté par le système KronosDDS -->
   <HardwireControlUnit>
       <Sink uuid="SINK 0" alias="Host0" networkInterfaceAddress="192.168.41.236" networkInterfaceCard="eth0"/>
       <Sink uuid="SINK 1" alias="Host0" networkInterfaceAddress="192.168.41.237" networkInterfaceCard="eth1"/>
       <Strand uuid="STRAND 0" alias="trim">
           <Wire uuid="WIRE 0-0" sourceAlias="Foo" sourcePlug="test boolean" destinationAlias="Bar" destinationPlug="test boolean"/>
           wire uuid="WIRE 0-1" sourceAlias="Foo" sourcePlug="test char" destinationAlias="Bar" destinationPlug="test char"/>
           wire uuid="WIRE 0-2" sourceAlias="Bar" sourcePlug="test octet" destinationAlias="Foo" destinationPlug="test octet"/>
12
13
14
           <Wire uuid="WIRE 0-3" sourceAlias="Bar" sourcePlug="test short" destinationAlias="Foo" destinationPlug="test short"/>
       </Strand>
       <Strand uuid="STRAND 1" alias="sim">
           wire uuid="WIRE 1-0" sourceAlias="Bar" sourcePlug="test boolean" destinationAlias="Foo" destinationPlug="test boolean"/>
           wire uuid="WIRE 1-1" sourceAlias="Bar" sourcePlug="test char" destinationAlias="Foo" destinationPlug="test char"/>
           wire uuid="WIRE 1-2" sourceAlias="Foo" sourcePlug="test octet" destinationAlias="Bar" destinationPlug="test octet"/>
           wire uuid="WIRE 1-3" sourceAlias="Foo" sourcePlug="test short" destinationAlias="Bar" destinationPlug="test short"/>
           [...]
       </Strand>
   </HardwireControlUnit>
```

HCU.xml the simulation physical I/O file



```
<?xml version="1.0" encoding="UTF-8"?>
   <!-- Fichier de configuration de la séguence exécuté par le système KronosDDS -->
   <SequentialLogicUnit>
       <Participant>
           <Backend uuid="BACKEND 0" alias="HW Resources 0" sinkAlias="Host0"/>
           <Backend uuid="BACKEND 1" alias="HW Resources 1" sinkAlias="Host1"/>
           <Frontend uuid="FRONTEND 0" alias="SW Consumer 0" modelAlias="Foo"/>
           <Frontend uuid="FRONTEND 1" alias="SW Consumer 1" modelAlias="Bar"/>
11
12
       </Participant>
       <Stage uuid="STAGE 0" alias="trim" strandAlias="trim">
           <Steps uuid="STEPS 0" alias="" mode="Sequential">
15
               <Step uuid="STEP 0-0" alias="first step trim" backendAlias="HW Resources 0" frontendAlias="SW Consumer 0"/>
               <Step uuid="STEP 0-1" alias="second step trim" backendAlias="HW Resources 1" frontendAlias="SW Consumer 1"/>
17
           </Steps>
       </Stage>
       <Stage uuid="STAGE 1" alias="sim" strandAlias="sim">
           <Steps uuid="STEPS 1" alias="" mode="Sequential">
               <Step uuid="STEP_1-0" alias="first_step_sim" backendAlias="HW Resources 0" frontendAlias="SW Consumer 1"/>
               <Step uuid="STEP 1-1" alias="second step sim" backendAlias="HW Resources 1" frontendAlias="SW Consumer 0"/>
               [\ldots]
           </Steps>
       </Stage>
   </SequentialLogicUnit>
```

SLU.xml

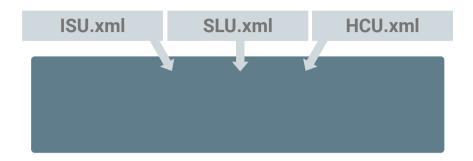
the simulation orchestrator file



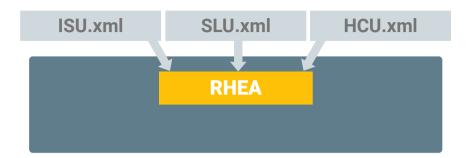
ISU.xml HCU.xml

**Frontend** 

**Backend** 

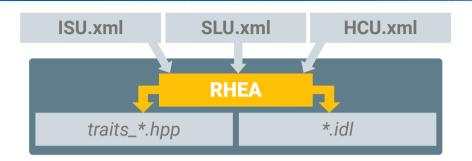




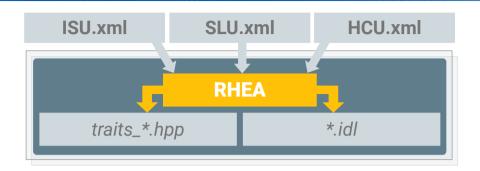






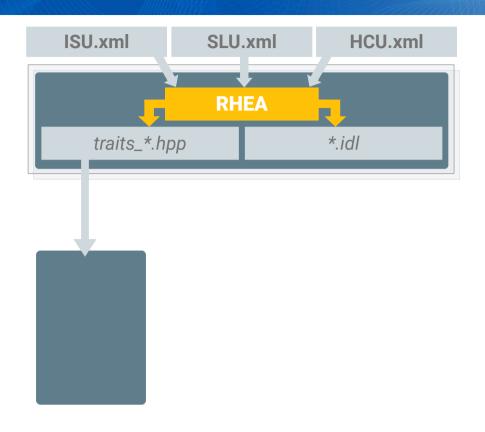






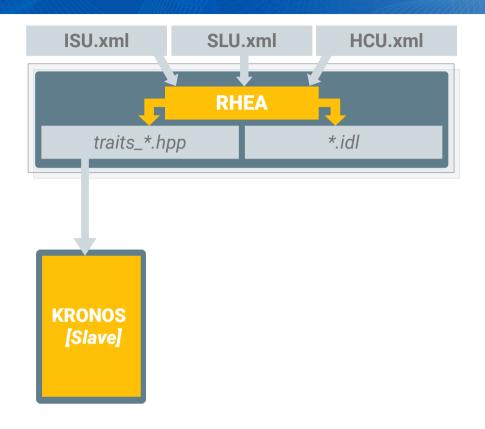






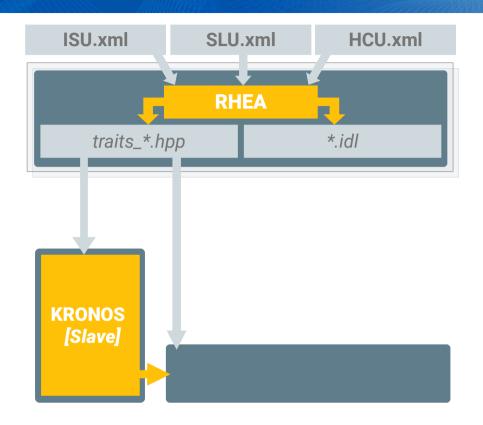




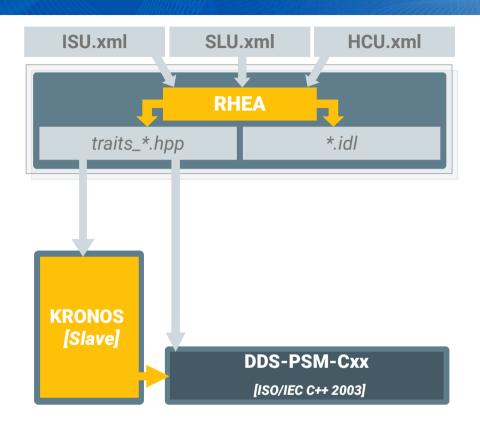




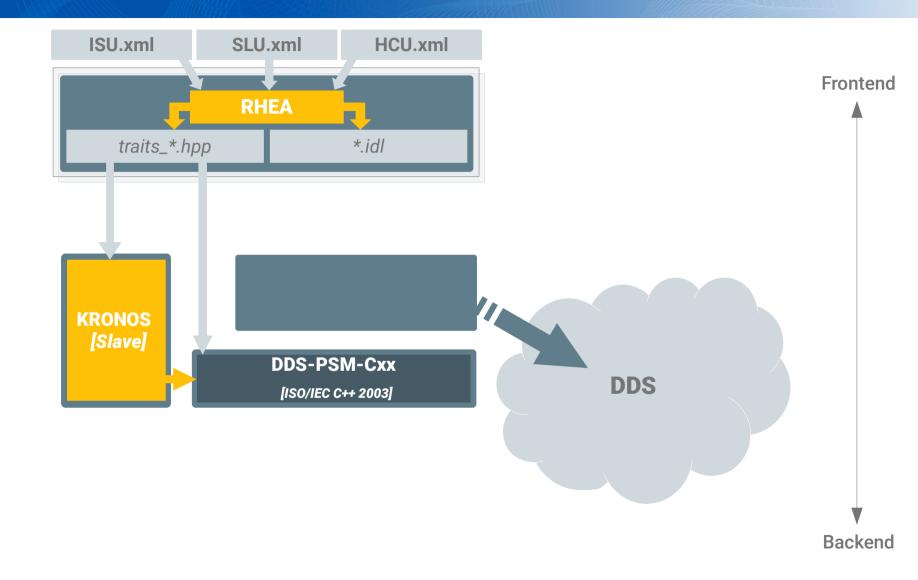




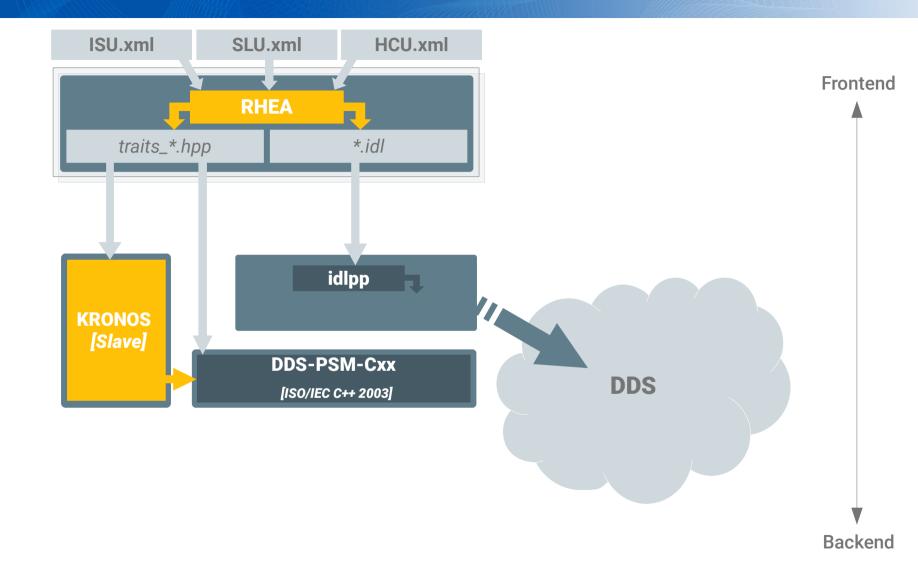




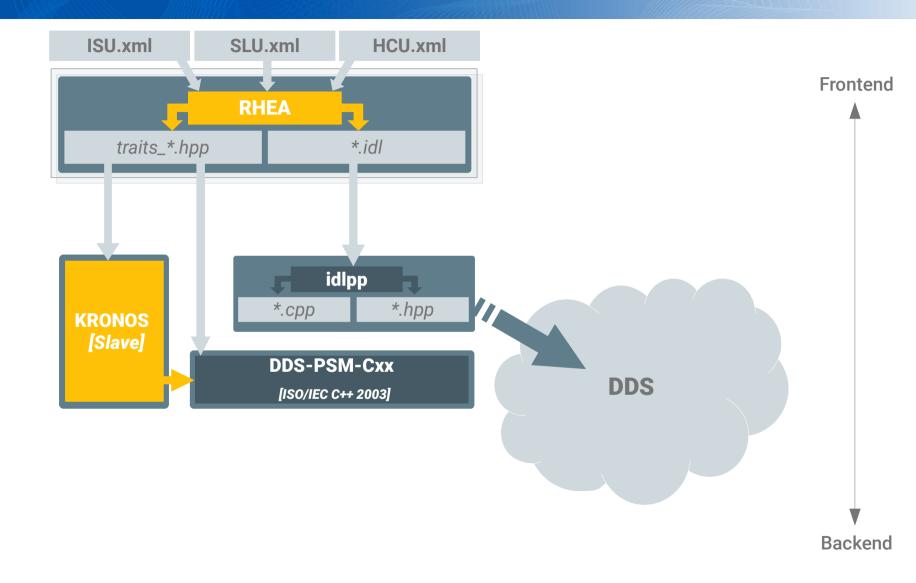




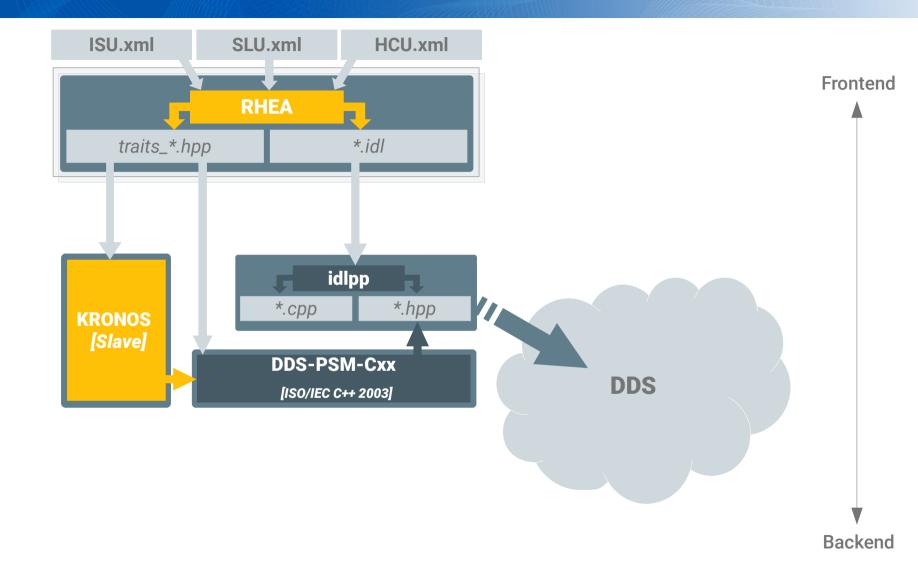




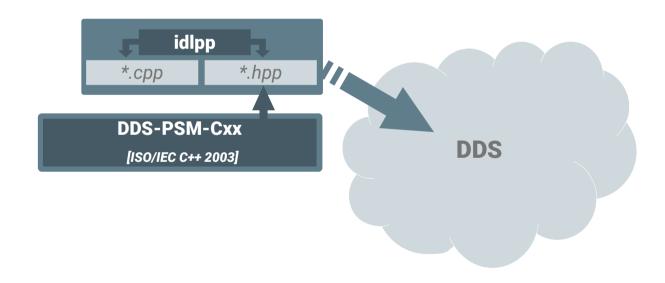










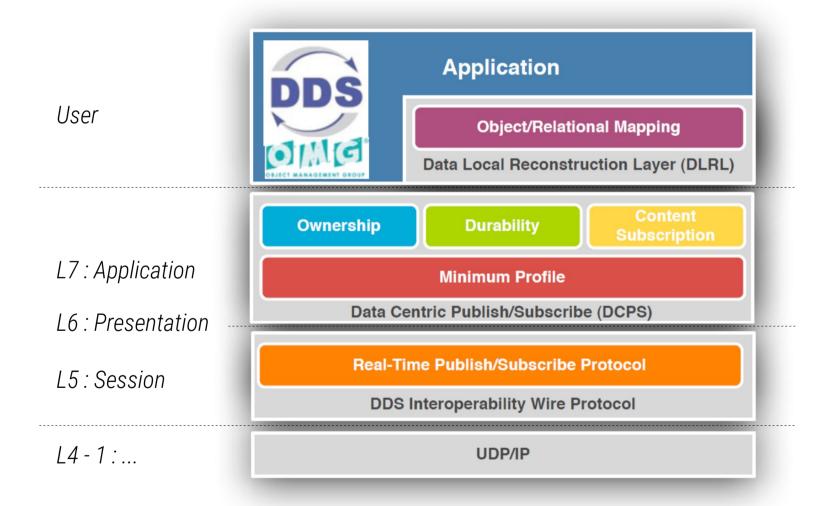




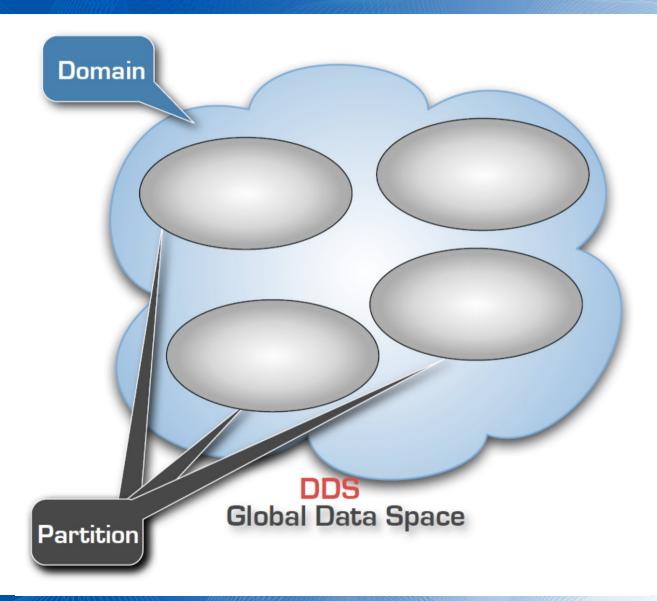


The **OMG** (Object Management Group) **DDS** (Data Distribution Service) standard is a cloud-based middleware introduced in **2004**. It stand as a standard technology for ubiquitous, interoperable, secure, platform independent, and real-time data sharing across network connected devices. DDS behaviour and semantics can be controlled via a rich set of **QoS** (Quality of Service) Policies.





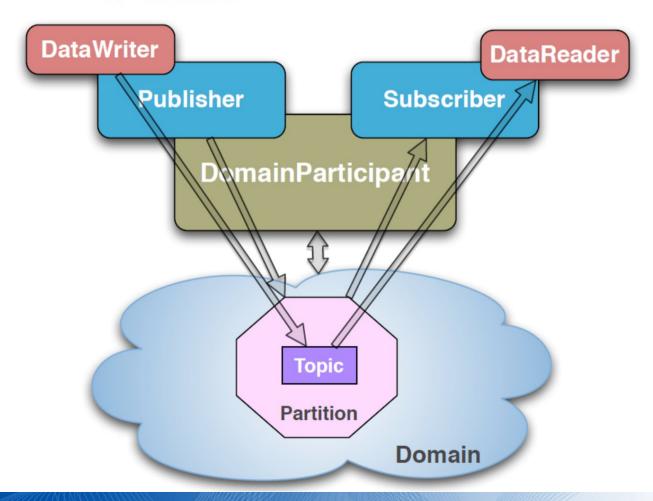




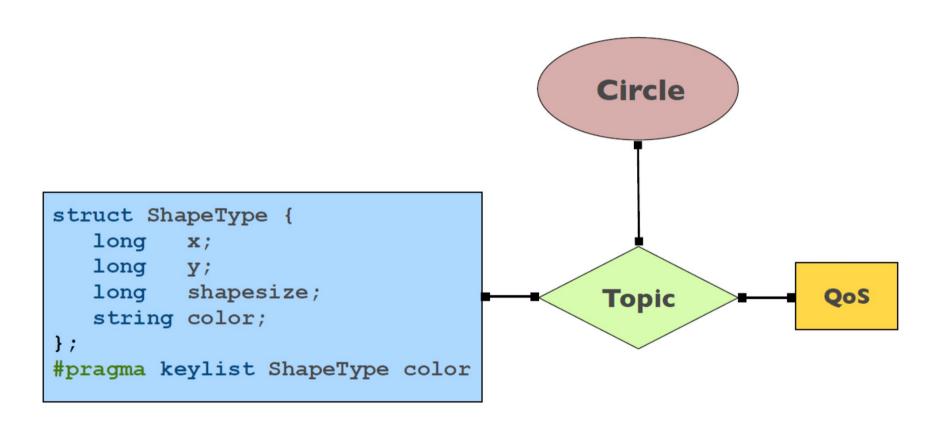


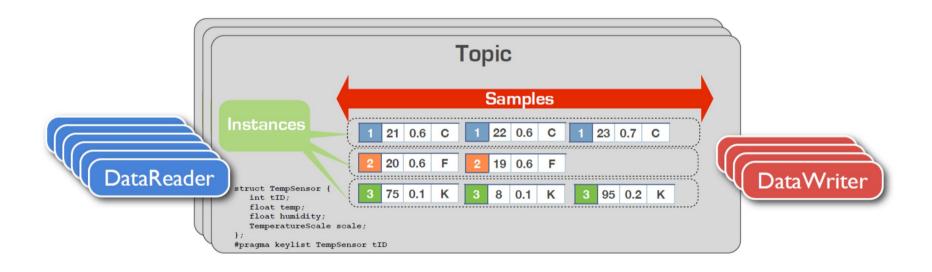
DP: DomainParticipantP: DataReaderDW: DataWriter

S: Subscribe

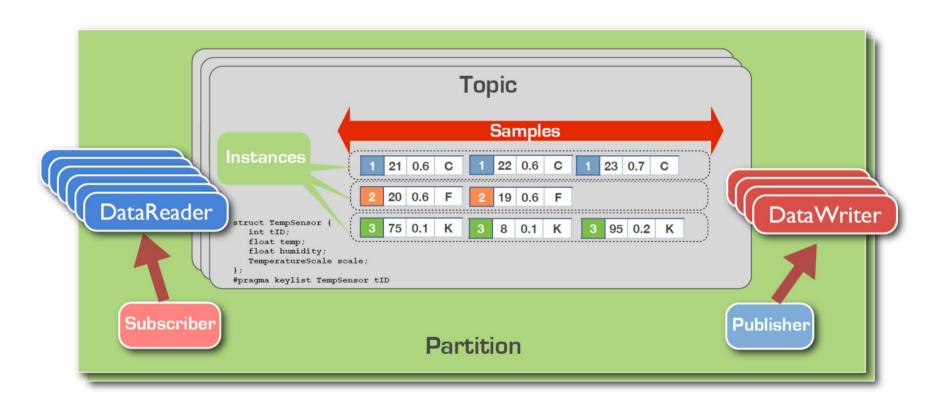




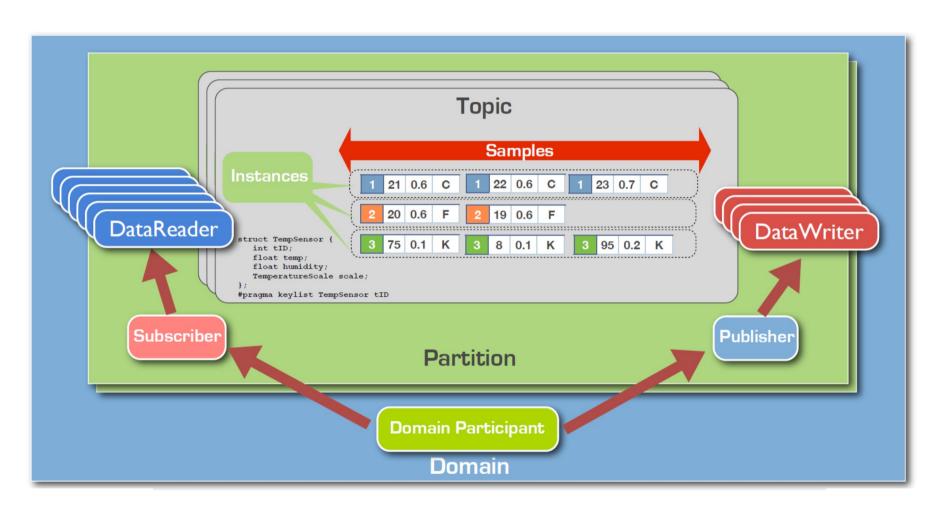




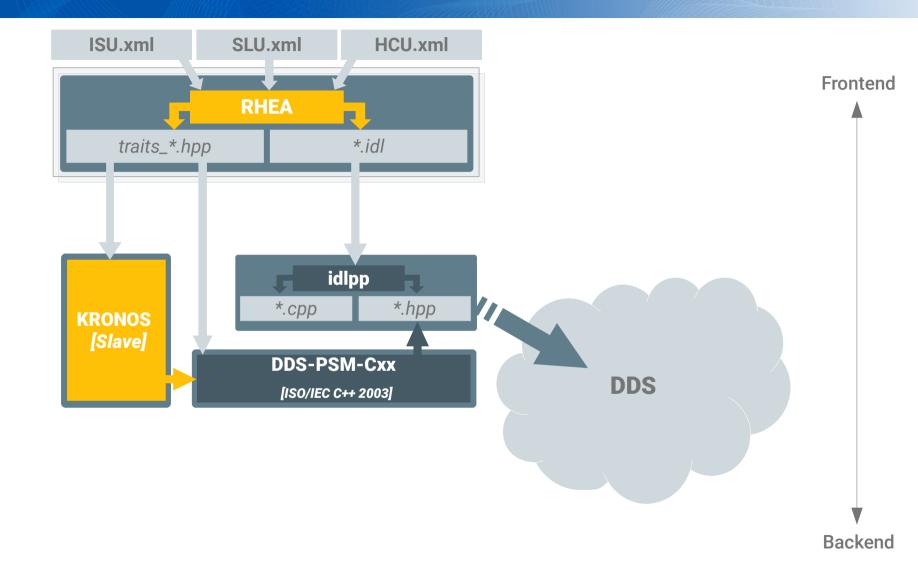




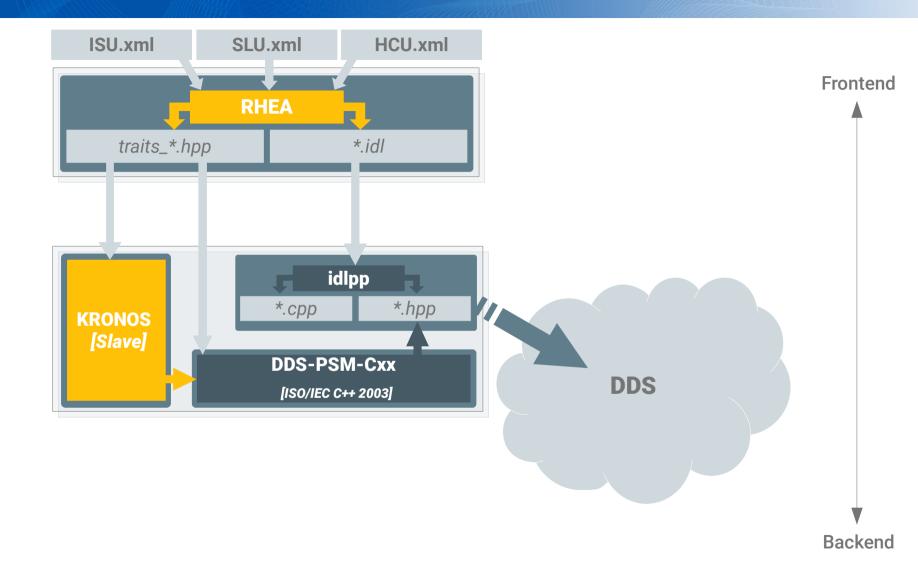




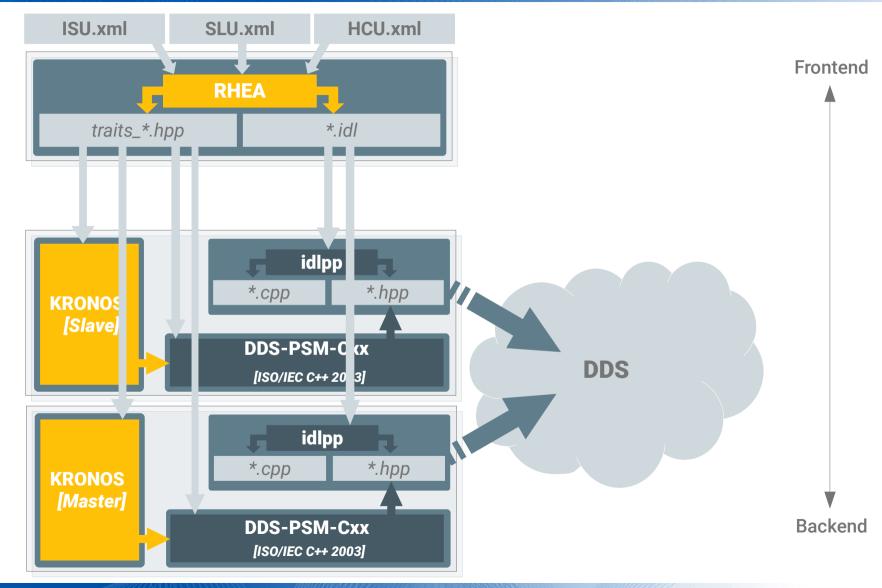














your\_awesome\_code.\*

Modelize

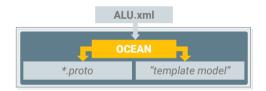


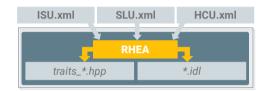
ALU.xml ISU.xml SLU.xml HCU.xml

Configure



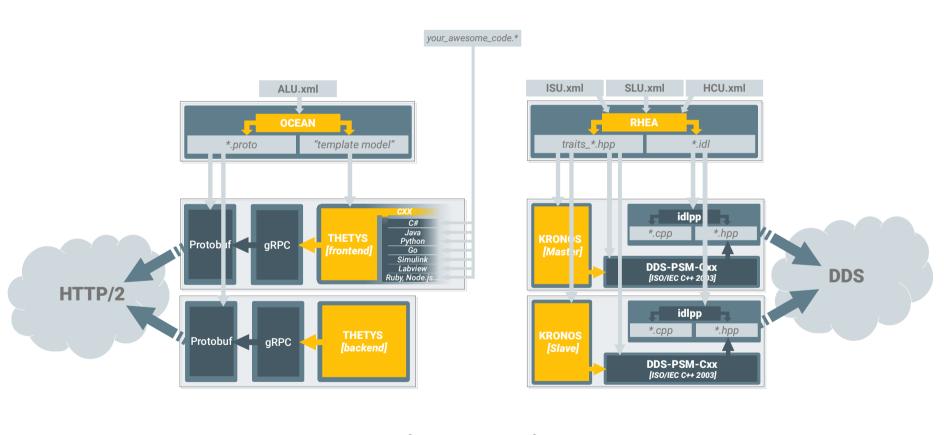
your\_awesome\_code.\*





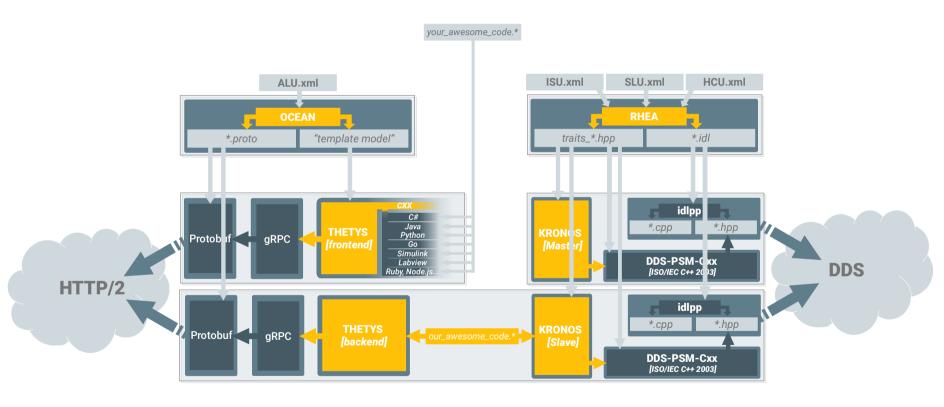
Generate





Scale





Connect





http://www.labsim.github.io

