

# SICK MLG-2 PRO LINE SHAPE BARRIER COMMISSIONING AND SETTING

3	20/01/2021	Trovato M. Cappelletti A. Bussei P.	17/3/2021	Bussei P.	Added commissioning contents and pictures, graphical revision
2	20/09/2020	Chierago L.			Added some clarifications
1	29/07/2020	Chierago L.			Added screenshots
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Rev.	Date	Issued	Verified	Approved	Description

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## 1 – CHANGELOG

Revision 0: First release from R&D.

Revision 1: Added some screenshot to the procedure.

Revision 2: Added some clarifications.

Revision 3: Contents were extended according to field feedbacks, reorder of the index and graphical revision according to ITP-AVV standard template.

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## 2 – SCOPE OF THE GUIDE

This document describes the standard commissioning procedures for SICK MLG2 PRO barriers applied on FIL spa Induction Lines (GeniFeed) Controllers.

Due an agreement with SICK these sensors will be delivered already pre-configured and ready-to-use, but it might be necessary to restore Fives' standard configuration if something goes wrong.

The first part of the guide (Commissioning Check) illustrates the procedure to check that the barrier has the right parameter settings, the second part (Standard Configuration) shows how to configure the barrier completely from the factory setting.

## 3 – TOOLS REQUIRED

For checking and programming a FIL spa customized barrier, the following tools are needed:

- Computer with USB and Ethernet port
- SOPAS software from SICK (downloadable free from the SICK official website).
- Ethernet CAT5 cable with RJ45 connector on one end and M12-D coded connector on the other end, to connect directly the PC to the barrier (such as the SICK YM2D24-020EA1MRJA4, FIL code 17A\_00384).

Note: this is the same cable to address the PROFINET devices with M12 connectors.

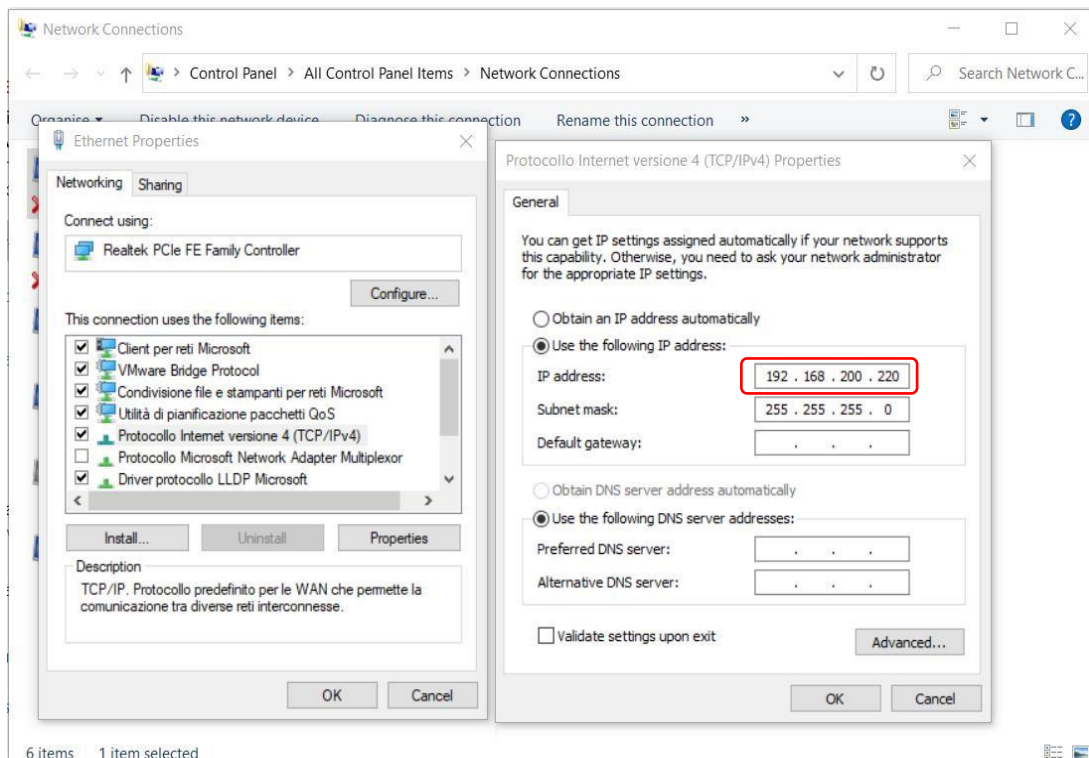
For barriers with old firmware from 2.0 to 2.2.0, "data exchange mode" parameter can only be set with the following tools:

- IO-Link USB Master called "SiLink2 Master" with relative software, IO-Link connection cable, USB cable and 230Vac power adapter, all included in the FIL article 18A\_00215 (This is the same IO-Link USB Master used by the Leuze CML)

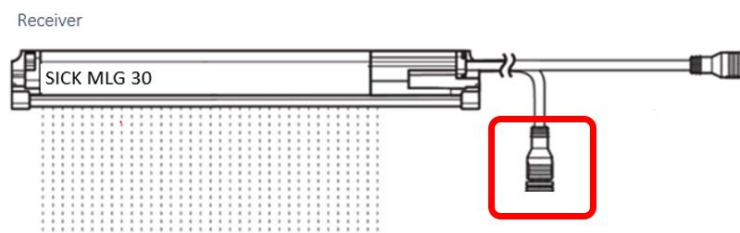
Therefore pay attention to bring those tools with you in case of "barrier factory reset" or barriers without configuration.

## 4 – COMMISSIONING CHECK

1. Set the IPv4 address of your Laptop Ethernet card to 192.168.200.220 and Subnet Mask to 255.255.255.0 (barrier default IP Address is 192.168.200.100):



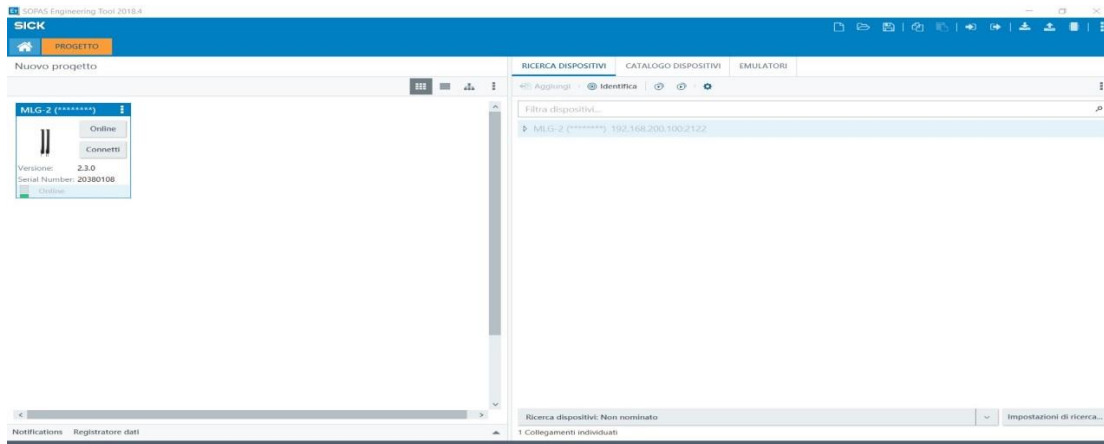
2. Turn ON the barrier and connect to the Service M12 connector located on the Receiver with M12 to RJ45 cable.



3. Run the SOPAS software and wait for the automatic detection of the barrier, a barrier with IP 192.168.200.100 will appear, if not check that the barrier is properly powered and the connections.

Note: the first time you will connect to the barrier, you will be asked for the SDD and IODD files specific for the MLG-2 barriers, you may preventively download these files from the SICK official website or SOPAS will automatically install the files ([Internet connection needed](#))

4. On the Barrier Picture, Double click on the Barriers picture or click Connect, the configuration window will appear.



5. In the induction line check list available on our corporate disk Y at Y:\\_projectCode\04\_Documenti\07\_Collaudi\Elettrici\REPORT\ , check if parameters have already been set/checked

For Example:

8FQT\_LINEA\_IN11\_CKL03\_Check List QEE Introduzioni \_rev2.xlsx [Protected View] - Excel

File Home Insert Page Layout Formulas Data Review View Tell me what you want to do...

C40 PROGRAMMAZIONE E ALLINEAMENTO BARRIERE DI SAGOMA (secondo i documenti e le istruzioni ITP)

	A	B	C	D	E	F	G	H
24			TARATURA DEI TERMICI DI PROTEZIONE PER TUTTI I MOTORI (Verificare che siano indicati a schema)		OK			
25			MOVIMENTO DEI NASTRI IN MODALITA' MANUALE (Verifica funzionamento, velocità e senso di rotazione)		OK			
26			COLLAUDO DEL BUS PROFINET CON IL PC TEST (configurazione e interfaccia con tutti i nodi)		OK			
27			PROGRAMMAZIONE E ALLINEAMENTO BARRIERE DI SAGOMA (secondo i documenti e le istruzioni ITP)		NC	OPEN		
28			ATTIVAZIONE DEI SEGNALE (Verificare corrispondenza con schema e ITP)		OK			
29			COMANDO MOTORI (Con comando da Pc Test)		OK			
30			VERIFICA DELLA DOCUMENTAZIONE (Riportare eventuali modifiche o mancanze)		NC	OPEN		
31			ARCHIVIO FOTO SIGNIFICATIVE DEI PUNTI SEGNALATI E DEI FOGLI DELLO SCHEMA CHE DEVONO ESSERE AGGIORNATI					
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NOTES & COMMENTS

27) La barriera 2 ha le ottiche 43-45 sotto spalletta per trasporto

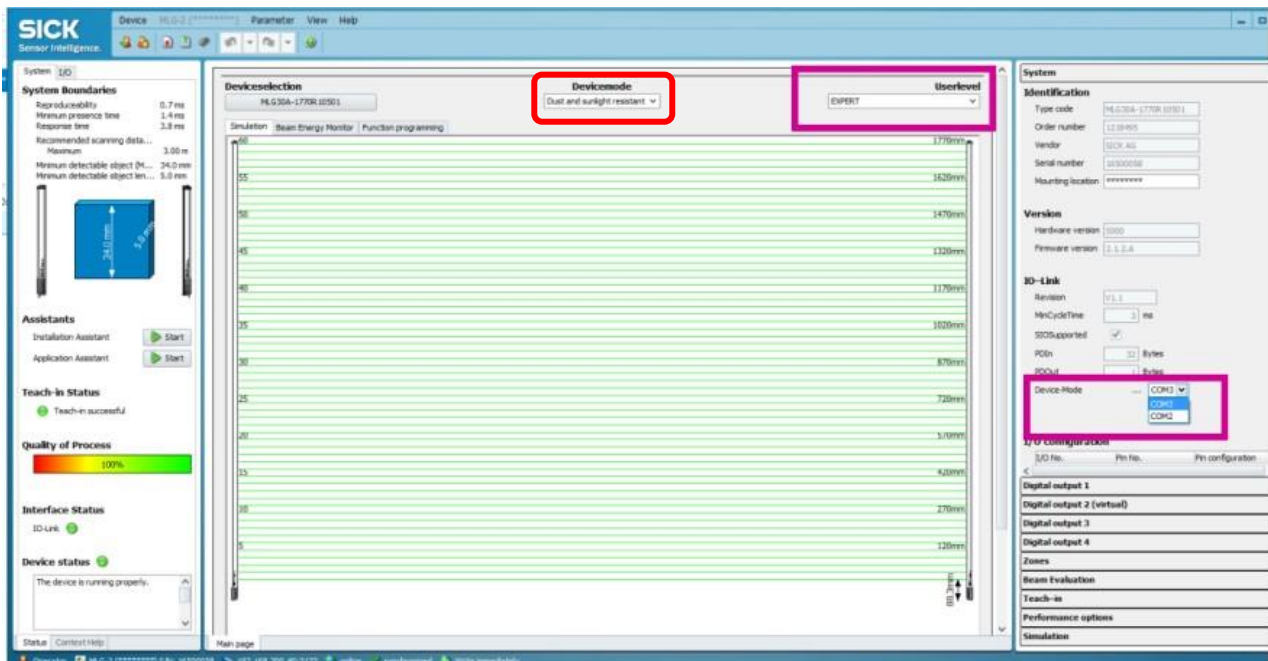
27) La barriera 3 ha le ottiche 1, 42-45 sotto spallette per trasporto

27) Le barriere 2-3 il teach dovrà essere fatto in cantiere dopo aver tolto le spallette per trasporto

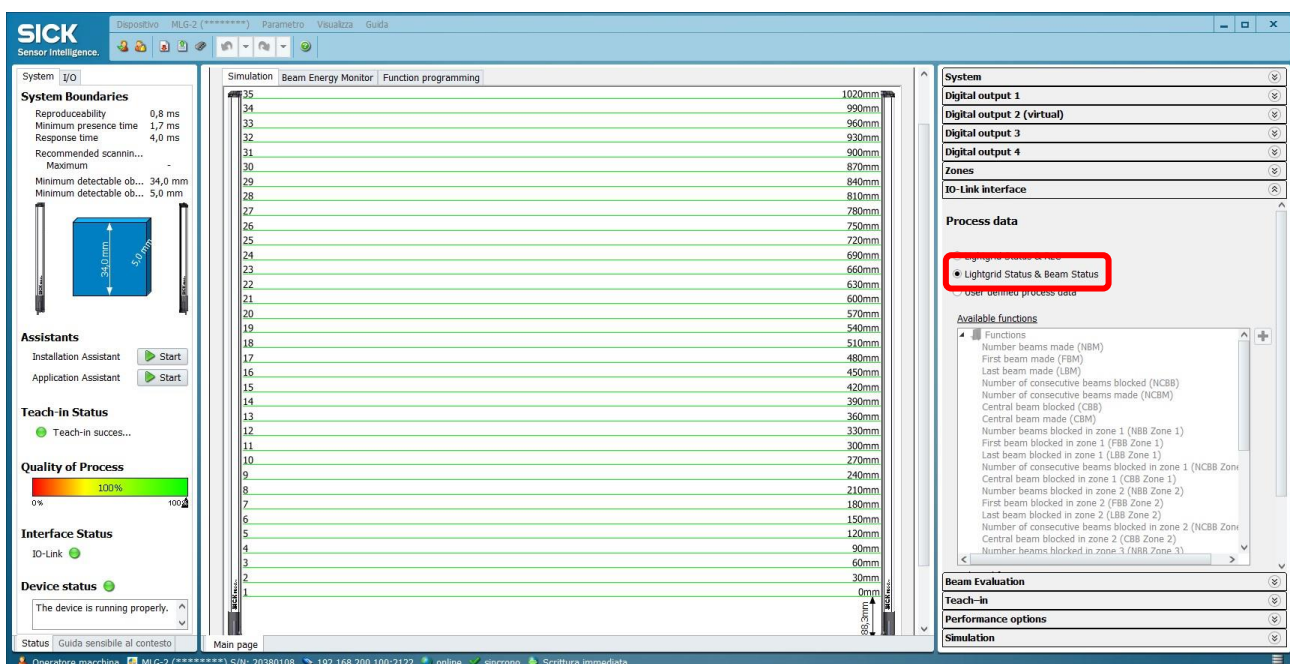
CKL03\_CQA r2

If parameters have been checked (as in the example) skip to “BARRIERS ALIGNMENT”

- Check the following settings:  
 Devicemode = Dust and sunlight resistant  
 Userlevel = Expert  
 System -> I/O Link = COM3

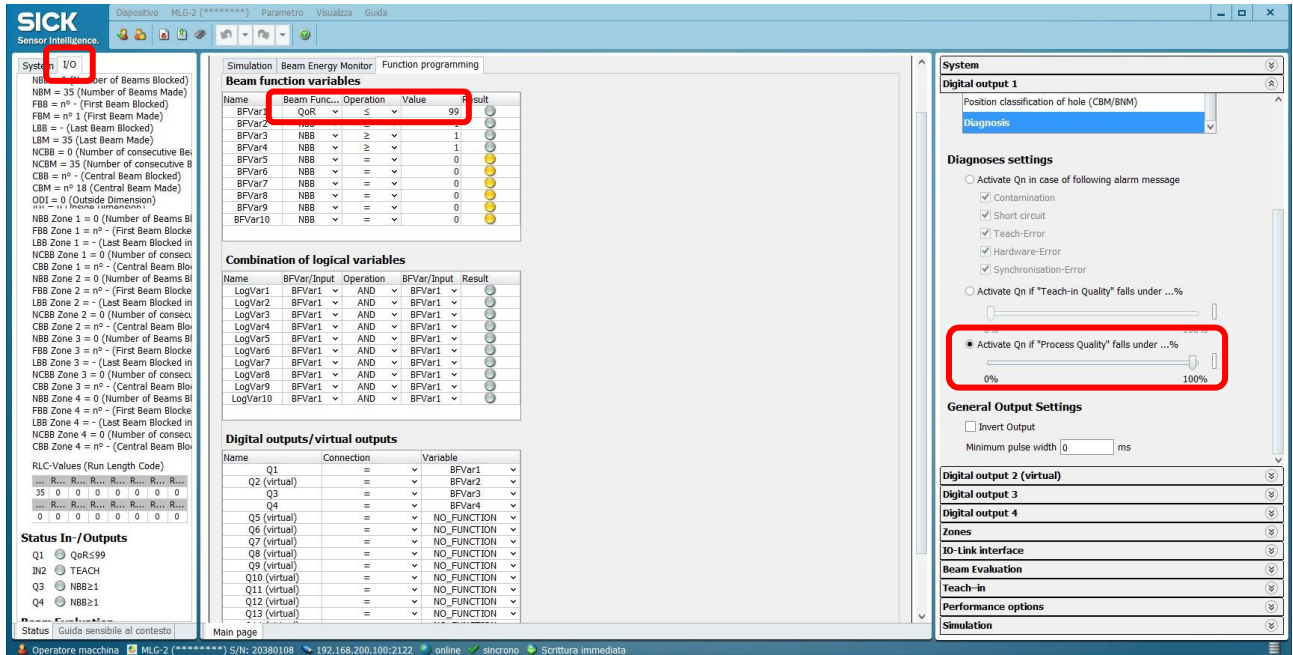


- Check the setting under I/O-Link interface->Process data = Lightgrid Status & Beam Status



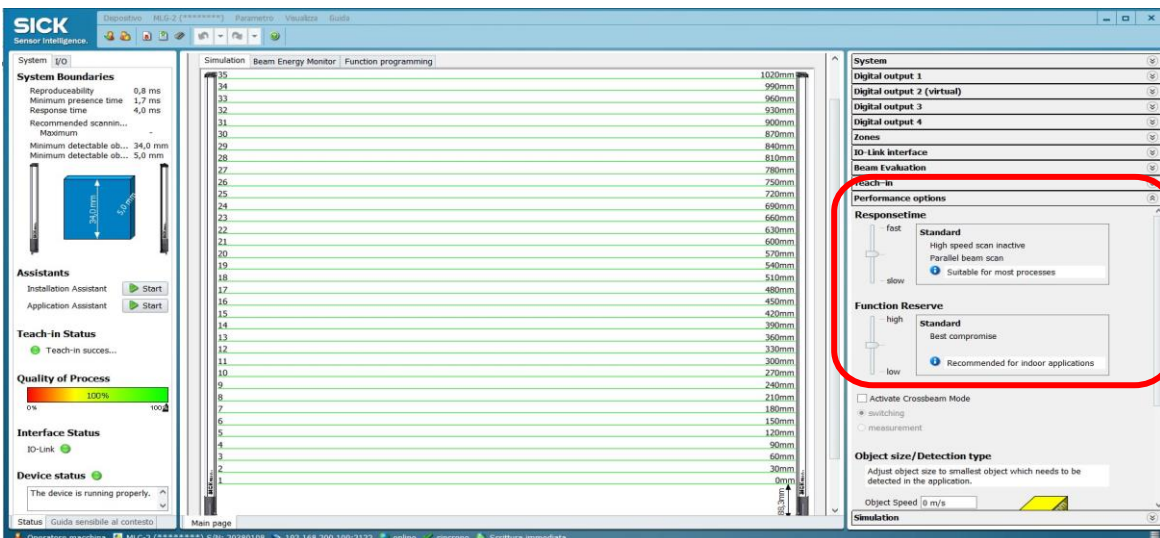


8. Check the setting under Digital output 1 -> Diagnosis settings = Activate Qn if Process Quality falls under...% 99%.



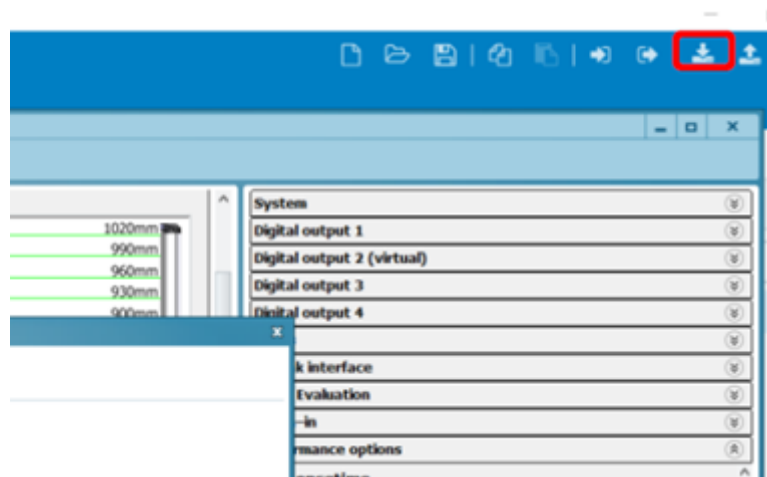
The screenshot shows the SICK MLG-2 PRO Commissioning and Settings software. The 'System' tab is active, and the 'Digital output 1' configuration is selected. The 'Diagnosis settings' section is highlighted with a red box, showing the option 'Activate Qn if Process Quality falls under ...%' selected, with a value of 99%.

9. Check the setting under Performance options -> Response time and Function Reserve = Standard.



The screenshot shows the SICK MLG-2 PRO Commissioning and Settings software. The 'System' tab is active, and the 'Teach-in' configuration is selected. The 'Performance options' section is highlighted with a red box, showing the settings 'Response time' and 'Function Reserve' both set to 'Standard'.

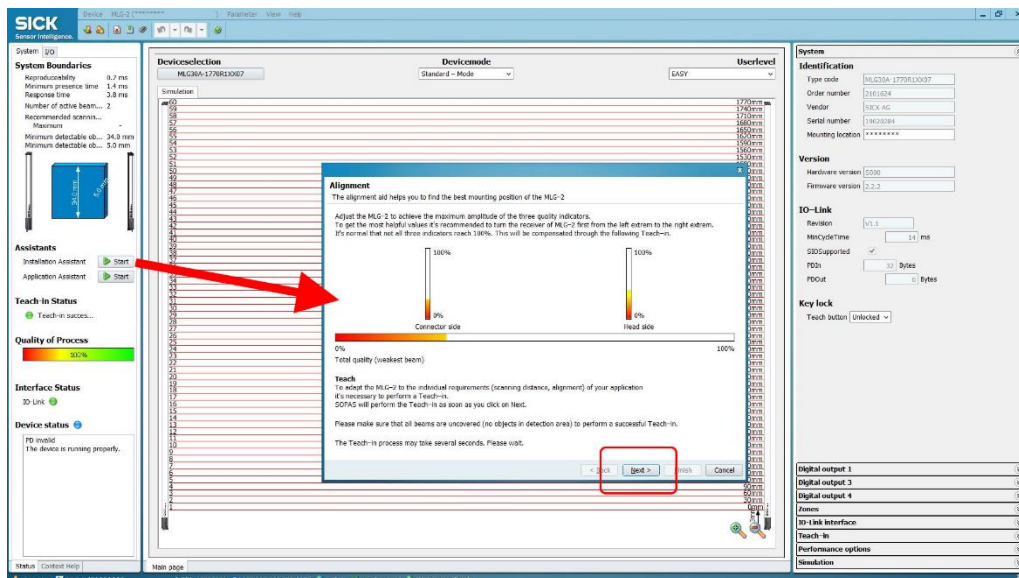
If any configuration has been made, save permanently changes by pressing the “Download to device” button in the top-left part of the screen





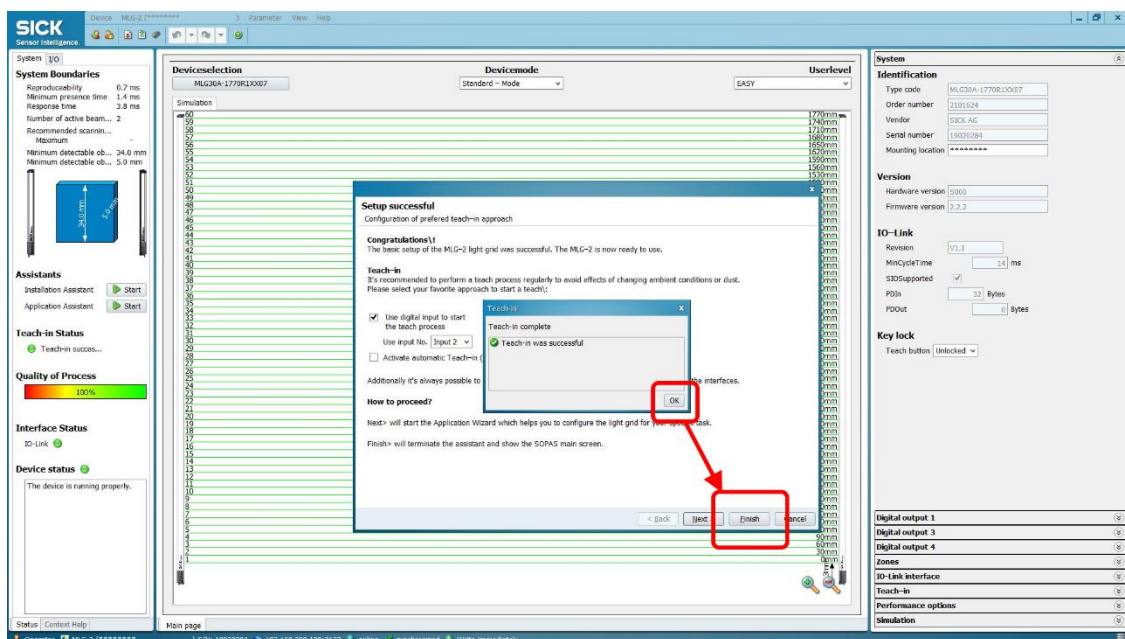
## 5 – BARRIERS ALIGNMENT

Make sure that all the beams are green (free) or skip to “set masking (point 6)” if some beams will never be free. Start the Installation Assistant on the left part of the window, press “Next” then



proceed with the alignment of the barrier

Mechanically align the barriers and make sure that the Total Quality bar is as greener as possible then click “Next” to perform the Teach.



Once the teach is successful, click on “Finish”, don’t click “Next” to avoid any changes to the Barrier settings

## 6 – MASKING

In the general case Masking (called blanking) should not be necessary, as all barrier beams should be free during operation (after all the special supports used for line transportation were removed).

Any mechanical impediment or obstruction should be removed.

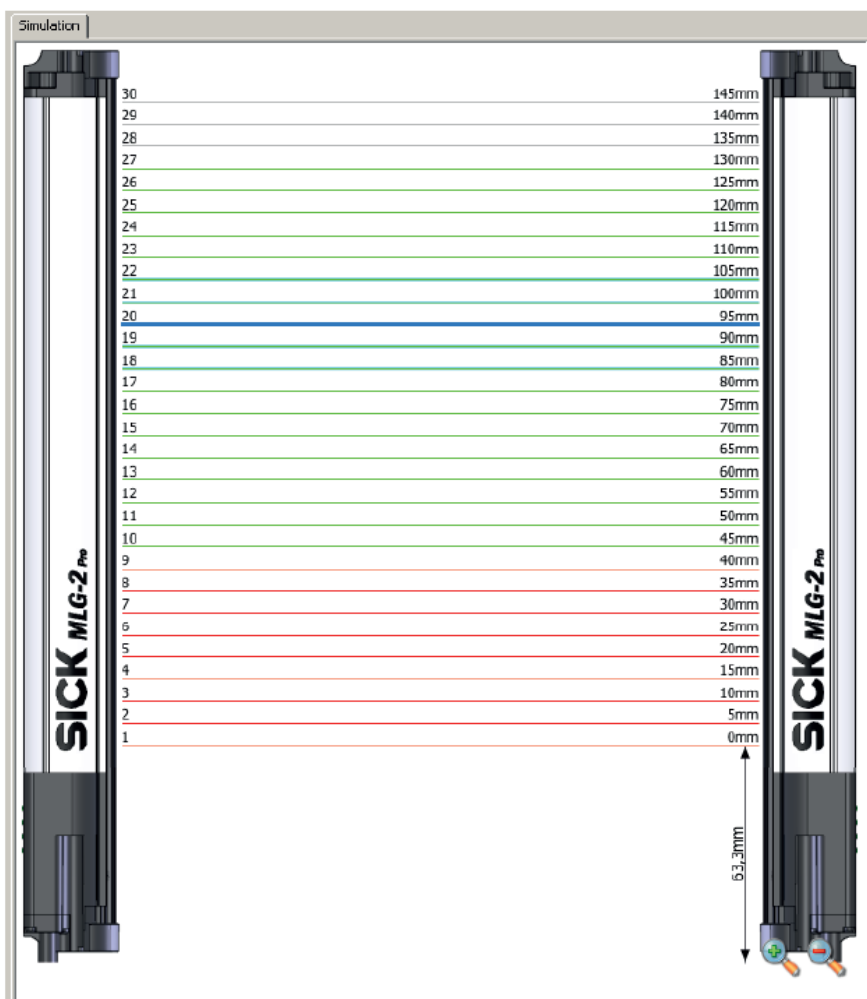
The only case when masking is needed is when the barrier is longer than the line width and, consequently, it might “see” the sideguards or other parts.

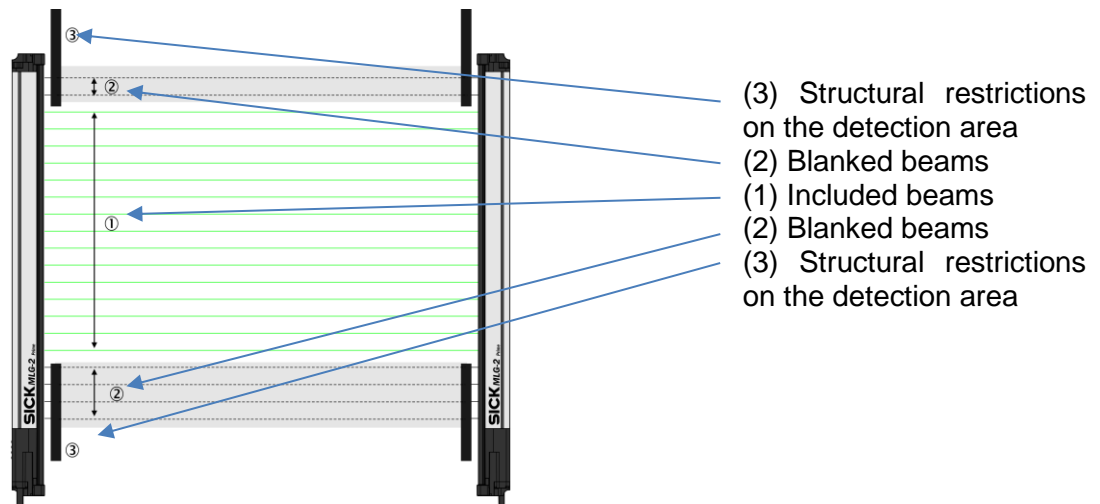
In such a case, masking is helpful to make sure that the barrier led switches only when an item pass through the sensor.

Masking, if required, must be set during barrier configuration (NO masking during database configuration).

In Sick’s manuals, masking is called “**blanking**” or “**muting**”. In the detection area, blanked beams are grey color

(Green = free beams, Red = Blocked beams, Gray = Blanked beams, Blue = Beams selected with the mouse, Turquoise = Configured tolerance)

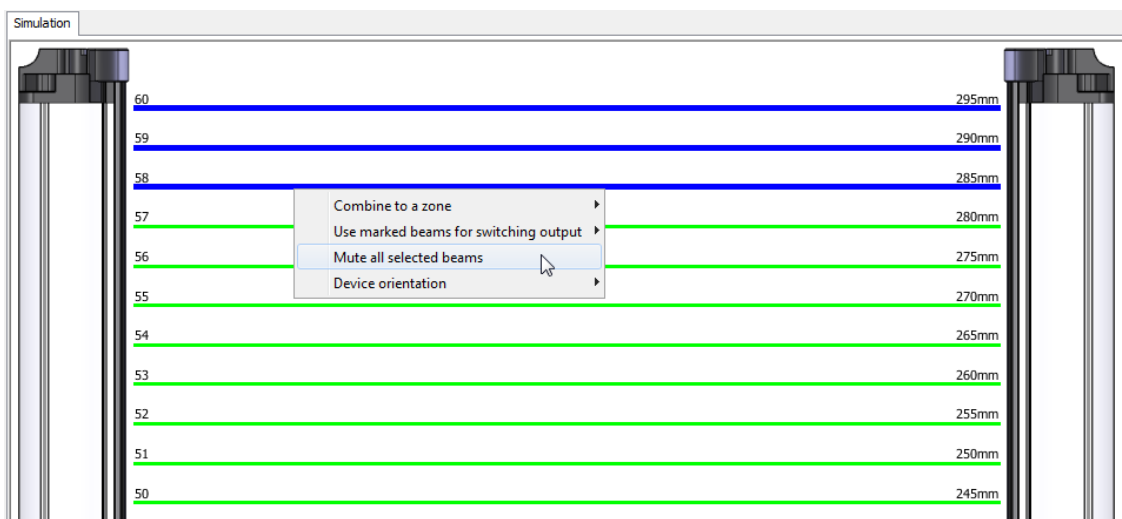




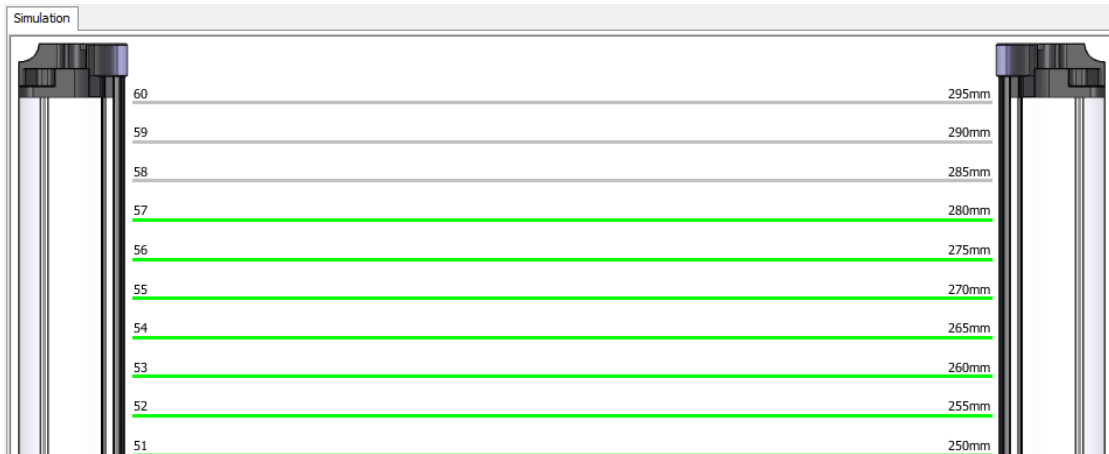
Set **only manually** blanking so that those beams will be ignored during teaching (**first blanking, later teaching**):

1. Move the mouse on the beam and right click for selecting (press the Ctrl key to select several beams)
2. In the context menu, select the command Blank all selected beams ("mute selected beams")

For example:



After muting, the beams will be grey color



## 7 – BEAM INVERSION

One important rule is that for the ISPC application, the first “logical” sensor beam (**PH1 in the trace**) **MUST** be the one on the side of the induction “**where the sorter arrives**”.

**“where the sorter leaves” side is the opposite side. Please never say right/left side nor short/long side but only side seeing the sorter arriving/leaving.**

Barriers are usually assembled with the first “physical” beam next to the connector side.

If connector side isn’t on the same side of the sorter arriving, inversion is required **during database configuration (NO beams inversion during barrier configuration)** so that PH1 will always be next to the side seeing the sorter arriving.

## 8 – BEAM MAPPING CHECK

Using the ISPC Engine application, there is a commissioning mode log that is always automatically enabled when induction line is switched in maintenance mode or you need to enable trace level 20 “shape barrier” if induction line is in automatic mode.

In the ISC log, beam status is reported by a row like:

```
[Lv:Barrier] [CBarrierClass::Main] BID:1; PH 9 ( 170) >>
          XXX          << 11 ( 850)
```

Where

BID = barrier ID: 1 = BOOK, 2=FEEDBACK, 3=ORIENTING

PH 9 = index of the first busy beam (start counting from the side seeing the sorter arriving of induction line, this index starts from 1)

(170) = distance from beam 1 of the first busy beam -> [(first busy beam – 1) + 0,5] \* pitch

\_ = free beam

X = busy beam

11 = index of the last busy beam

(850) = distance of the last busy beam from the last beam of the barrier -> [(number of beams – index of the last busy beam) + 0,5] \* pitch

### EXAMPLE 1

The commissioner will move his finger from the side seeing the sorter arriving to the side seeing the sorter leaving, beams 1 and 53 were blanked during barrier configuration:

```

10:57:01 (0502176726) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 2 ( 30) >> X << 2 (1030)
10:57:02 (0502177546) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 3 ( 50) >> X << 3 (1010)
10:57:03 (0502178286) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 4 ( 70) >> X << 4 (990)
10:57:03 (0502178986) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 5 ( 90) >> X << 5 (970)
10:57:04 (0502180086) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 6 ( 110) >> X << 6 (950)
10:57:05 (0502180696) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 7 ( 130) >> X << 7 (930)
10:57:06 (0502181676) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 8 ( 150) >> X << 8 (910)
10:57:06 (0502182246) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 9 ( 170) >> X << 9 (890)
10:57:07 (0502182806) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 10 ( 190) >> X << 10 (870)
10:57:08 (0502183646) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 11 ( 210) >> X << 11 (850)
10:57:08 (0502184166) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 12 ( 230) >> X << 12 (830)
10:57:09 (0502184616) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 13 ( 250) >> X << 13 (810)
10:57:09 (0502185086) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 14 ( 270) >> X << 14 (790)
10:57:10 (0502185406) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 15 ( 290) >> X << 15 (770)
10:57:10 (0502185776) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 16 ( 310) >> X << 16 (750)
10:57:10 (0502186006) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 17 ( 330) >> X << 17 (730)
10:57:10 (0502186226) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 17 ( 330) >> XX << 18 (710)
10:57:11 (0502186246) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 18 ( 350) >> X << 18 (710)
10:57:11 (0502186436) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 19 ( 370) >> X << 19 (690)
10:57:11 (0502186686) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 19 ( 370) >> XX << 20 (670)
10:57:11 (0502186696) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 20 ( 390) >> X << 20 (670)
10:57:11 (0502186846) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 21 ( 410) >> X << 21 (650)
10:57:11 (0502187066) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 22 ( 430) >> X << 22 (630)
10:57:12 (0502187266) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 23 ( 450) >> X << 23 (610)
10:57:12 (0502187486) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 24 ( 470) >> X << 24 (590)
10:57:12 (0502187686) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 25 ( 490) >> X << 25 (570)
10:57:12 (0502187866) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 26 ( 510) >> X << 26 (550)
10:57:12 (0502188036) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 27 ( 530) >> X << 27 (530)
10:57:12 (0502188206) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 28 ( 550) >> X << 28 (510)
10:57:13 (0502188356) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 29 ( 570) >> X << 29 (490)
10:57:13 (0502188526) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 30 ( 590) >> X << 30 (470)
10:57:13 (0502188706) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 30 ( 590) >> XX << 31 (450)
10:57:13 (0502188716) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 31 ( 610) >> X << 31 (450)
10:57:13 (0502188836) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 31 ( 610) >> XX << 32 (430)
10:57:13 (0502188856) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 32 ( 630) >> X << 32 (430)
10:57:13 (0502188966) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 32 ( 630) >> XX << 33 (410)
10:57:13 (0502188996) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 33 ( 650) >> X << 33 (410)
10:57:13 (0502189116) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 33 ( 650) >> XX << 34 (390)
10:57:13 (0502189126) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 34 ( 670) >> X << 34 (390)
10:57:13 (0502189196) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 35 ( 690) >> X << 35 (370)
10:57:14 (0502189336) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 35 ( 690) >> XX << 36 (350)
10:57:14 (0502189346) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 36 ( 710) >> X << 36 (350)
10:57:14 (0502189526) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 37 ( 730) >> X << 37 (330)
10:57:14 (0502189656) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 37 ( 730) >> XX << 38 (310)
10:57:14 (0502189666) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 38 ( 750) >> X << 38 (310)
10:57:14 (0502189826) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 38 ( 750) >> XX << 39 (290)
10:57:14 (0502189846) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 39 ( 770) >> X << 39 (290)
10:57:14 (0502190036) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 40 ( 790) >> X << 40 (270)
10:57:14 (0502190216) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 40 ( 790) >> XX << 41 (250)
10:57:15 (0502190286) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 41 ( 810) >> X << 41 (250)
10:57:15 (0502190376) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 41 ( 810) >> XX << 42 (230)
10:57:15 (0502190406) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 42 ( 830) >> X << 42 (230)
10:57:15 (0502190596) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 42 ( 830) >> XX << 43 (210)
10:57:15 (0502190626) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 43 ( 850) >> X << 43 (210)
10:57:15 (0502190766) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 43 ( 850) >> XX << 44 (190)
10:57:15 (0502190866) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 44 ( 870) >> X << 44 (190)
10:57:15 (0502190956) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 44 ( 870) >> XX << 45 (170)
10:57:15 (0502191036) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 45 ( 890) >> X << 45 (170)
10:57:15 (0502191176) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 45 ( 890) >> XX << 46 (150)
10:57:15 (0502191216) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 46 ( 910) >> X << 46 (150)
10:57:16 (0502191416) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 46 ( 910) >> XX << 47 (130)
10:57:16 (0502191456) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 47 ( 930) >> X << 47 (130)
10:57:16 (0502191586) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 47 ( 930) >> XX << 48 (110)
10:57:16 (0502191686) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 48 ( 950) >> X << 48 (110)
10:57:16 (0502191766) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 48 ( 950) >> XX << 49 (90)
10:57:16 (0502191816) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 49 ( 970) >> X << 49 (90)
10:57:16 (0502191936) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 50 ( 990) >> X << 50 (70)
10:57:16 (0502192156) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 50 ( 990) >> XX << 51 (50)
10:57:17 (0502192256) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 51 (1010) >> X << 51 (50)
10:57:17 (0502192366) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 51 (1010) >> XX << 52 (30)
10:57:17 (0502192436) [Lv:Barrier] [CBarrierClass::Main] BID:1; PH 52 (1030) >> X << 52 (30)

```

### EXAMPLE 2



[illegible]

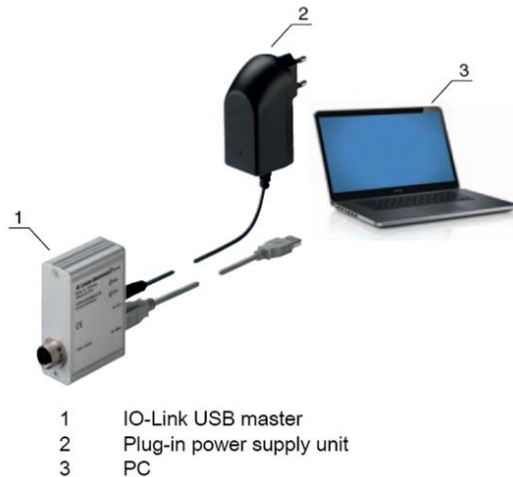
10:56:22 (0502137566)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 46 ( 910) >>	XXXX	<< 49 ( 90)
10:56:22 (0502137636)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 46 ( 910) >>	XX_X	<< 49 ( 90)
10:56:22 (0502137656)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 46 ( 910) >>	XXXXX	<< 50 ( 70)
10:56:22 (0502137666)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 46 ( 910) >>	X_XXX	<< 50 ( 70)
10:56:22 (0502137686)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 48 ( 950) >>	XXX	<< 50 ( 70)
10:56:22 (0502137696)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 47 ( 930) >>	XX_X	<< 50 ( 70)
10:56:22 (0502137706)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 47 ( 930) >>	XX_XX	<< 51 ( 50)
10:56:22 (0502137726)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 47 ( 930) >>	XXXXX	<< 51 ( 50)
10:56:22 (0502137746)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 47 ( 930) >>	X_XXX	<< 51 ( 50)
10:56:22 (0502137756)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 47 ( 930) >>	X_X_X	<< 51 ( 50)
10:56:22 (0502137776)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 47 ( 930) >>	X_XXX	<< 51 ( 50)
10:56:22 (0502137796)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 48 ( 950) >>	X_X_X	<< 52 ( 30)
10:56:22 (0502137806)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 48 ( 950) >>	X_X	<< 50 ( 70)
10:56:22 (0502137826)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 48 ( 950) >>	X_XX	<< 51 ( 50)
10:56:22 (0502137846)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 48 ( 950) >>	X_X	<< 51 ( 50)
10:56:22 (0502137856)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 48 ( 950) >>	XX_X	<< 51 ( 50)
10:56:22 (0502137866)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 49 ( 970) >>	X_X	<< 51 ( 50)
10:56:22 (0502137876)	[Lv:Barrier]	[CBarrierClass::Main]	BID:1; PH 49 ( 970) >>	X	<< 49 ( 90)



## 9 – CONNECTION WITH I/O LINK MASTER

### 8.1. IO-Link master setup

An IO-Link master (Fives code 18A\_00215) is needed to set the sensor data exchange mode only for firmware version from 2.0 to 2.2.0.



### 8.2. Installation on the PC of all required software

All software and documentation is available on the network “Application Manual folder” or on the SICK website.

- To be installed:
- SOPAS software
- Driver for the SLink2 Master
- IODD file for SICK MLG2-Pro barrier
- SDD file for SICK MLG2-Pro IO-Link COM3 barrier

### 8.3. Connection with IO-Link master

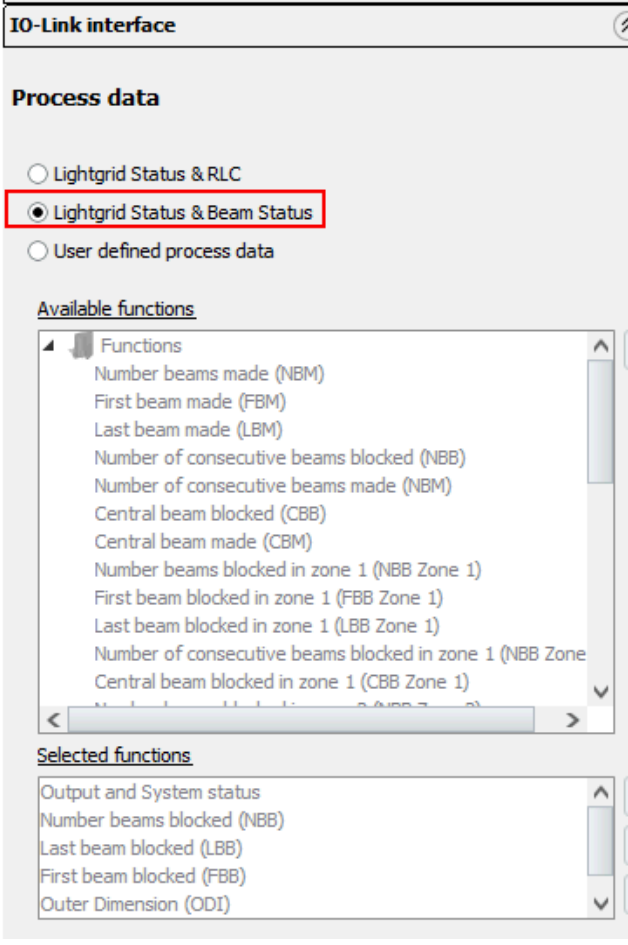
In situations where it is necessary to connect with the IO-Link master (less practical), the following steps must be completed:

- Connect the IO-Link master to a USB port.
- Connect the auxiliary power supply of the USB master
- Connect the IO-Link master with the barrier, disconnecting first the connection going to the - induction line IO-Link master.
- Open SOPAS.
- Scan for connected devices
- Updated the device driver (SDD file) if suggested by SOPAS. Follow the instructions. Exit - and enter SOPAS
- Click on the barrier to connect

## 10 – STANDARD CONFIGURATION

### Configuration of IO-Link communication

It is necessary to set up the barrier with the following parameters:

Parameter	Value	Notes
Bit rate	COM3	Can be set via IO-Link or Ethernet.
Process select data	1	<p>Meaning: output the status of each beam.</p> <p>Can be set via IO-Link <b>ONLY</b> on firmware 2.0 to 2.2.0.</p> <p>On newer firmware versions it can be set via Ethernet</p> 
Q1 (Bit 0 of Output-Status)	QoR <= 99%	<p>Meaning: switching to 1 when at least one beam is "dirty".</p> <p>Can be set via IO-Link or Ethernet.</p>

The following diagram shows how the process data should be configured:

b) Condition: ISDU: Process Data Select, Index 120; Value 1

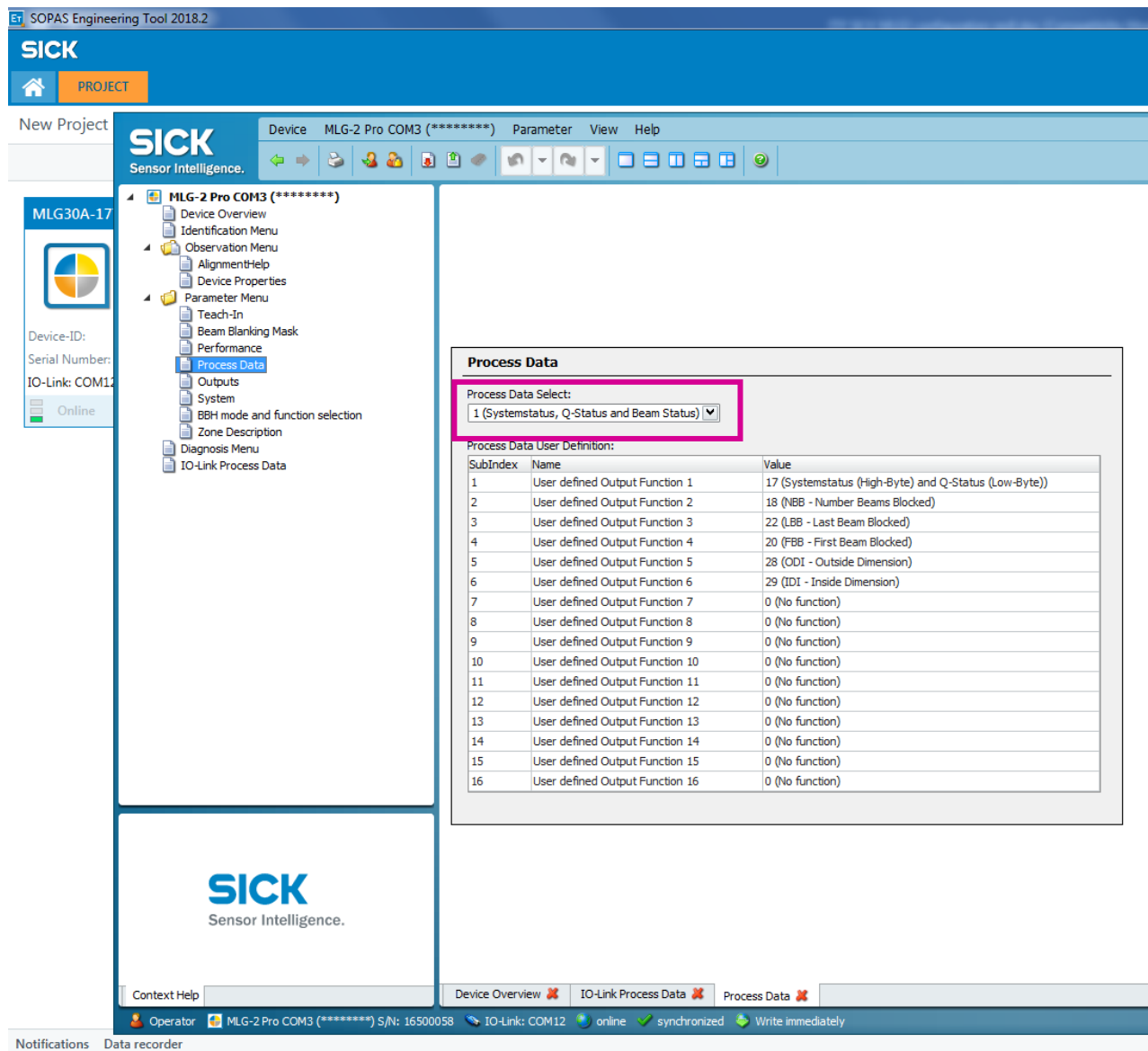
System- & Q-Status + Beam Status																			
Byte-Offset	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13
	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam	beam
	225...240	209...224	193...208	177...192	161...176	145...160	129...144	113...128	97...112	81...96	65...80	49...64	33...48	17...32	1...16	System Status*)	Output Status*)		

The following diagram shows the content of bytes 1 and 0:

Bit-Offset	*) System Status								*) Output-Status							
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
	Sync Error	Teach Fail	Hardware Error	Contamination Alarm	Teach Active	Over Temperature	Process Data Invalid	Q-Short Circuit	not used	not used	not used	not used	Q4	Q3	Q2	Q1
	0 = false 1 = true								0 = OFF 1 = ON							

## Change / Check the process data mode

Connecting via IO-Link master, change the following:



The screenshot shows the SICK MLG-2 Pro COM3 software interface. The left sidebar contains a tree view with the following items: Device Overview, Identification Menu, Observation Menu, AlignmentHelp, Device Properties, Parameter Menu, Teach-In, Beam Blanking Mask, Performance, **Process Data** (highlighted), Outputs, System, BBH mode and function selection, Zone Description, Diagnosis Menu, and IO-Link Process Data. The main window displays the 'Process Data' configuration screen. The 'Process Data Select' dropdown is set to '1 (Systemstatus, Q-Status and Beam Status)'. Below this, the 'Process Data User Definition' table is shown.

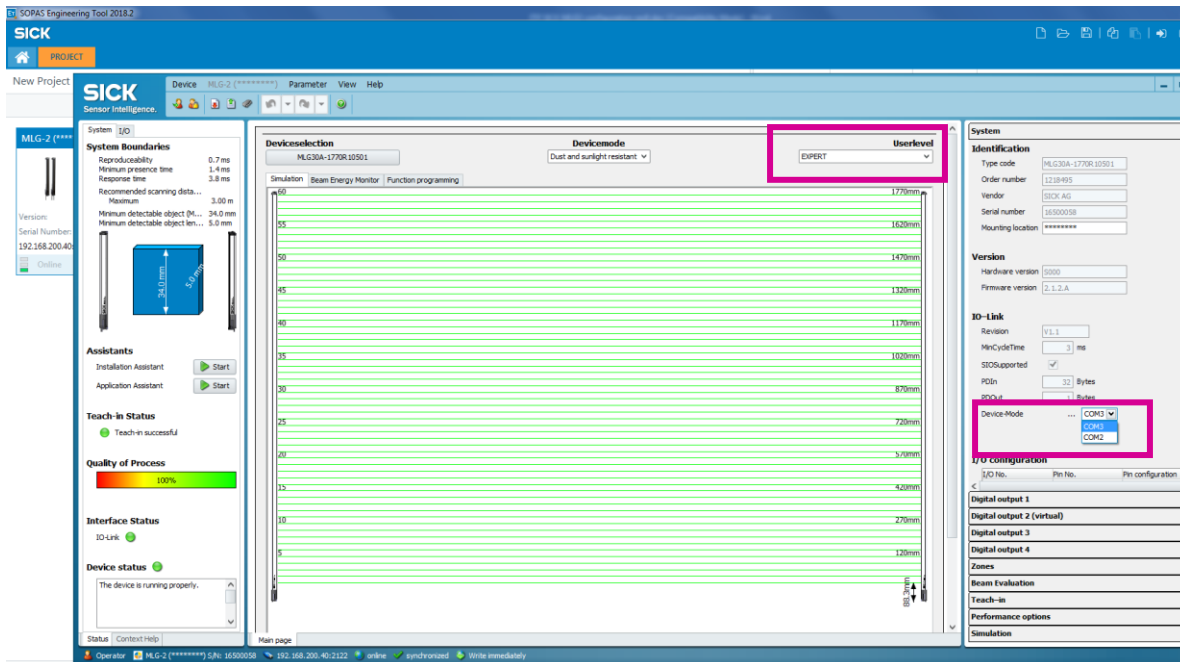
SubIndex	Name	Value
1	User defined Output Function 1	17 (Systemstatus (High-Byte) and Q-Status (Low-Byte))
2	User defined Output Function 2	18 (NBB - Number Beams Blocked)
3	User defined Output Function 3	22 (LBB - Last Beam Blocked)
4	User defined Output Function 4	20 (FBB - First Beam Blocked)
5	User defined Output Function 5	28 (ODI - Outside Dimension)
6	User defined Output Function 6	29 (IDI - Inside Dimension)
7	User defined Output Function 7	0 (No function)
8	User defined Output Function 8	0 (No function)
9	User defined Output Function 9	0 (No function)
10	User defined Output Function 10	0 (No function)
11	User defined Output Function 11	0 (No function)
12	User defined Output Function 12	0 (No function)
13	User defined Output Function 13	0 (No function)
14	User defined Output Function 14	0 (No function)
15	User defined Output Function 15	0 (No function)
16	User defined Output Function 16	0 (No function)

The default should already be 1.

## Change / Check the IO-Link communication speed

Connecting via Ethernet,

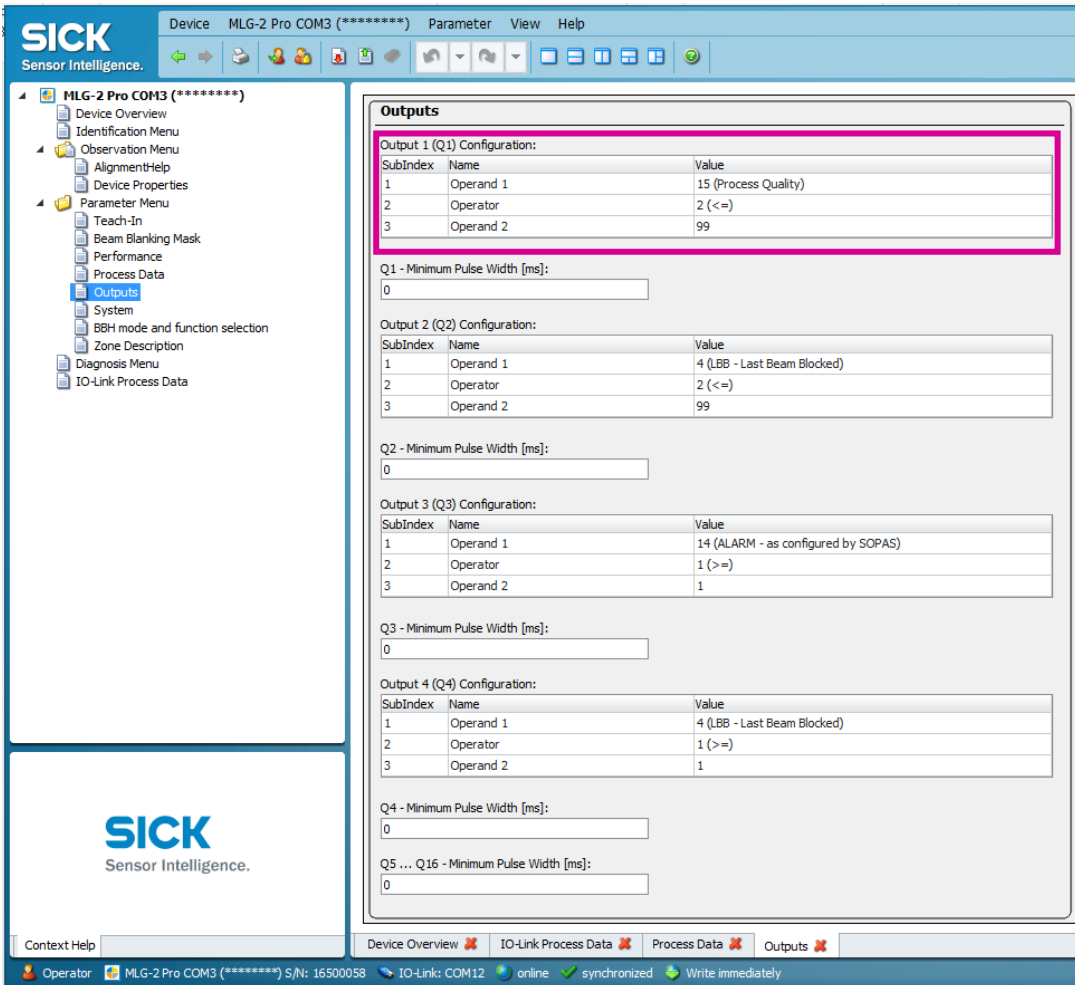
- Select the Userlevel "Expert"
- Check the Device mode control in the IO-Link section:



The default should already be COM3

## Check the Q1 output configuration

Connecting via IO-Link master, change the following:



The screenshot shows the SICK MLG-2 Pro COM3 software interface. The left sidebar contains a tree view with the following items: Device Overview, Identification Menu, Observation Menu, AlignmentHelp, Device Properties, Parameter Menu, Teach-In, Beam Blanking Mask, Performance, Process Data, **Outputs** (highlighted), System, BBH mode and function selection, Zone Description, Diagnosis Menu, and IO-Link Process Data. The main area displays the 'Outputs' configuration page. The 'Output 1 (Q1) Configuration' table is highlighted with a red box. Below it are input fields for 'Q1 - Minimum Pulse Width [ms]' and 'Q2 - Minimum Pulse Width [ms]'. Further down are configurations for Output 2 (Q2), Output 3 (Q3), and Output 4 (Q4), each with a table of SubIndex, Name, and Value, and a 'Minimum Pulse Width [ms]' input field. The status bar at the bottom shows 'Operator', 'MLG-2 Pro COM3 (\*\*\*\*\*) S/N: 16500058', 'IO-Link: COM12', 'online', 'synchronized', and 'Write immediately'.

SubIndex	Name	Value
1	Operand 1	15 (Process Quality)
2	Operator	2 (<=)
3	Operand 2	99

Q1 - Minimum Pulse Width [ms]:  
0

Q2 - Minimum Pulse Width [ms]:  
0

Output 2 (Q2) Configuration:

SubIndex	Name	Value
1	Operand 1	4 (LBB - Last Beam Blocked)
2	Operator	2 (<=)
3	Operand 2	99

Q2 - Minimum Pulse Width [ms]:  
0

Output 3 (Q3) Configuration:

SubIndex	Name	Value
1	Operand 1	14 (ALARM - as configured by SOPAS)
2	Operator	1 (>=)
3	Operand 2	1

Q3 - Minimum Pulse Width [ms]:  
0

Output 4 (Q4) Configuration:

SubIndex	Name	Value
1	Operand 1	4 (LBB - Last Beam Blocked)
2	Operator	1 (>=)
3	Operand 2	1

Q4 - Minimum Pulse Width [ms]:  
0

Q5 ... Q16 - Minimum Pulse Width [ms]:  
0

The default should already be 15, 2, 99.

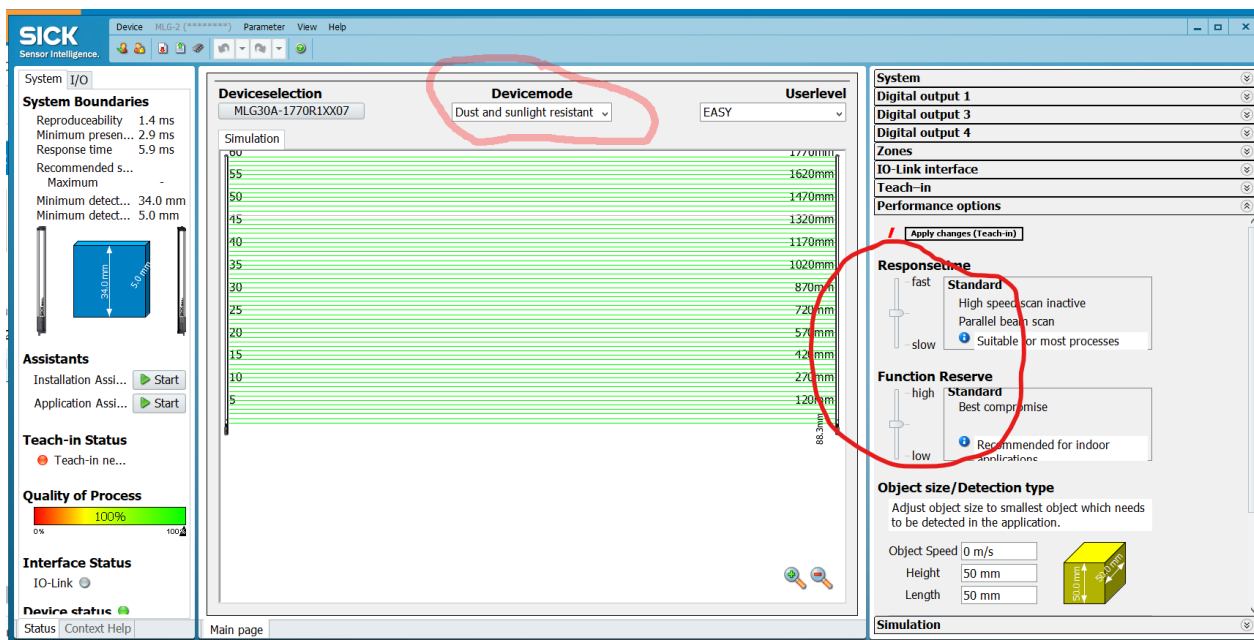
## Performance configuration

The default setup is the following:

- Devicemode = dust and sunlight resistant
- Responsetime = medium: parallel beam disabled, high speed scan disabled
- Function reserve = medium

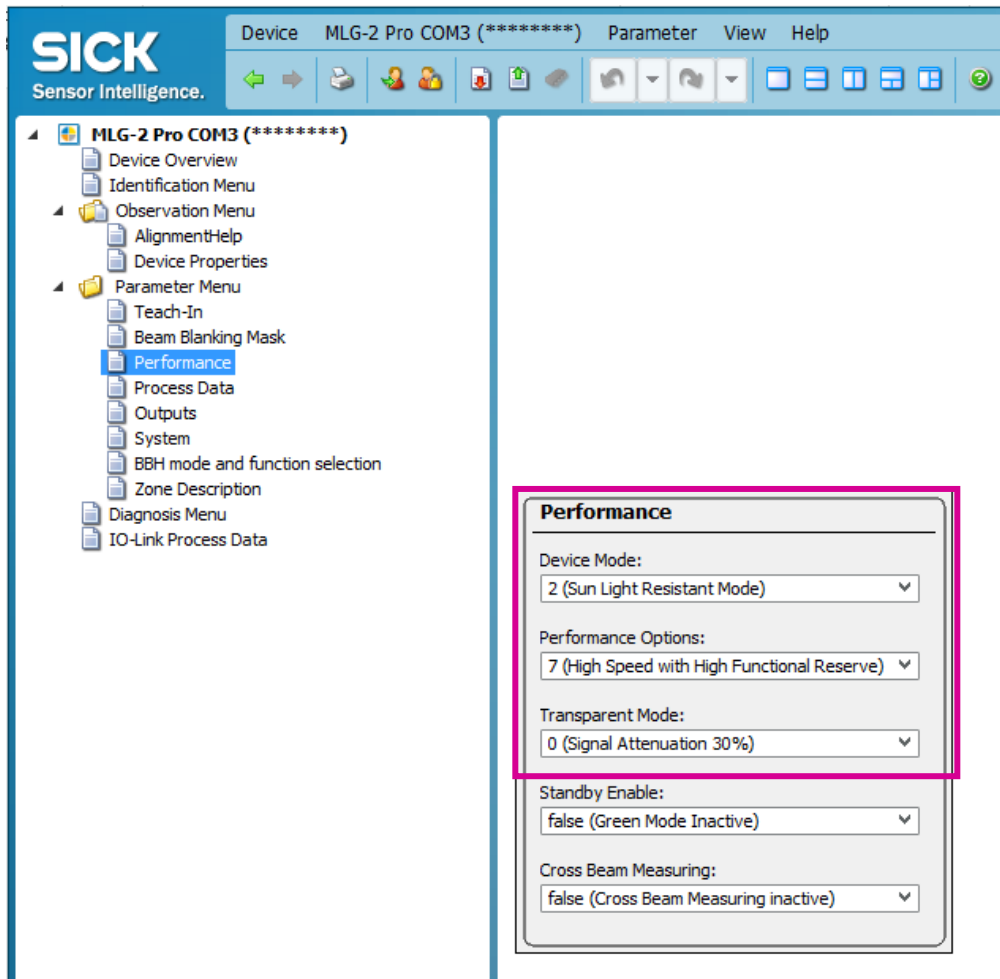
It is advised to change them only in case of transparent objects or reading issues.

On the latest SOPAS software, you should find this performance setting:





On older versions:



### Download the configuration in the barriers

Remaind to download the configuration in the barrier after any modifications to make them active.

To double-check, power off and on the barrier and upload the configuration in SOPAS.