

# wclsSimChannelSink

```
[21:08:03.455] I [ timer ] Timer: WireCell::SigProc::OmnibusSigProc : 6.48 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::DepoTransform : 1.22 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Root::MagnifySink : 1.09 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::Digitizer : 0.51 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::IncoherentAddNoise : 0.33 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Root::MagnifySink : 0.28 sec
[21:08:03.455] I [ timer ] Timer: wcls::SimChannelSink : 0.18000002 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::DepoBagger : 0.109999999 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::Drifter : 0.0999999994 sec
[21:08:03.455] I [ timer ] Timer: wcls::FrameSaver : 0.08 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::Reframer : 0.07 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::FrameFanin : 0.07 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::Retagger : 0.07 sec
[21:08:03.455] I [ timer ] Timer: wcls::SimDepoSource : 0.07 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::DumpFrames : 0.05 sec
[21:08:03.455] I [ timer ] Timer: WireCell::Gen::DepoSetFanout : 0.03 sec
[21:08:03.455] I [ timer ] Timer: Total node execution : 10.740000093355775 sec
SimChannelSink Total Charge at tick 0: 0
wclsFrameSaver: no traces tagged "gauss"
FrameSaver: q=0 n=0 tag=gauss
wclsFrameSaver: no traces tagged "wiener"
FrameSaver: q=0 n=0 tag=wiener
08-Mar-2023 21:08:03 EST Opened output file with pattern "sp.root"
08-Mar-2023 21:08:03 EST Closed output file "sp.root"
08-Mar-2023 21:08:03 EST Closed input file "g4_mu_100cm.root"

TrigReport ----- Event summary -----
TrigReport Events total = 1 passed = 1 failed = 0

TrigReport ----- Modules in End-path -----
TrigReport          Run      Success      Error Name
TrigReport          1          1          0 out

TimeReport ----- Time summary [sec] -----
TimeReport CPU = 12.001762 Real = 12.048145

MemReport ----- Memory summary [base-10 MB] -----
MemReport VmPeak = 2302.93 VmHWM = 1035.25

Art has completed and will exit with status 0.

real    0m19.142s
user    0m17.386s
sys     0m1.331s
```

# wclsDepoSetSimChannels

```
[21:11:14.096] I [ timer ] Timer: WireCell::SigProc::OmnibusSigProc : 6.5 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Gen::DepoTransform : 1.22 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Root::MagnifySink : 1.03 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Gen::Digitizer : 0.5 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Gen::IncoherentAddNoise : 0.27 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Root::MagnifySink : 0.26 sec
[21:11:14.097] I [ timer ] Timer: wcls::DepoSetSimChannelSink : 0.12 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Gen::DepoSetDrifter : 0.05 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Gen::Reframer : 0.02 sec
[21:11:14.097] I [ timer ] Timer: wcls::FrameSaver : 0 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Gen::DumpFrames : 0 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Gen::Retagger : 0 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Gen::FrameFanin : 0 sec
[21:11:14.097] I [ timer ] Timer: WireCell::Gen::DepoSetFanout : 0 sec
[21:11:14.097] I [ timer ] Timer: wcls::SimDepoSetSource : 0 sec
[21:11:14.097] I [ timer ] Timer: Total node execution : 9.969999998807907 sec
DepoSetSimChannelSink Total Charge at tick 0: 0
wclsFrameSaver: no traces tagged "gauss"
FrameSaver: q=0 n=0 tag=gauss
wclsFrameSaver: no traces tagged "wiener"
FrameSaver: q=0 n=0 tag=wiener
08-Mar-2023 21:11:14 EST Opened output file with pattern "sp.root"
08-Mar-2023 21:11:14 EST Closed output file "sp.root"
08-Mar-2023 21:11:14 EST Closed input file "g4_mu_100cm.root"

TrigReport ----- Event summary -----
TrigReport Events total = 1 passed = 1 failed = 0

TrigReport ----- Modules in End-path -----
TrigReport          Run      Success      Error Name
TrigReport          1          1          0 out

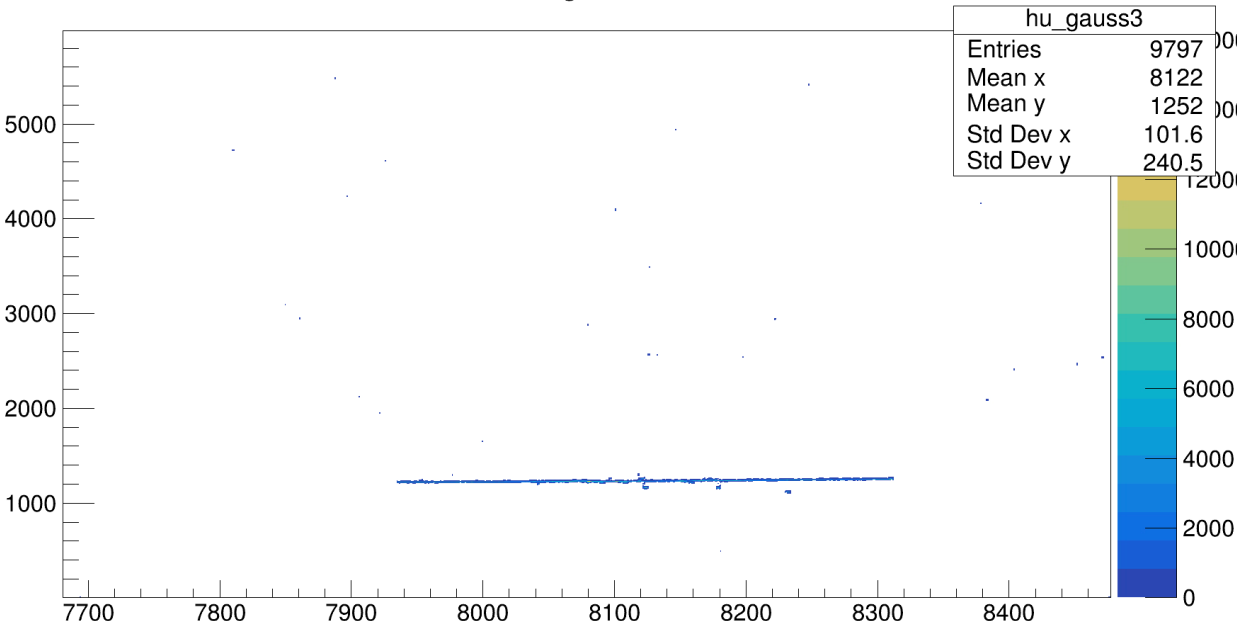
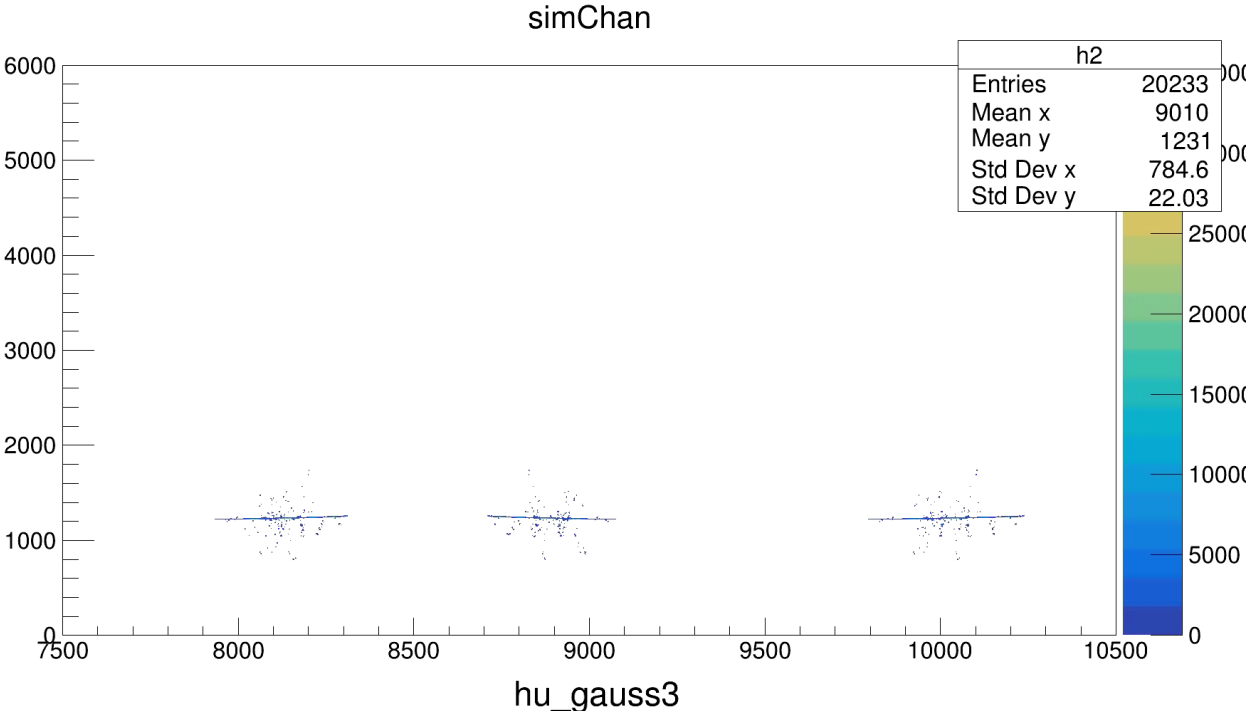
TimeReport ----- Time summary [sec] -----
TimeReport CPU = 10.123672 Real = 10.177411

MemReport ----- Memory summary [base-10 MB] -----
MemReport VmPeak = 2271.49 VmHWM = 1012.31

Art has completed and will exit with status 0.

real    0m17.545s
user    0m16.098s
sys     0m0.698s
```

# wclsSimChannelSink



# wclsDepoSetSimChannelSink

simChan

