Contents

List of Abbreviations

ABV Assertion-Based Verification

AHB Advanced High-Performance Bus

AMBA Advanced Microcontroller Bus Architecture

APR Automated Placement and Routing

ATPG Auto Test Pattern Generation

ASIC Application-Specific Integrated Circuit

AXI Advanced eXtensible Interface

BFM Bus Functional Model

CDC Clock Domain Crossing

CDV Coverage-Driven Verification

CLB Configurable Logic Block

CMOS Complementary Metal-Oxide-Semiconductor

CRV Constrained-Random Verification

DFT Design for Test

DRC Design Rule Check

DUT Design-Under-Test

DMA Direct Memory Access

ECO Engineering Change Order

EDA Electronic Design Automation

FPGA Field-Programmable Gate Array

GDS Graphic Data System

HDL Hardware Description Language

HCL Hardware Construction Language

HLS High Level Synthesis

IC Integrated Circuit

IDM Integrated Device Manufacturer

IO Input and Output

IP Intellectual Property

LVS Layout Versus Schematic

LUT Look-Up Table

LSB/MSB Least-/Most-Significant Bit

MPW Multi Project Wafer, or Shuttle

MOF Maximum Operational Frequency

MOSFET Metal-Oxide-Semiconductor Field-Effect Transistors

NRE Non-Recurring Engineering

OOP Object-Oriented Programming

Pre/Post-Sim Pre-/Post-Layout Simulation

Post-STA Post-Layout Static Timing Analysis

PCB Printed Circuit Board

RAM Random Access Memory

ROM Read-Only Memory

RTL Register-Transfer Level

SDF Standard Delay Format

SPF Standard Parasitic Format

STA Static Timing Analysis

UVM Universal Verification Methodology

VMM Verification Methodology Manual

WNS Worst Negative Slack