
Algorithm 1 edge GPU SSSP

Input: $G(V, E)$, source vertex s ;**Output:** $dist(v)$, ($v \in V$), the weight of the shortest path from s to v ;

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1:
2: function initial( $s, V$ )
3:   for each  $v \in V$  do
4:      $dist(v) \leftarrow +\infty$ ; ▷ initialize dist to positive infinity;
5:   end for
6:    $dist(s) \leftarrow 0$ ; ▷ set the source distance to 0;
7: end function
8:
9: function edgeCudaFunc( $G(V, E), dist$ ) ▷  $G(V, E)$ , the initially distance array dist;
10:    $u0 \leftarrow threadId$ ; ▷ get the thread id;
11:    $offset \leftarrow blockDim$ ; ▷ get the number of threads in a block;
12:    $flag \leftarrow (\_shared\_memory) 1$ ; ▷ whether the dist has changed;
13:    $old \leftarrow -1$ ;
14:   while true do
15:     if  $flag = 0$  then
16:       break;
17:     end if
18:      $flag \leftarrow 0$ ;
19:     for each  $(u, v, w) \in |E|$  do
20:        $old \leftarrow atomicMin(\&dist(v), dist(u) + w)$ ; ▷ use the atomic opt to exclusive mutually;
21:       if  $old > dist(v)$  then
22:          $flag \leftarrow 1$ ;
23:       end if
24:        $old \leftarrow atomicMin(\&dist(u), dist(v) + w)$ ; ▷ use the atomic opt to exclusive mutually;
25:       if  $old > dist(u)$  then
26:          $flag \leftarrow 1$ ;
27:       end if
28:     end for
29:
30:      $\_syncthreads()$ ; ▷ synchronize all threads in the same block;
31:
32:     if  $flag == 0$  then
33:       break;
34:     end if
35:   end while
36: end function
37:
38: initial( $s, V$ );
39:
40: host_to_device(dist), host_to_device( $G(V, E)$ ); ▷ copy the dist and
    $G(V, E)$  from main memory to GPU memory;
41:
42: edgeCudaFunc(); ▷ call the CUDA kernal;
43: device_to_host(dist); ▷ copy the dist back;
44:
45: return result
```
