TRACEABILITY REPORT

Railway Control System

Group 1

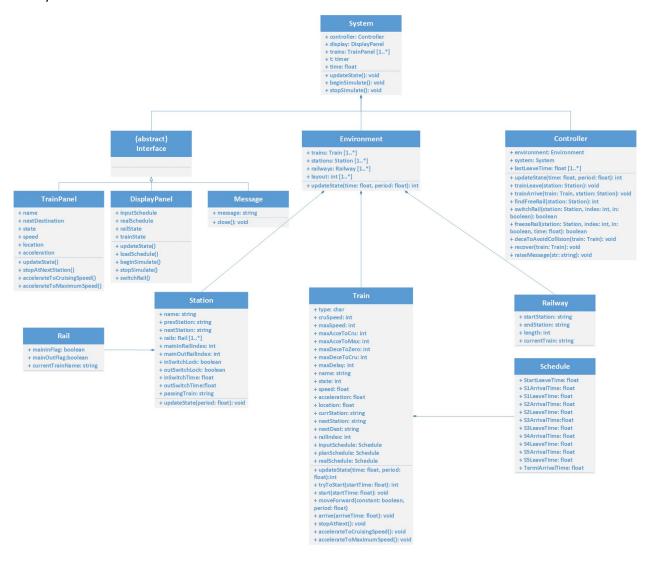
Author: Tiansu Chen, Chenyang Zhang

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System Architecture

The system architecture is shown below:



Software Traceability

	Is implemented by	Is validated by
R1 (Display Panel UI)	S1	T2.2, T2.7
R1.1	S1.1, S1.5	T2.2
R1.1.1	S1.1.1, S1.5.2	T2.2.1
R1.1.2	S1.1.1, S1.5.3	T2.2.1
R1.1.3	S1.1.1, S1.5.2	T2.2.1
R1.2	S1.4, S1.5	T2.7
R1.2.1	S1.4.1	T2.7.1
R1.2.2	S1.4.1, S1.5.3	T2.7.1
R2 (Train Panel UI)	S2	T2.2, T2.4, T2.5, T2.6
R2.1	S2.2	T2.2
R2.1.1	S2.2.1	T2.2.1
R2.1.2	S2.2.1	T2.2.1

R2.2	S2.1	T2.4, T2.5, T2.6
R2.2.1	S2.1.1	T2.4.1
R2.2.2	S2.1.2	T2.5.1
R2.2.3	S2.1.3	T2.6.1
R3 (Controller)	S3, S4	T2.2
R3.1	S3.1	T2.8
R3.1.1	S3.1.1, S3.1.2	T2.8.1
R3.1.2	S3.1.1, S3.1.2	T2.8.1
R3.1.3	S3.1.1, S3.1.2	T2.8.1
R3.1.4	S3.1.1, S3.1.2	T2.8.1
R3.2	S3.1. S4.1	T2.4, T2.5, T2.6, T2.7
R3.2.1	S3.1.2, S4.1.1	T2.4.1, T2.5.1, T2.6.1, T2.7.1