

Timing diagram showing a clock signal and two data signals (00400000 and 00000000) over a 200 ns period. The signals are represented by green waveforms on a black background. The clock signal is a regular square wave. The data signals are constant at 00400000 and 00000000 respectively.

Timing diagram for the 74163 4-bit binary counter. The diagram shows a 10ns clock signal and several data signals over a 200ns period. A yellow vertical line marks 239.985 ns. The signals include:

- A green clock signal.
- A green signal with values 00400000 and 00000000.
- A blue signal with a long binary string: 001000000000010000000000000101,001000000000100100000000000000111,101011111010100000000000000000...
- A green signal with values 000000000000000000000000,0000... and 000000000000000000000000,0000...
- A green signal with values 0, 5, 8, 7, 10, 0, 17, 15, 12.
- A green signal with values 0, 7, 15, 12.





