



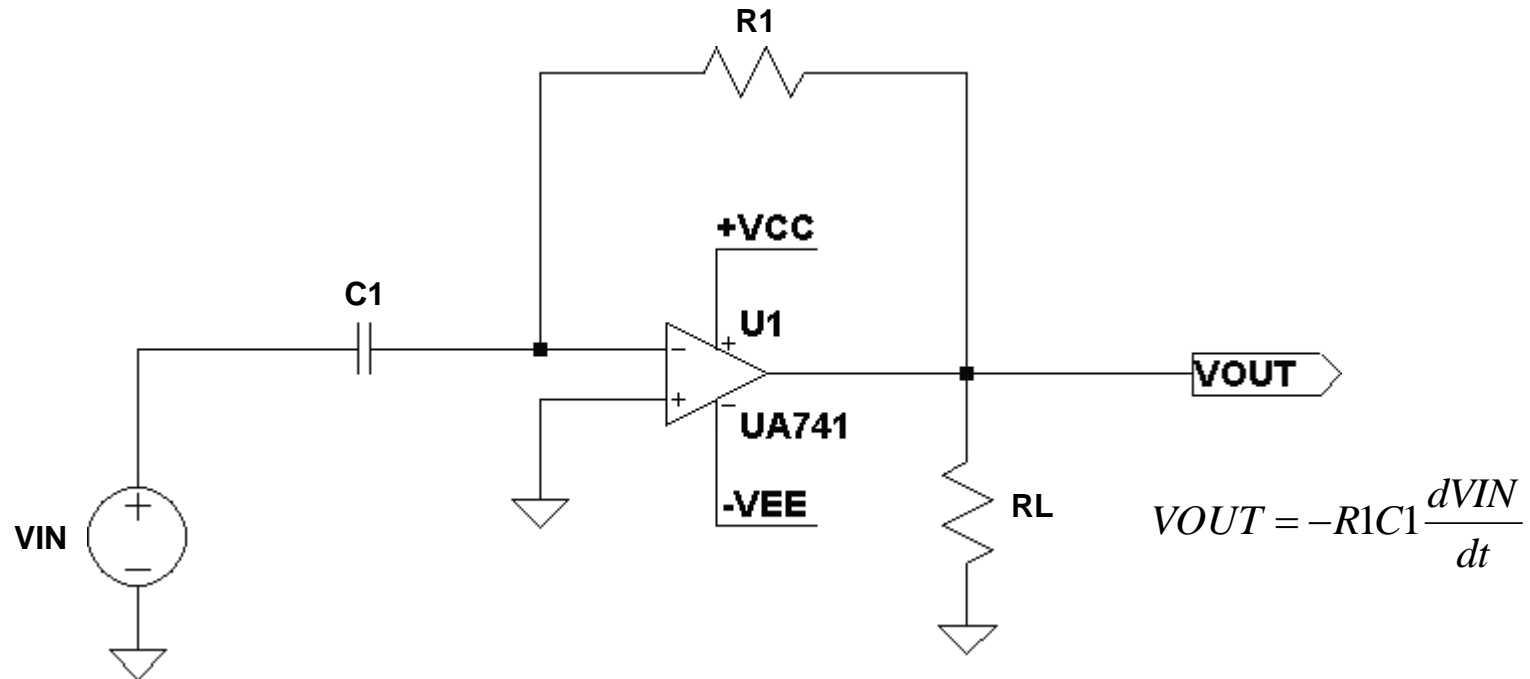
Electronic Circuits Design

Lecture – 5

- *Differentiator*
- *Integrator*

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Differentiator



- Unstable
- Very susceptible to high frequency noise



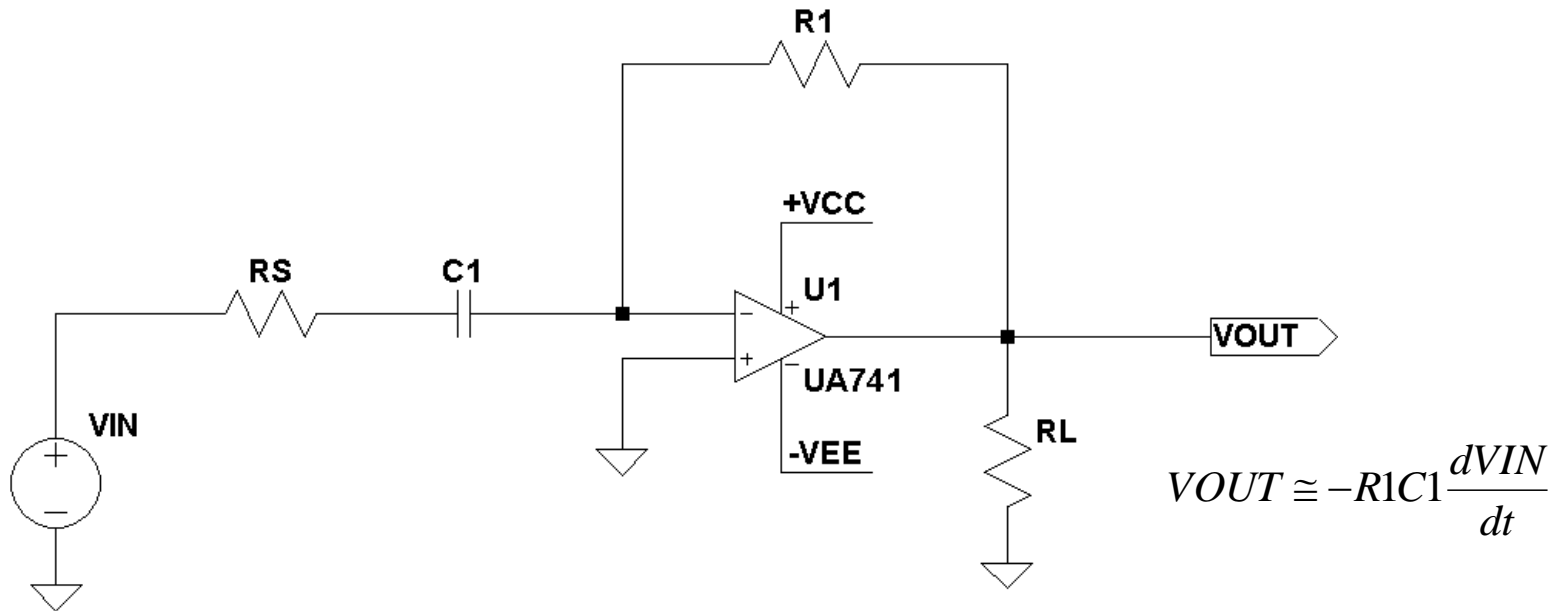
Lab-1: Differentiator

❖ Simulation Condition

- Op Amp: $\mu A741$
- $R1 = 40\text{ k}\Omega$, $C1 = 10\text{ nF}$, $R_L = 5\text{ k}\Omega$
- $+VCC = 15\text{ V}$, $-VEE = -15\text{ V}$
- Transient analysis 0 to 10ms

- 1) When $V_{IN} = 2\sin(2\pi \times 10^3 t)\text{ V}$, obtain a plot of V_{IN} and V_{OUT} versus time. V_{OUT} should be $-5\cos(2\pi \times 10^3 t)\text{ V}$ for all the time. If not, make comments on your result.
- 2) Change $R1$ to $10\text{ k}\Omega$. When V_{IN} is a symmetrical sawtooth voltage of 4-V peak-to-peak, 0 average, and 2ms-period, which means $V_{IN} = \text{PWL}(0\text{ms } -2\text{V } 1\text{ms } 2\text{V } 2\text{ms } -2\text{V } 3\text{ms } 2\text{V } \dots 9\text{ms } 2\text{V } 10\text{ms } -2\text{V})$, obtain a plot of V_{IN} and V_{OUT} versus time. V_{OUT} should be a symmetrical square wave of 0.8-V peak-to-peak, 0 average, and 2ms-period. If not, make comments on your result.

Practical Differentiator





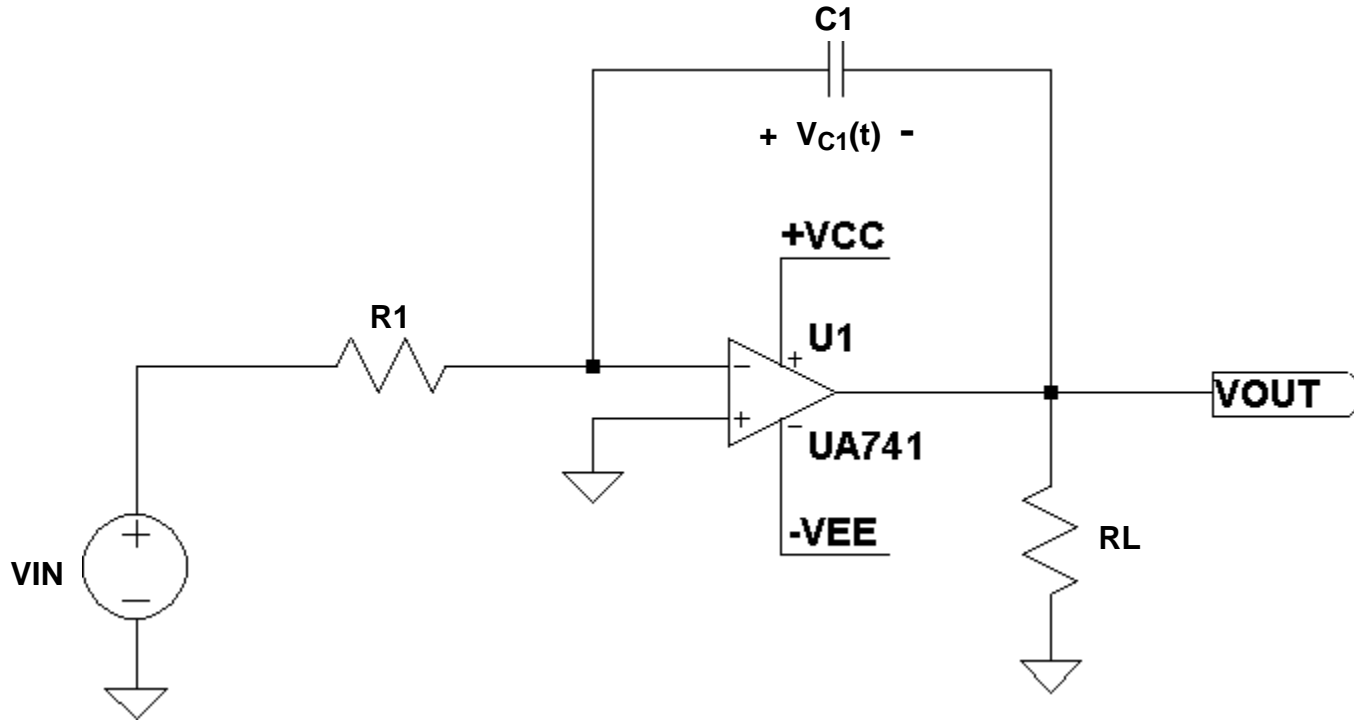
Lab-2: Practical Differentiator

❖ Simulation Condition

- Op Amp: $\mu A741$
- $R_S = 100\ \Omega$, $R_L = 5\ k\Omega$
- $+V_{CC} = 15\ V$, $-V_{EE} = -15\ V$
- Transient analysis 0 to 10ms

- 1) When $V_{IN} = 2\sin(2\pi \times 10^3 t)\ V$, design the differentiator to provide $V_{OUT} = -6\cos(2\pi \times 10^3 t)\ V$. Make comments on your design if you need.
- 2) When V_{IN} is a symmetrical sawtooth voltage of 4-V peak-to-peak, 0 average, and 2ms-period, which means $V_{IN} = \text{PWL}(0\text{ms } -2\text{V } 1\text{ms } 2\text{V } 2\text{ms } -2\text{V } 3\text{ms } 2\text{V } \dots 9\text{ms } 2\text{V } 10\text{ms } -2\text{V})$, design the differentiator to provide V_{OUT} of a symmetrical square wave of 10-V peak-to-peak, 0 average, and 2ms-period. Make comments on your design if you need.

Integrator



$$V_{OUT} = -\frac{1}{R1C1} \int_0^t V_{IN}(t) dt - V_{C1}(t=0)$$

- Any tiny DC component makes the output to saturate
- At $t = 0$, input offset voltage and part of input current charging $C1$ produce the error voltage at the output



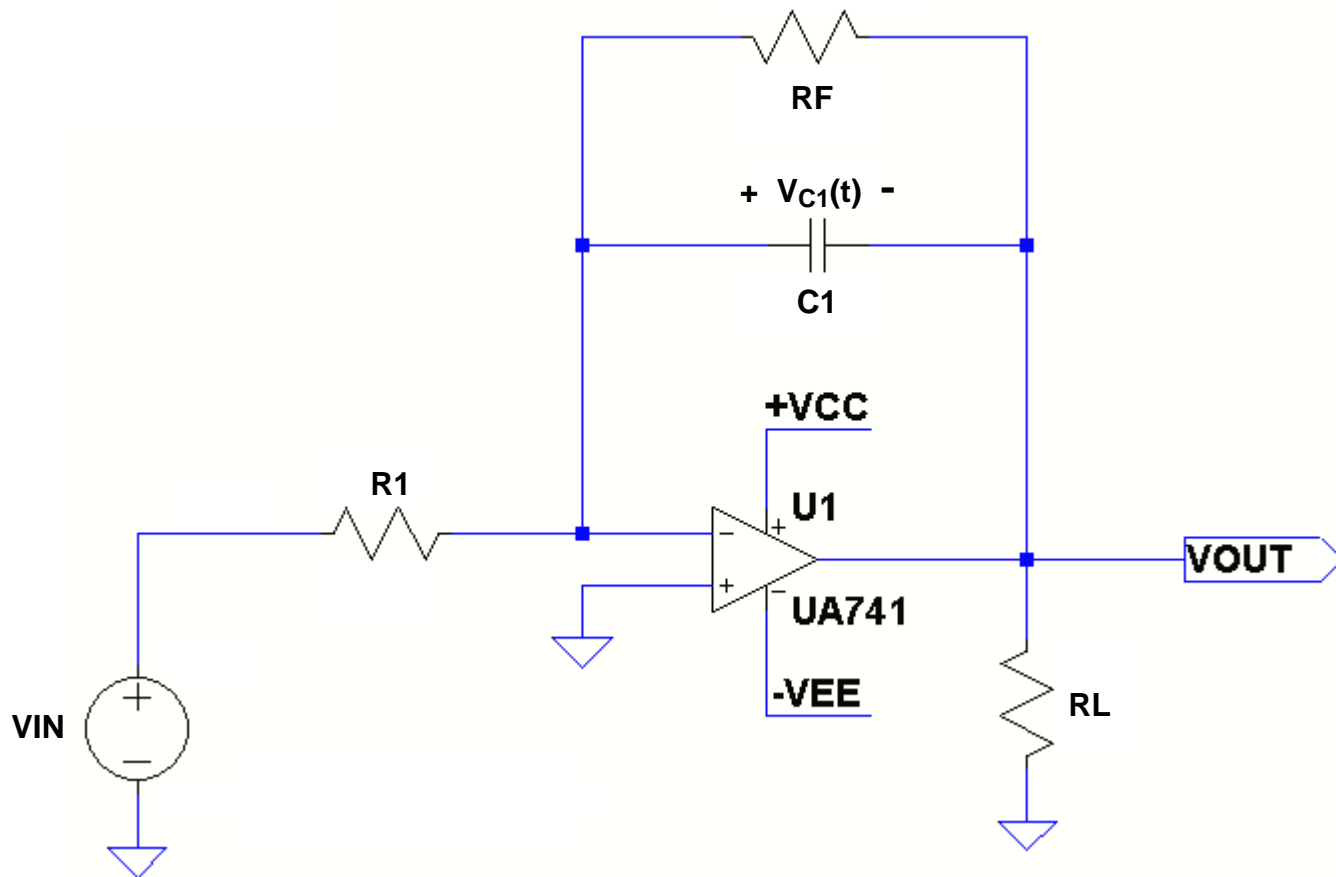
Lab-3: Integrator

❖ Simulation Condition

- Op Amp: $\mu A741$
- $R1 = 4\text{ k}\Omega$, $C1 = 10\text{ nF}$, $RL = 5\text{ k}\Omega$
- $+VCC = 15\text{ V}$, $-VEE = -15\text{ V}$
- Transient analysis 0 to 10ms

1) When $V_{IN} = \sin(2\pi \times 10^3 t)\text{ V}$, obtain a plot of V_{IN} and V_{OUT} versus time. V_{OUT} should be $4\cos(2\pi \times 10^3 t)\text{ V}$. If not, make comments on your result.

Practical Integrator



$$V_{OUT} \cong -\frac{1}{R_1 C_1} \int_0^t V_{IN}(t) dt - V_{C1}(t = 0)$$



Lab-4: Practical Integrator

❖ Simulation Condition

- Op Amp: $\mu A741$
- $R_F = 200\text{ k}\Omega$, $R_L = 5\text{ k}\Omega$
- $+V_{CC} = 15\text{ V}$, $-V_{EE} = -15\text{ V}$
- Transient analysis from 0 to 20ms

- 1) When $V_{IN} = 1\sin(2\pi \times 10^3 t)\text{ V}$, design the integrator to provide $V_{OUT} = 4\cos(2\pi \times 10^3 t)$ with a negligible phase difference after $t = 10\text{ms}$. Make comments on your design.
- 2) When V_{IN} is a symmetrical square voltage of 2-V peak-to-peak, 0 average, and 1ms-period, which means $V_{IN} = \text{PULSE}(-1\text{V } 1\text{V } 0.5\text{ms } 1\text{ns } 1\text{ns } 0.5\text{ms } 1\text{ms})$, design the integrator to provide V_{OUT} of a symmetrical sawtooth wave of 12-V peak-to-peak, 0 average, and 1ms-period after $t = 10\text{ms}$. Make comments on your design.
- 3) When $V_{IN} = \text{PULSE}(1\text{V } -1\text{V } 1\text{ms } 1\text{ns } 1\text{ns } 1\text{ms } 2\text{ms})$, design the integrator to provide V_{OUT} of a symmetrical sawtooth wave of 8-V peak-to-peak, 0 average, and 2ms-period after $t = 12\text{ms}$. Make comments on your design.