

Printed Electronics: Technologies, Challenges and Applications

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International Workshop on Flexible and Printed Electronics (IWPFPE 10)
Sept. 8-10, Muju Resort, Korea

parc
A Xerox Company

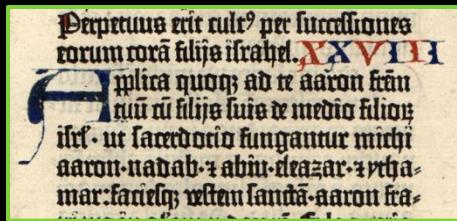
Outline

- Motivations for Printed Electronics
- Printing Technologies
- Printing Materials
- Challenges
- Applications

Motivation

Printed Electronics

print

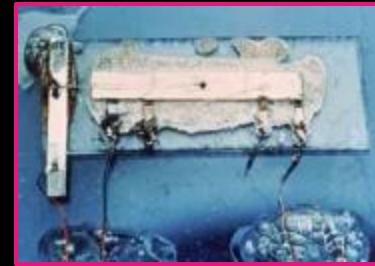


First printed book: Gutenberg

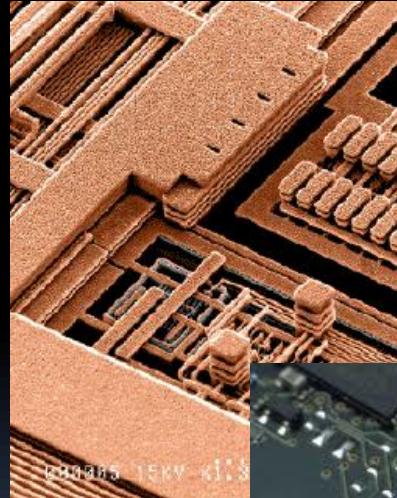
“uniting two worlds”



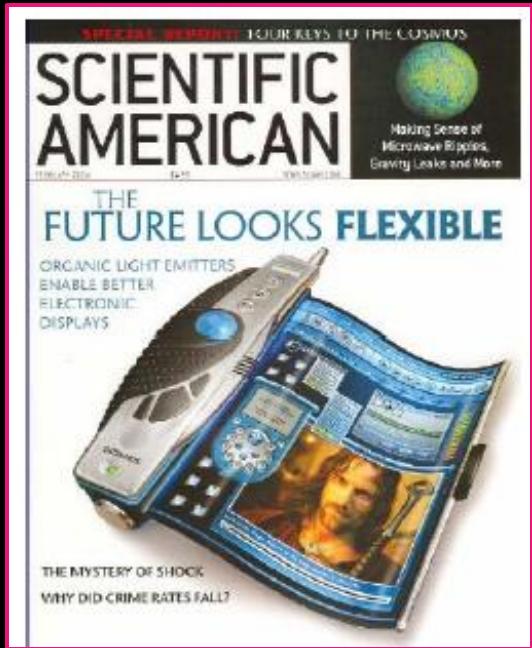
electronics



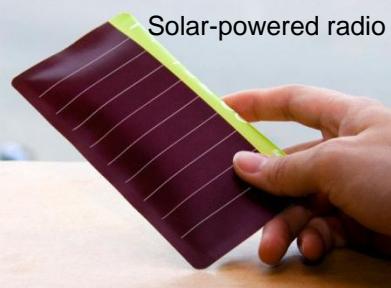
First integrated circuit: Kilby



The Vision of Flexible/Printed Electronics



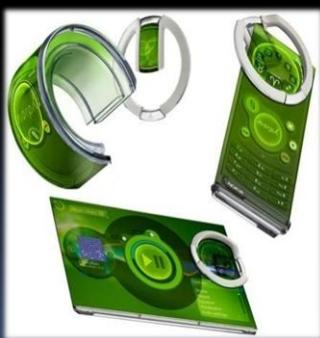
Flexio : Yanko design



Solar-powered radio



ITRI/ UCSB



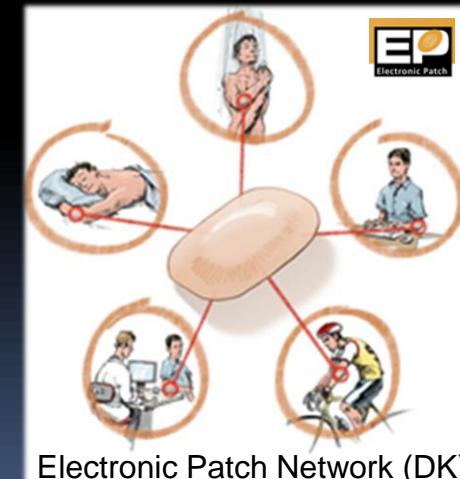
Nokia concepts



Antenna Design concept



Freshness sensor
(RF, H₂S gas)



Electronic Patch Network (DK)

Displays, sensors, RFID, batteries, solar cells, lighting, ...

'Printed Electronics' Market

IDTechEx

2250 organisations developing printed electronics:
up 50% from two years ago

\$1.9 billion 2009



\$57 billion 2019
(~76% printed)

"The market for **printed electronic devices, components and systems**, according to Hannah ,“(CEO of **\$300 billion worldwide within the next 20 years.**”
(Printed Electronics Now, Dec 3, 09)

Thin Film/Printable Batteries Market to Reach \$5.6 Billion by 2015 According to New NanoMarkets Report

New NanoMarkets Report Predicts Printed Sensors to Generate \$5.4 billion in Revenues by 2016

but recently also:

NanoMarkets

Home

Time for a Dose of Market Realism in Printed Electronics

March 01, 2010 •

There is a significant market potential for printed electronics

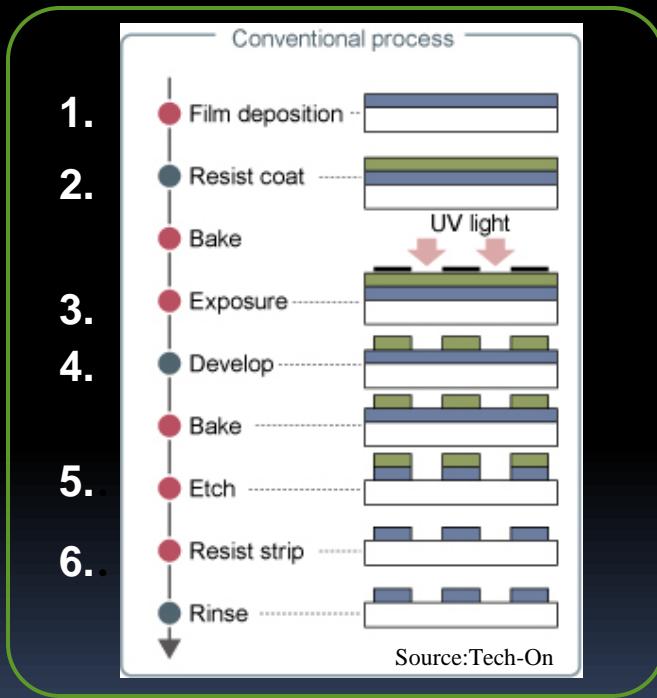
Key Motivations for Printing Electronics

- Large area or Roll-to-Roll processing
 - Other technologies would be difficult/slow (ex: printed connectors, keypads, PV, lighting,...)
- Difficulty in processing materials otherwise
 - Materials compatibility (ex: glucose sensor strips, OLEDs)
 - Substrate topography/fragility (e.g. thin solar cells, MEMS)
- Thin product form factor
 - Rollable, conformal
- Price:
 - Ultra-low price (ex: battery tester, PV, OLEDs, RFID ?)
 - ->market has to be very large to make money

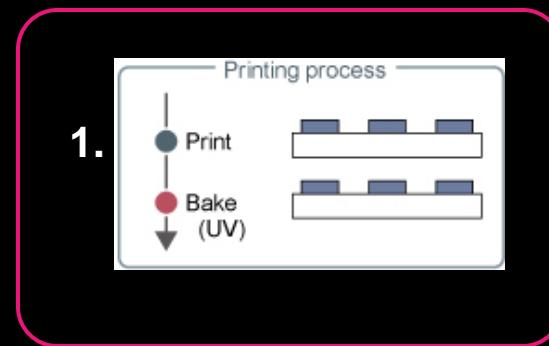
Printing = Additive Deposition

- 1 step vs 6+ patterning steps

Conventional process

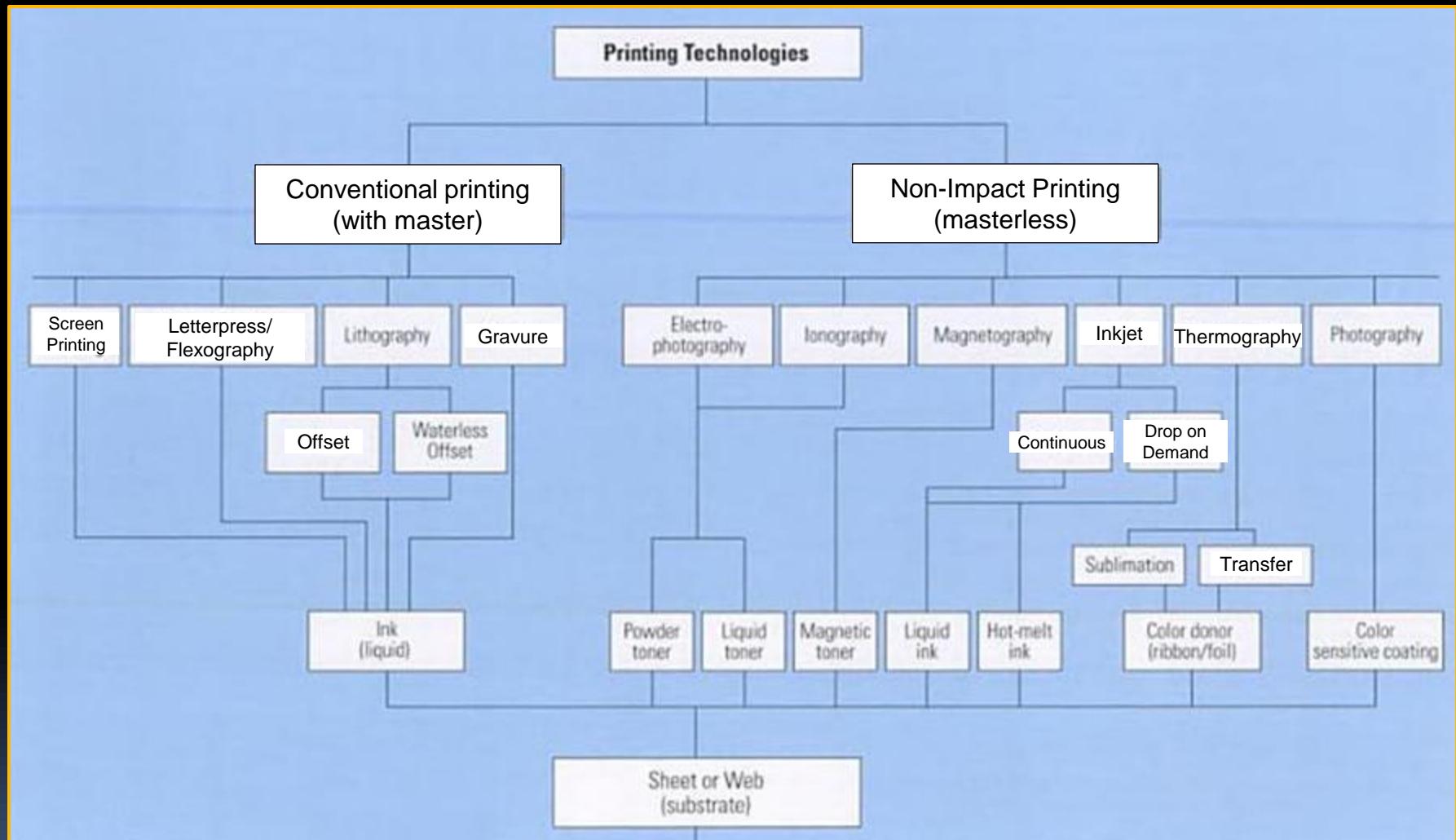


Printing process



Printing Technologies

Printing Technologies



after “Handbook of print media: technologies and production methods”, Helmut Kipphan

“Printing” for Electronics: Includes a Variety of Processes

- **Inkjet**, gravure, offset, flexo, (rotary) screen, ...
- Microcontact, (Nano)imprinting
- (Laser) transfer printing (of high performance circuits)
- Dip-pen nanolithography
- Dry printing (Organic vapor deposition)
- Hot embossing
- Laser processing (cutting sintering, patterning)
- Stamping/die cutting
- Slot-, dip-, spray-coating
- R2R etching, photolithography (!)
- Lamination, ...

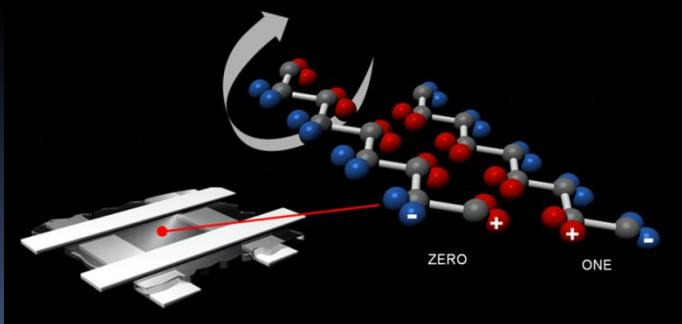


Example R2R Printing Process



<http://www.thinfilm.se/about-us/manufacturing>

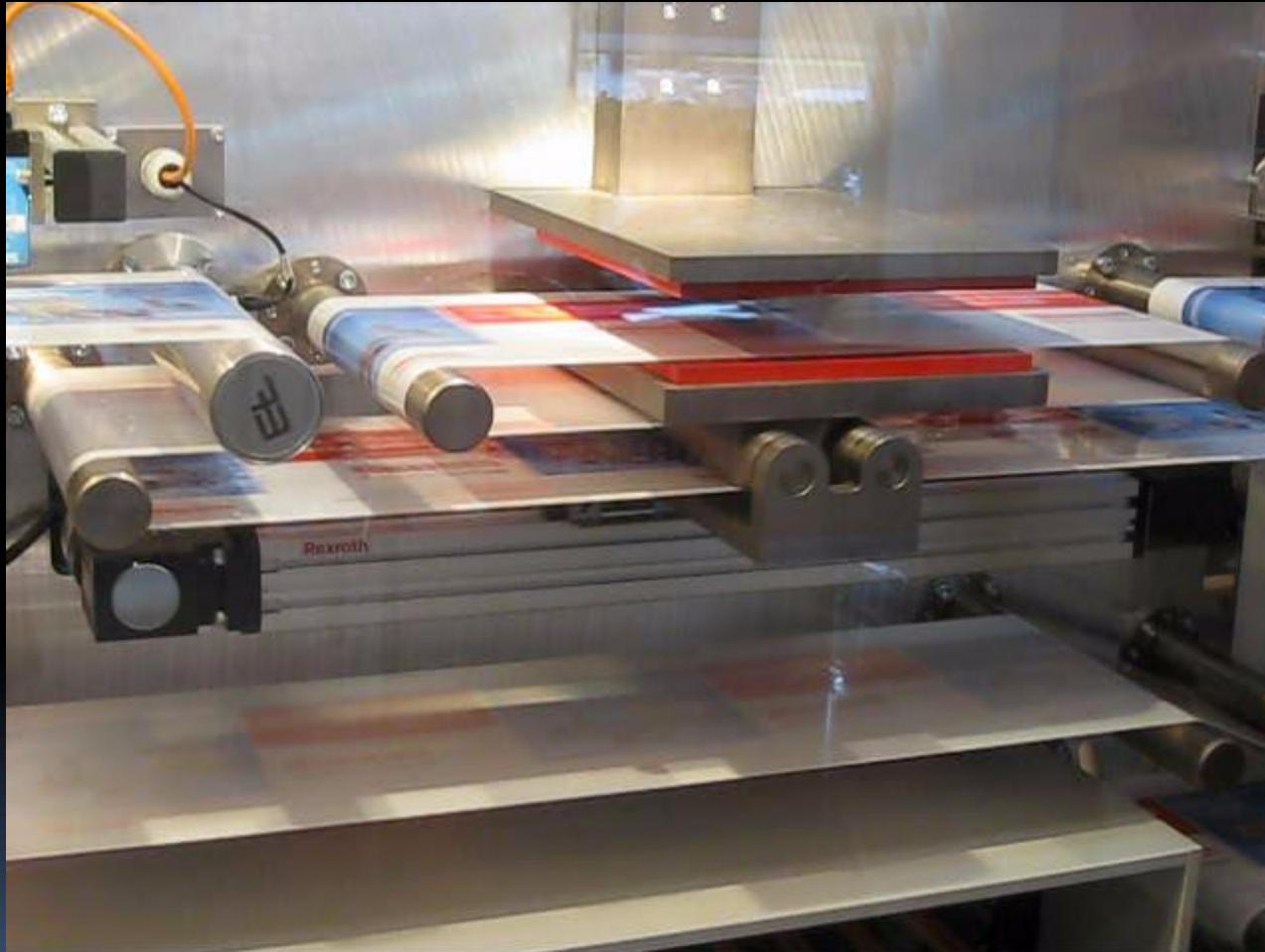
Ferroelectric polymer memory



Gravure, microgravure, rotary screen printing



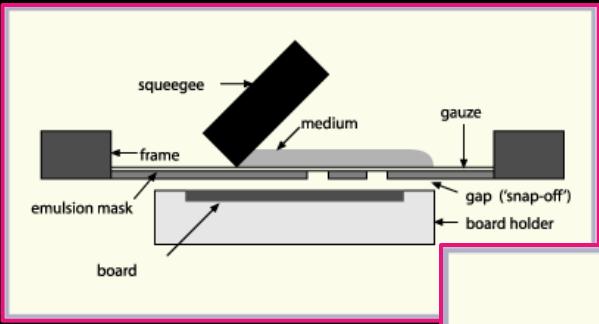
Example of a R2R System with Stationary Step (Bosch Rexroth)



ICFPE09

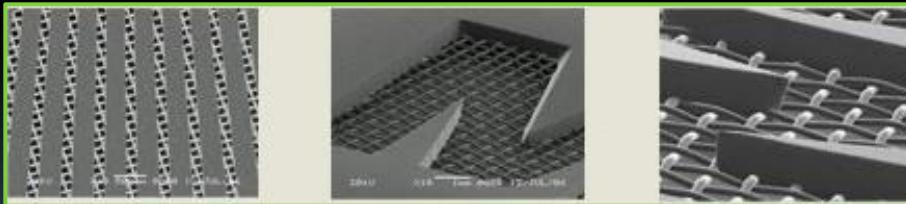
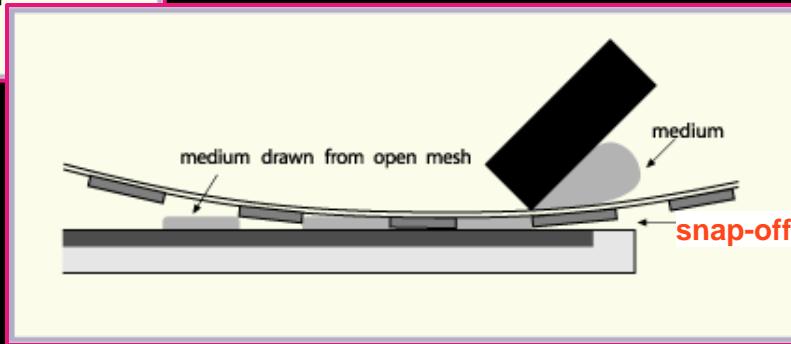
could enable a roll-to-roll (R2R) photolithography step

Screen Printing (I)



screen printing factors:

- screen parameters
- snap-off distance
- screen tension (N/cm)
- print speed
- squeegee pressure
- squeegee durometer
- angle of contact



Murakami Screen

Screen example:

- Mesh count (per inch): 305
- Thread dia(mu):34
- Mesh opening (mu): 47

Properties

- + robust
- + simple
- (+) thick layers
- large feature size (~100μm)
- high ink viscosity (>10,000cP)
- slower speed (5m/min)



Conventional screen printing
on clothing, packaging

Screen Printing (II)

- Flat screen or rotary screen printing



Kammann - Flat Screen Head on web printing machine



Mark Andy – Rotary Screen

‘kiss printing’
(zero off-contact)

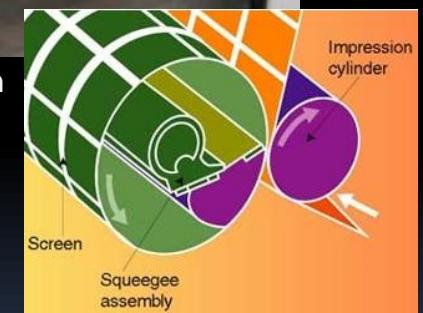
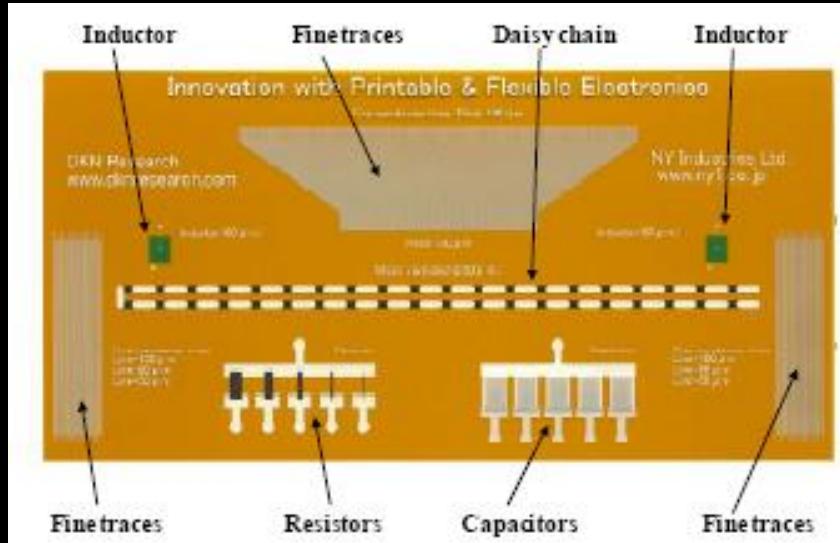


image courtesy of Storck Screens

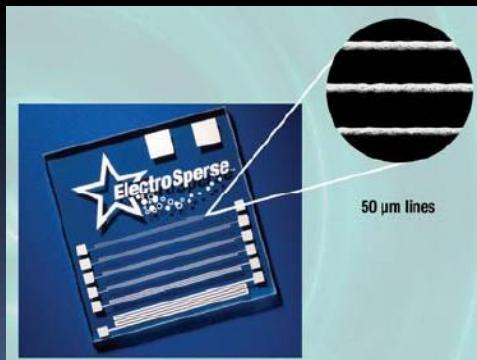
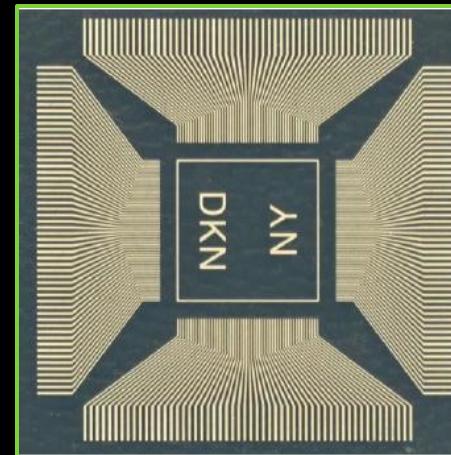
Speed : up to ~20 m/min (R2R)
[up to ~100m/min (~450ft/min)]

Screen Printing (III)

- “Advanced Screen Printing”: 30 µm lines/spaces (10-20 µm in development)



D. Numakura, DKN Research (<http://www.dknresearch.com/>)



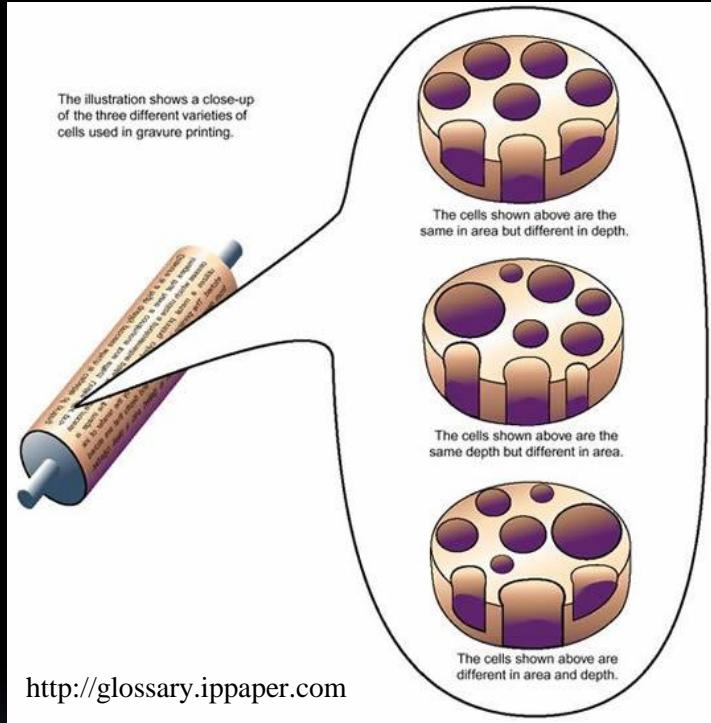
Five Star Technologies, ElectroSperse
inks 50 µm lines



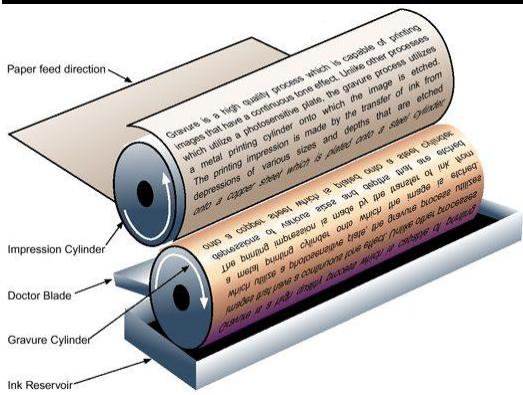
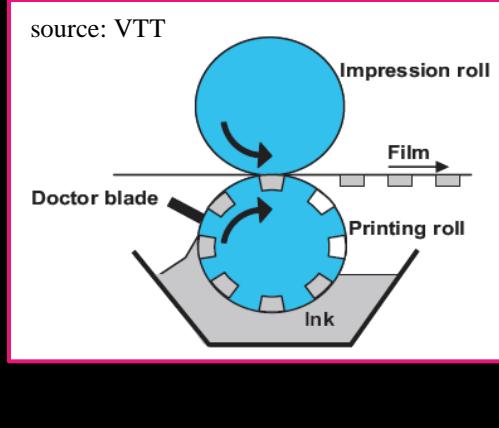
Screen-printed electroluminescent display

Gravure Printing

Rotogravure

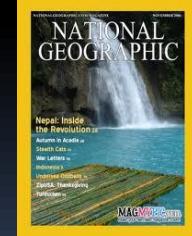


Daetwyler: Chrome plated cylinder



Properties

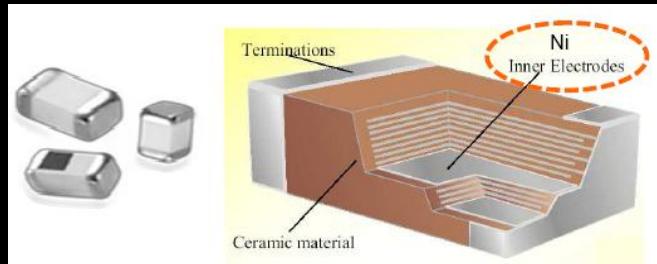
- + fast printing (50m/min) up to ~3000 ft/min
- + dot thickness variation
- + low dot gain
- + High resolution
- + use of organic solvents possible
- ~10-500cp ink viscosity
- relatively high plate cost (metal cylinder)



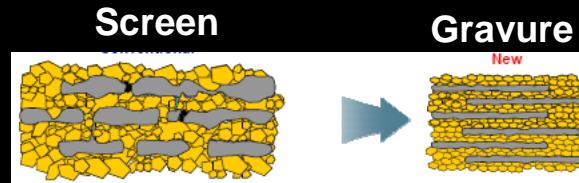
Gravure printing for high quality print "e.g. National Geographic"

Gravure Examples

MLCC(Multi-Layer Ceramic Capacitor)

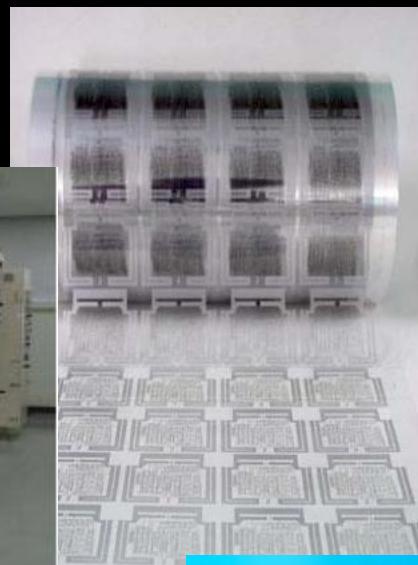


Samsung (SEMCO)



Thinner layers (lower ink viscosity)
Higher speed (50m/min vs 5m/min)

RFID tags



Sunchon National University

Flexographic Printing



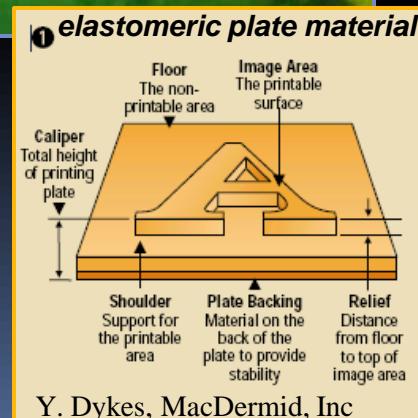
Flexo printing plate



MarkAndy LP3000: 230m/min



Source: wikipedia



Properties

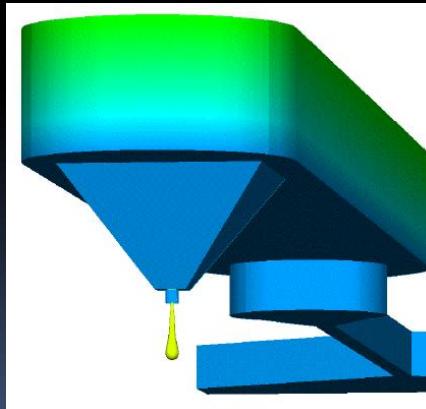
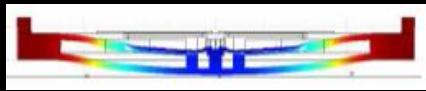
- + inexpensive plate pattern
- + high throughput
- + thin ink layers / low viscosity inks
- plate degradation due to solvents



Printing on packages:
Corrugated cardboard and foil

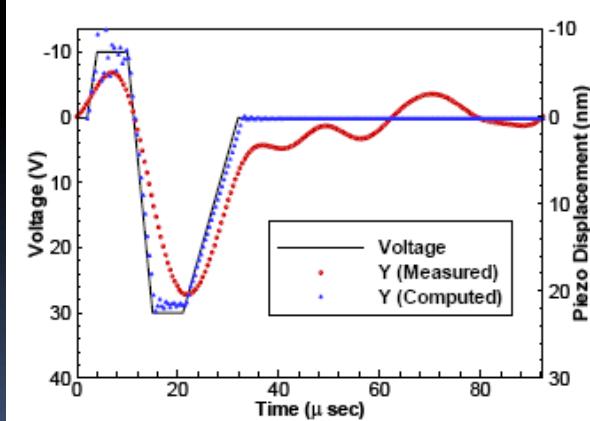
Inkjet Printing

- Piezo
- Thermal
- Electrostatic
- Acoustic
 - Continuous
 - Drop on demand

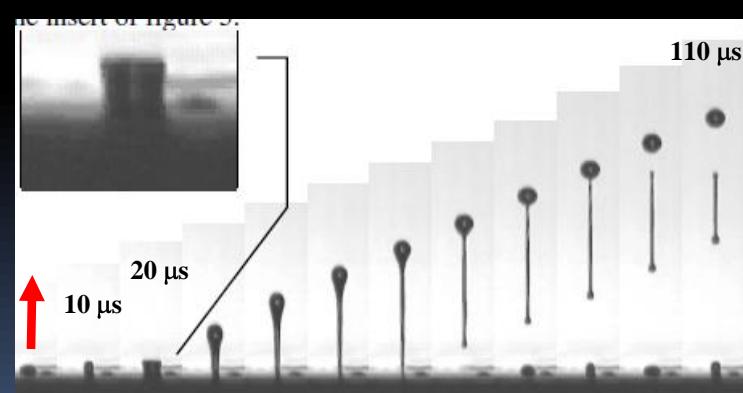


SAMSUNG (SEMCO)

Piezo-inkjet

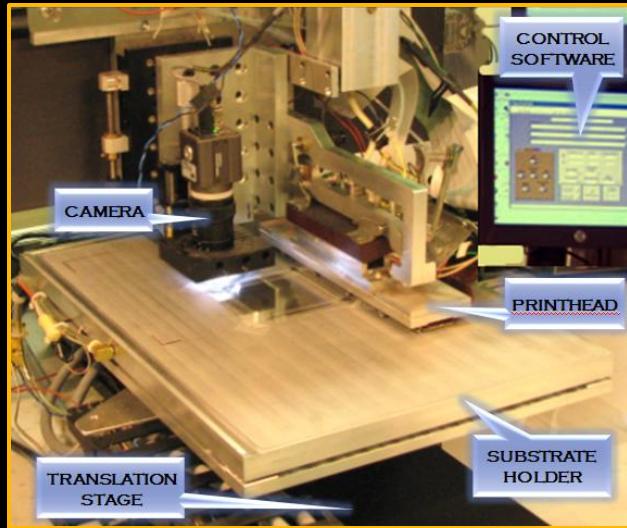


Y.Yoo, ICFPE09



H. Wijshoff, Oce Technologies

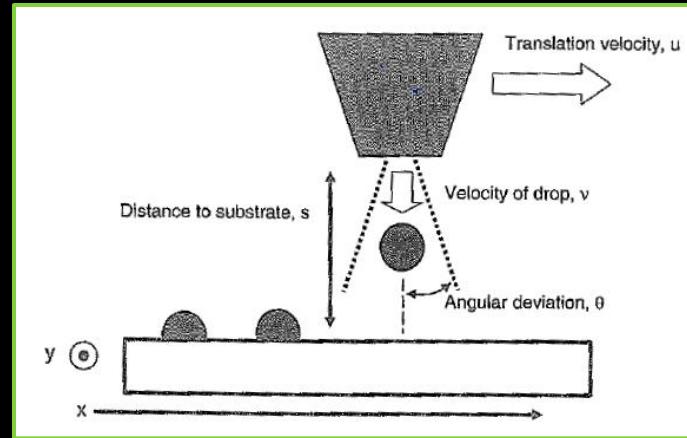
Inkjet Printing – Drop Formation



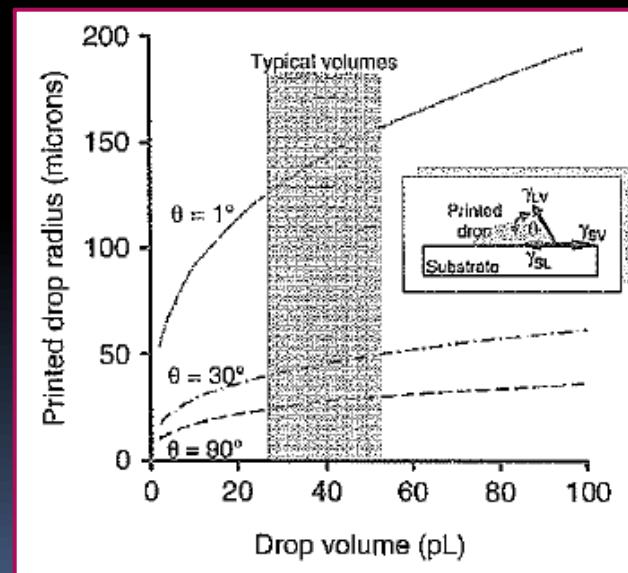
PARC inkjet system

Feature size:

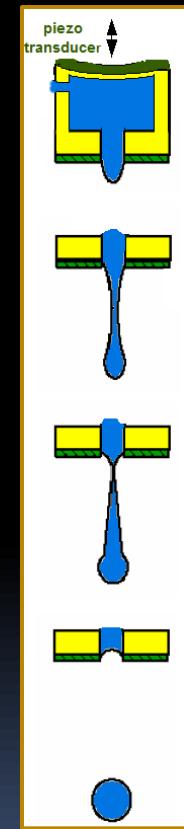
depends on printed volume and drop interaction with surface



For high accuracy keep distance low !



W. Wong, et al., in 'Flexible Electronics', W. Wong, A. Salleo, edt., Springer



Piezo-inkjet

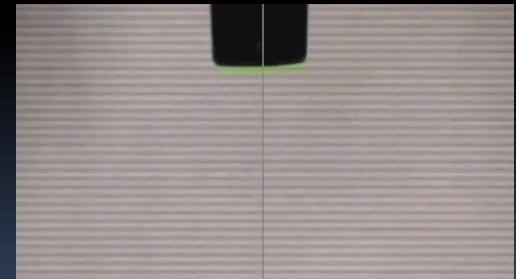
'Jet-printing' - why ?

- Variable digital pattern
- On-the-fly error correction (e.g. on flexible substrates)
- Non-contact printing (substrates with topography, fragile substrates)
- Low operation cost (low ink consumption)
- Wide range of inks (hot-melt wax, solder, bio-materials, low-viscosity inks, ...)

why potentially not ?

- limited resolution
- limited printing speed
- jetting reliability issues
- sensitivity to substrate variations

Piezo-inkjet

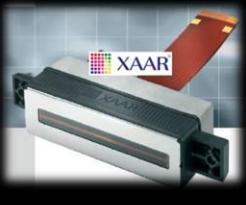
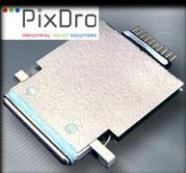


(PARC)

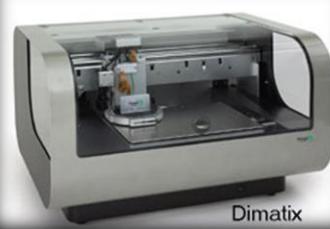
Jet-printing is excellent for prototyping !

Jet-printing for Electronics - components

printheads



systems

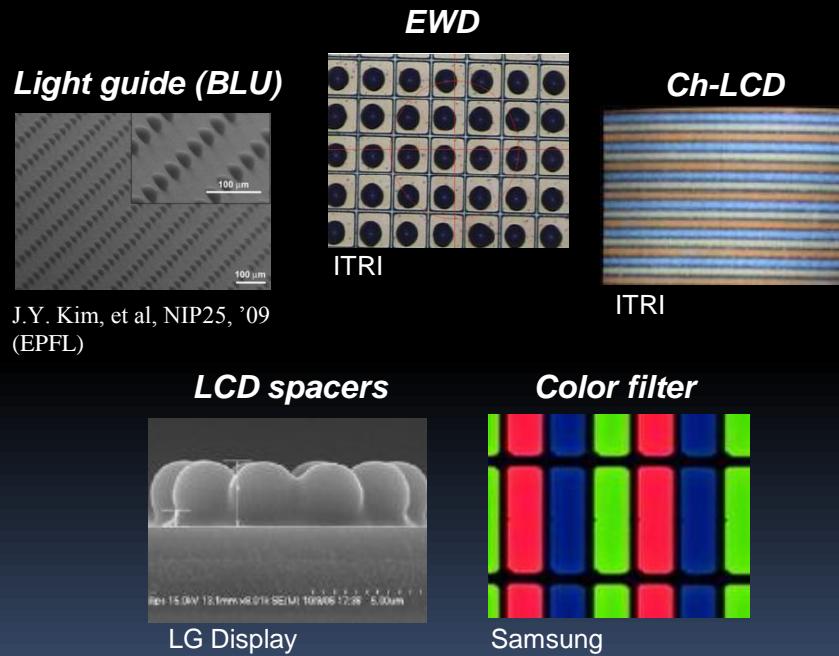
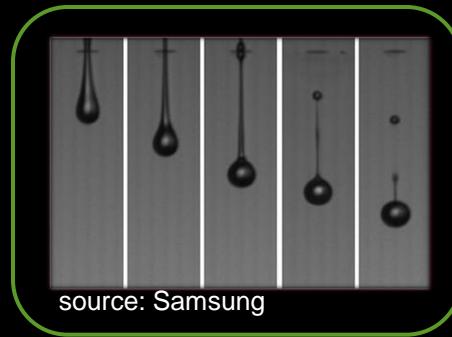


inks



Applications of Jet-Printing (for electronics)

- Color Filters
- LCD Spacers
- Polyimide alignment layer
- Scratch resistant layers
- Back light unit
- LCD materials
- OLED materials
- Electrophoretic media
- Electrowetting fluids
- **Electronic circuits**



Technology Comparison

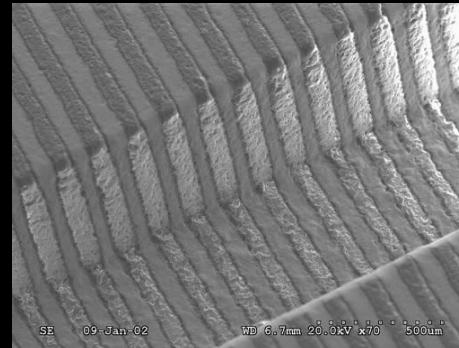
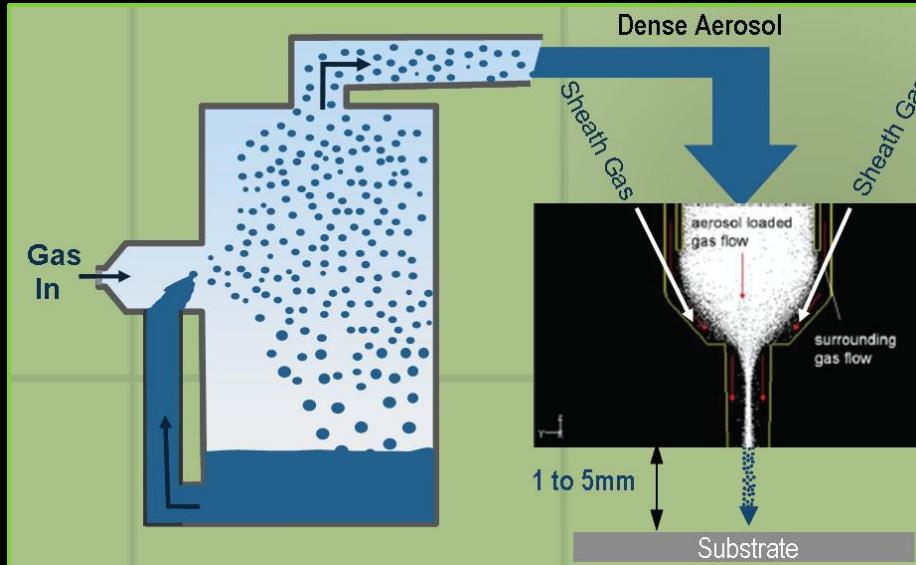
<i>Printing method</i>	<i>Viscosity (Pas)</i>	<i>Layer thickness (μm)</i>	<i>Feature size (μm)</i>	<i>Registration (μm)</i>	<i>Throughput (m^2/s)</i>
Gravure printing	0.01-0.2	< 0.1-8	75	> 20	3-60
Flexography printing	0.05-0.5	0.04-2.5	80	< 200	3-30
Offset printing	5-100	0.5-2	10-50	> 10	3-30
Screen printing	0.5-50	0.015-100	20-100	> 25	2-3
Ink jet printing	0.001-0.04	0.05-20	20-50	5-20	0.01-0.5

*G.E. Jabbour et al (2001), A. Huebler et al (2002), M. Bergsmann et al (2003),
T. Kawahara et al (2003), A. Blayo et al (2005), A. Maaninen et al (2005),
H. Sirringhaus et al (2006), M. Schrödner et al (2006), Y. Xia et al (2006)*

Source: H. Kopola, VTT

Aerosol Jet^(R) Printing

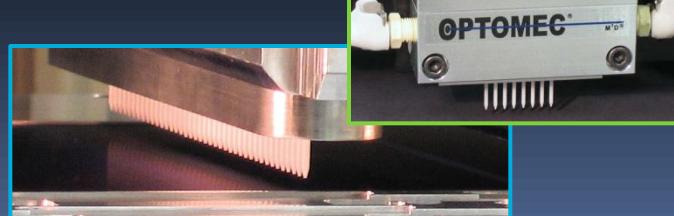
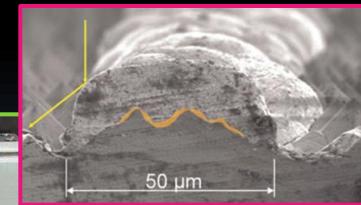
■ M3D (Maskless Mesoscale Materials Deposition)



60 μ m Ag lines written over a 500 μ m trench.

For solar cell gridlines:

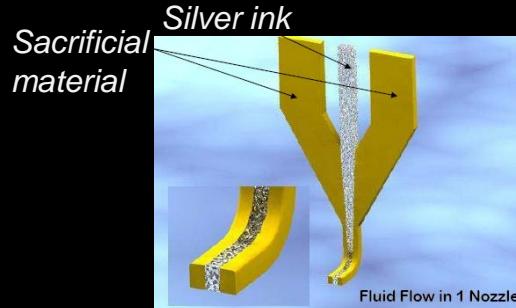
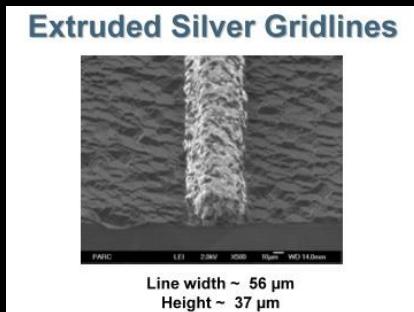
- Combines with light induced plating
- 2% efficiency gain vs screen printed lines (Fraunhofer Inst.)
- up to 40 nozzle head



- Conformal printing on 3D surfaces: 1-5 mm standoff
- Viscosity: 1-2500cP
- Feature size <35 μ m
- Non-contact
- Thickness: 100nm-5 microns (single layer)
- Write speed: 100mm/sec

Direct-write Extrusion

- Co-extrusion printing (developed at PARC)



PARC
(<http://www.parc.com/>)

- MDDW (Micro-Dispensing Deposition Write)



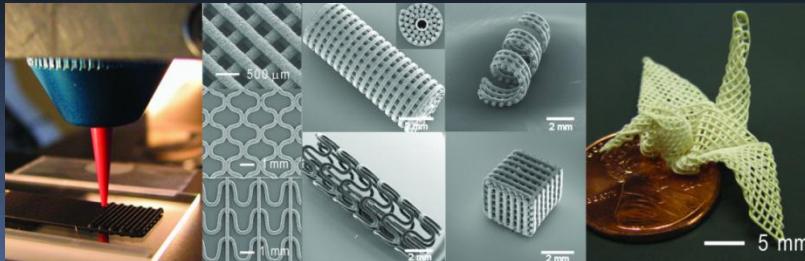
- 3D substrates
- Viscosities up to 1mio cP
- ~50-100 μ lines
- Up to 200mm/sec

3D antenna printed on helmet: N.S. Kim, K.N. Hahn, ICFPE 2009



(<http://www.nscriptinc.com/>)

- Direct Deposition

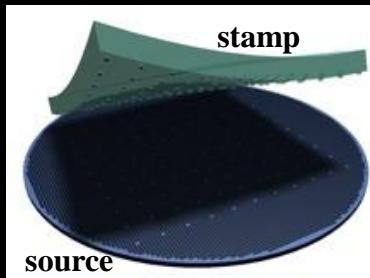


J. Lewis group, U. Illinois, Urbana-Champaign

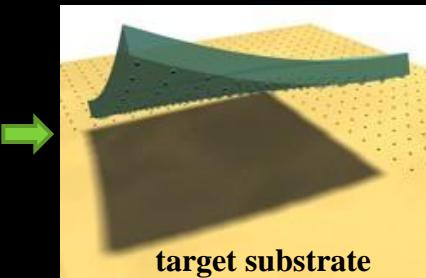
Bok Yeop Ahn , et al., **Advanced Materials**
Volume 22, Issue 20, pages 2251–2254, May 25, 2010

Transfer Printing (I)

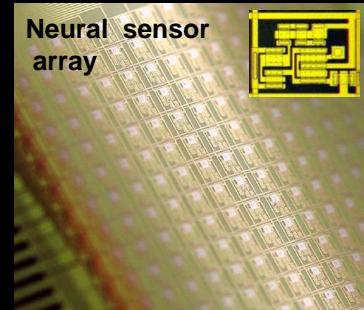
▪ Chip Micro-transfer Printing



credit: Semprius, Inc.

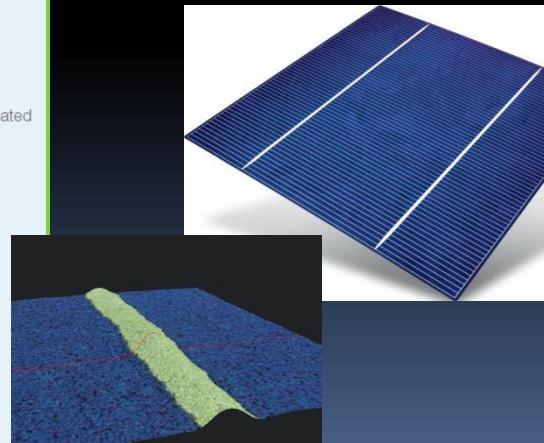
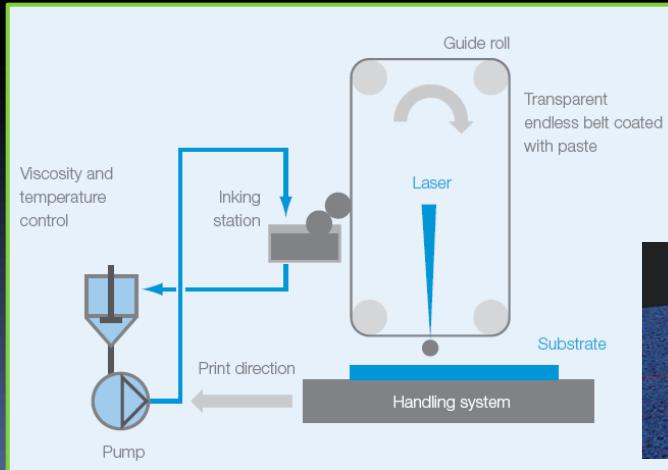


credit: John Rogers, U. Illinois, Urbana



▪ Laser Transfer

example: solar cell gridlines

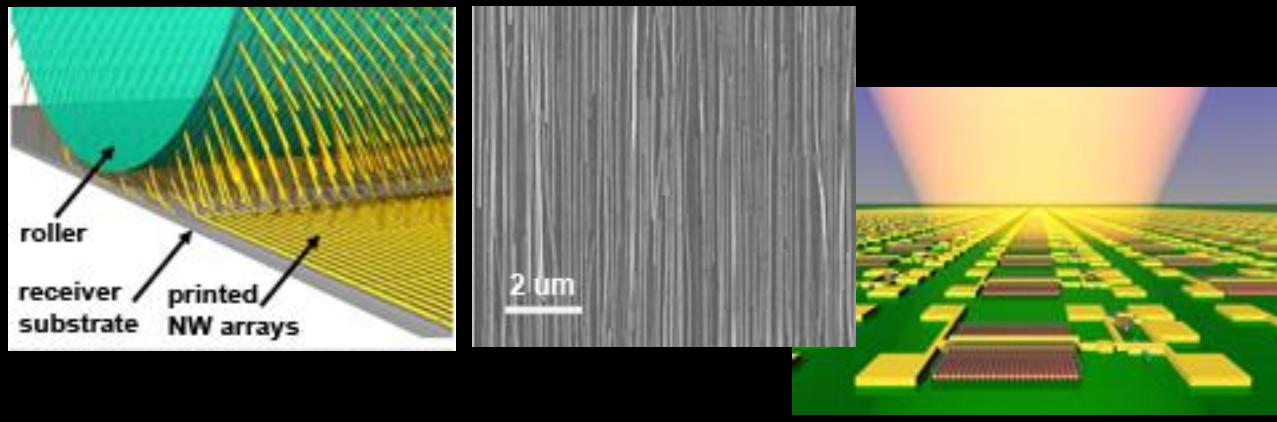


BASF / Schmid Group

- Fine line (< 100 μm)
- High aspect ratio(>0.3)
- Non-contact

Transfer Printing (II)

- Transfer of nanowires (Javey et al., UC Berkeley)



- Thermal transfer of conductive materials



Iimak thermal transfer ribbon:
<http://www.iimak.com/>

Printing Materials



graphic inks



electronic inks

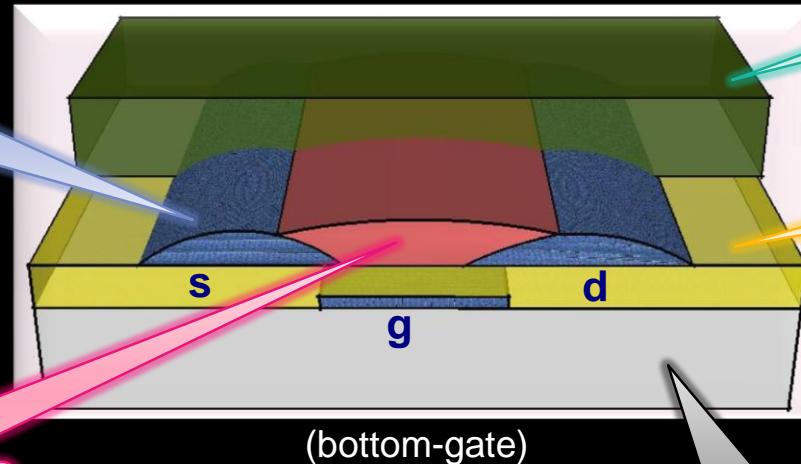


Source: DuPont

The TFT as the Basic Circuit Element

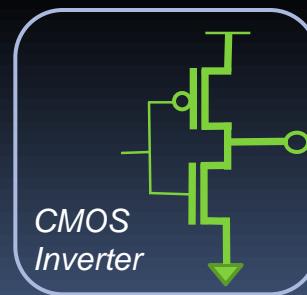
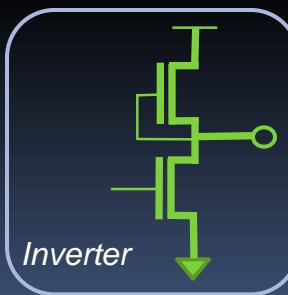
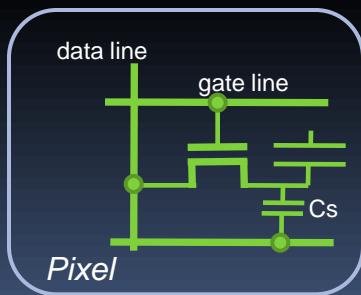
Thin Film Transistor (TFT)

s,d,g – contacts
+ interconnects:



semiconductor:

substrate:



...

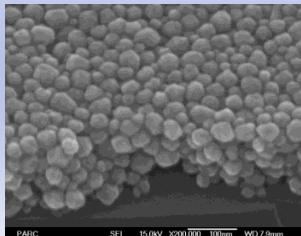
The transistor performance determines the extent of applications

The Printed TFT

- Materials compatible with solution processing

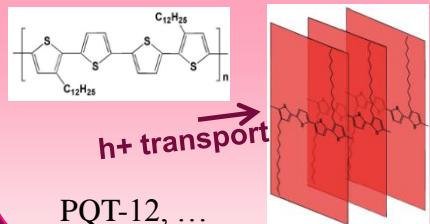
s,d,g - contacts:

(nanoparticles, polymers,...)

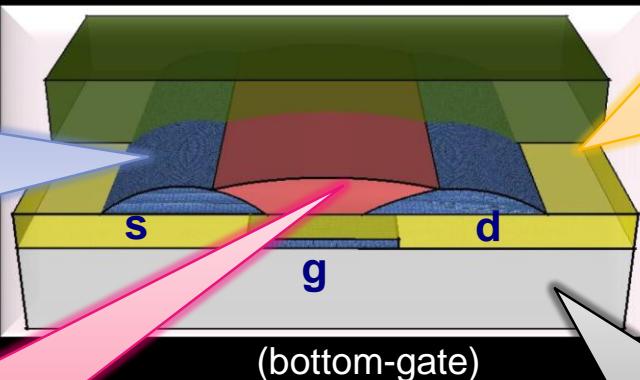


semiconductor:

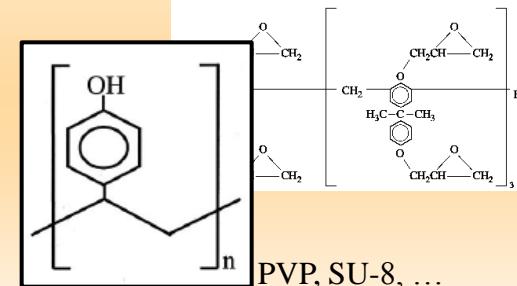
(polymers, oligomers,
precursors, nanoparticles)



Thin Film Transistor (TFT)



inter-layer dielectrics: (gate dielectric, top dielectric)



Substrate:

- polyester
 - polyimide
 - **PEN** (polyethylene naphthalate)

-> performance of the materials has to be optimized and process compatibility ensured

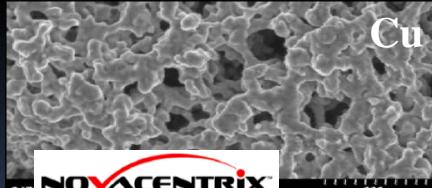
(similar materials sets apply for OPV, OLED, ...)

J. Daniel, et al., Mater. Res. Soc. Symp. Proc. Vol. 1114, Fall 2009, 1114-G13-03

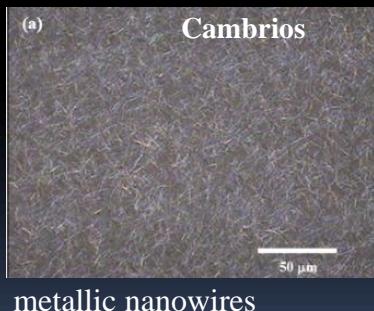
Many new materials are introduced into the process

Conductors

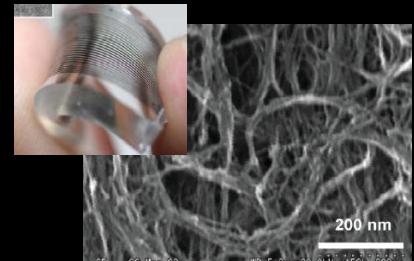
- Carbon, silver, aluminum flakes
- Polypyrrole, polyacetylene, thiophenes (PEDOT:PSS)
- Metal nanoparticles (Ag, Au, Cu, Ni - annealing or photonic curing)
- Catalyst printing + plating
- Reduced printed metal salts, metal precursors
- Carbon nanotubes, metal nanowires
- Graphene inks
- Solder
- Elastic Conductors



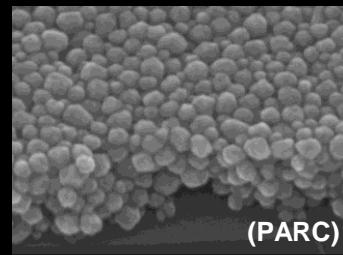
Photonic cured Cu (~10x bulk)



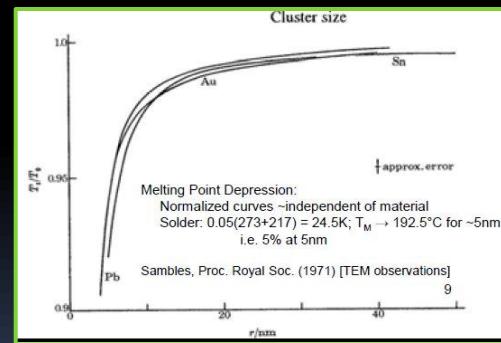
metallic nanowires



Elastic conductor
(T. Sekitani, T. Someya, Univ. of Tokyo)



Ag nanoparticles



Melting point depression in nanoparticles

'surface functionalization may be required for TFT s/d electrodes'

Conductors – selected new developments

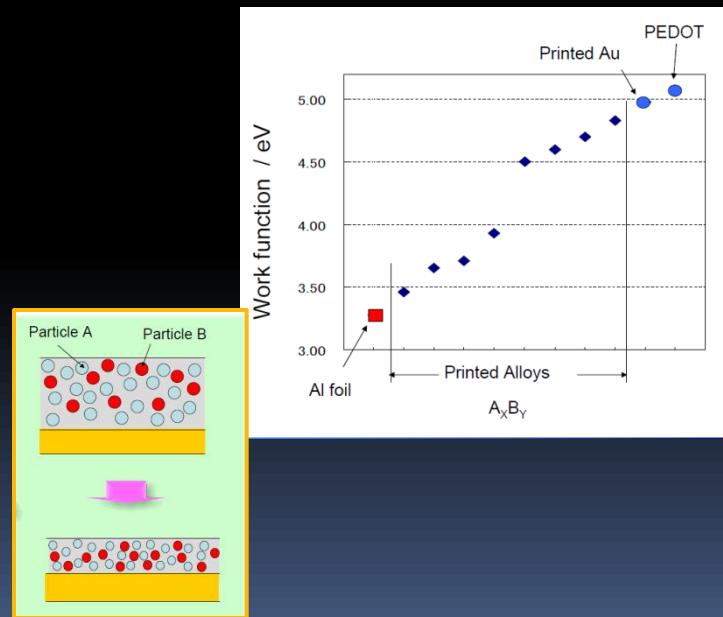
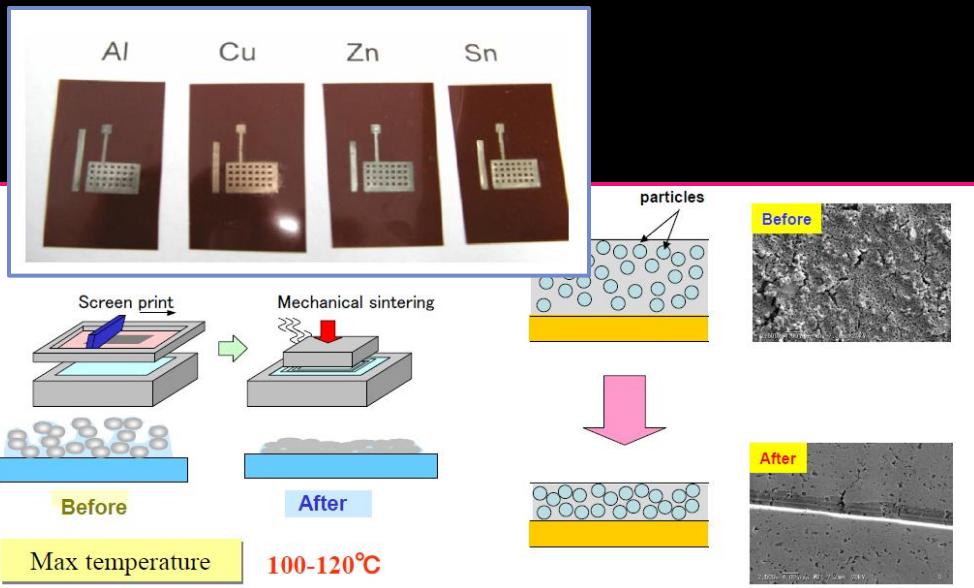
▪ Copper deposition

- in reducing environment (CO, H₂, hydrazine)
- photonic/laser curing



▪ Mechanical sintering (AIST, T. Kamata, Digital Fabrication '09)

- Alloy sintering -> control of work function
- Printed oxide semiconductors (NiO, SnO₂, In₂O₃)



Insulators

- Oxides, nitrides
- Al₂O₃, HfO₂ (anodization, atomic layer deposition)
- Polymer dielectrics (PVP, PMMA, ...)
- Spin-on-Glass
- Parylene (evaporated)
- Solid electrolytes
- Ion gels
-

potential desirable properties

- pinhole-free layer
- high k or low k
- smooth surface
- transparent
- barrier property
- ...

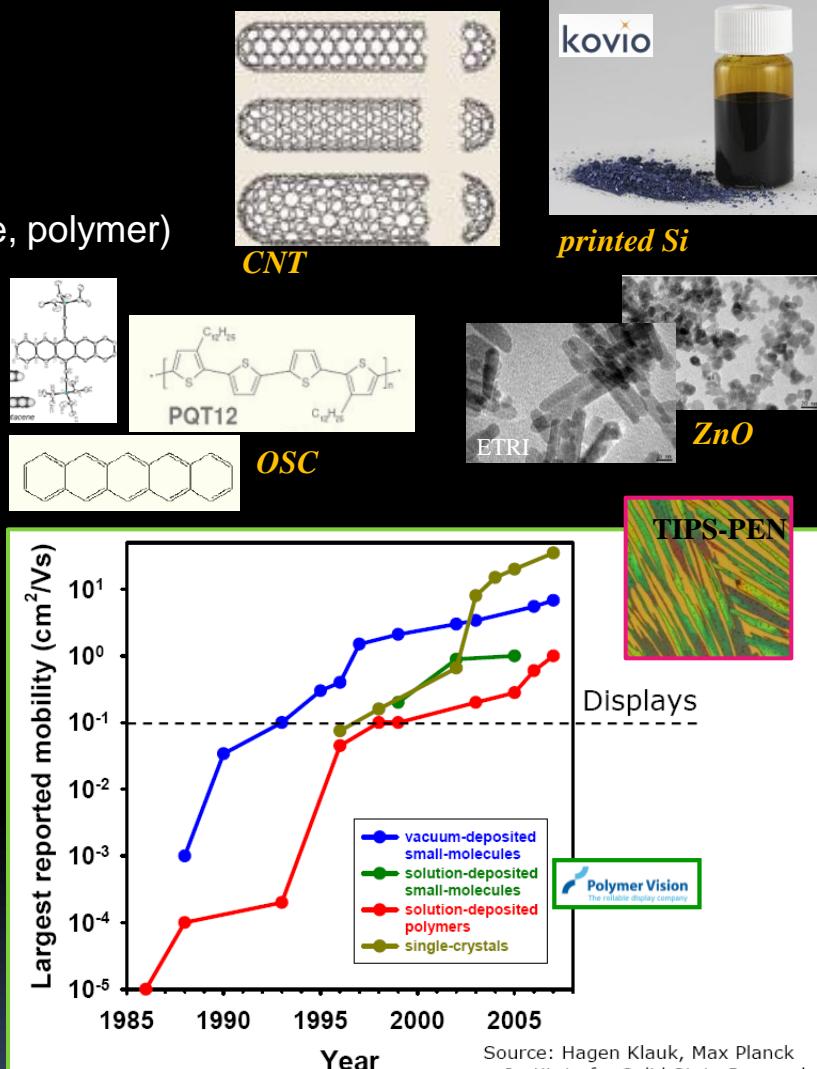
'surface functionalization may be required; high-k dielectrics perform poorly with OSCs'

Semiconductor/ insulator interface is crucial for TFTs

Semiconductors

- Organic semiconductors (small molecule, polymer)
- Oxide Semiconductors (ZnO, InGaZnO)
- CNT
- Nanowires, nanoribbons (Si, GaAs)
- Nanoparticles (Si, CdSe, ZnSe)

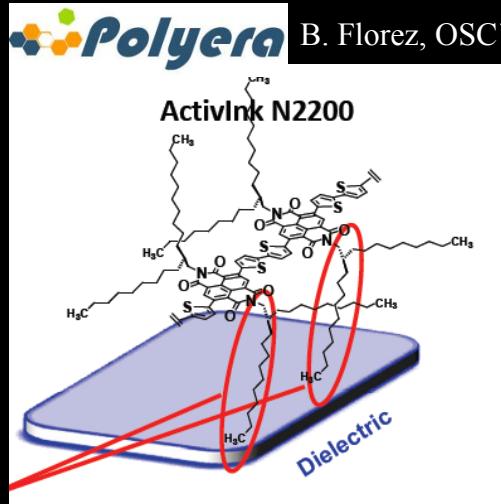
- some processes require annealing steps (e.g. laser or RTP)
- high mobility required for current driven displays (OLED, electrochromic), driver circuit integration and other circuit applications



mobilities of organic SC

Semiconductors – organic examples

■ Recent developments of printable OSC



Works with high-k dielectrics (alkyl chains keep distance from polar interface)

Mobility	On/off	V_{on}
0.2-0.8	10^7	0-5V

ActivInkTM N1500 (Small-Molecule)

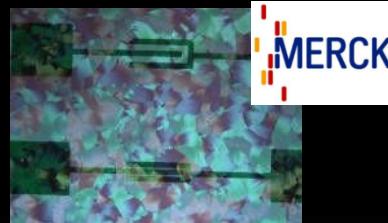
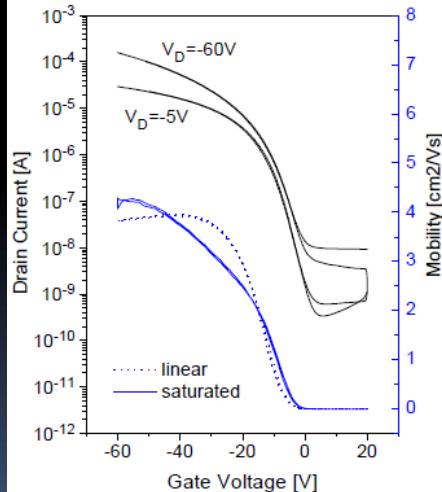
Mobility	On/off	V_{on}
$\sim 2 \text{ cm}^2/\text{Vs}$	$1 \times 10^{6-7}$	<5V

Mobility	On/off (0V)	V_{on}
$\sim 1 \text{ cm}^2/\text{Vs}$	$1 \times 10^{5-6}$	0-10V

N-type

P-type

multi-component OS ink



Mobility up to $\sim 4 \text{ cm}^2/\text{Vs}$

- also new amorphous polymer with mobility $\sim 0.5 \text{ cm}^2/\text{Vs}$ that is easy to process

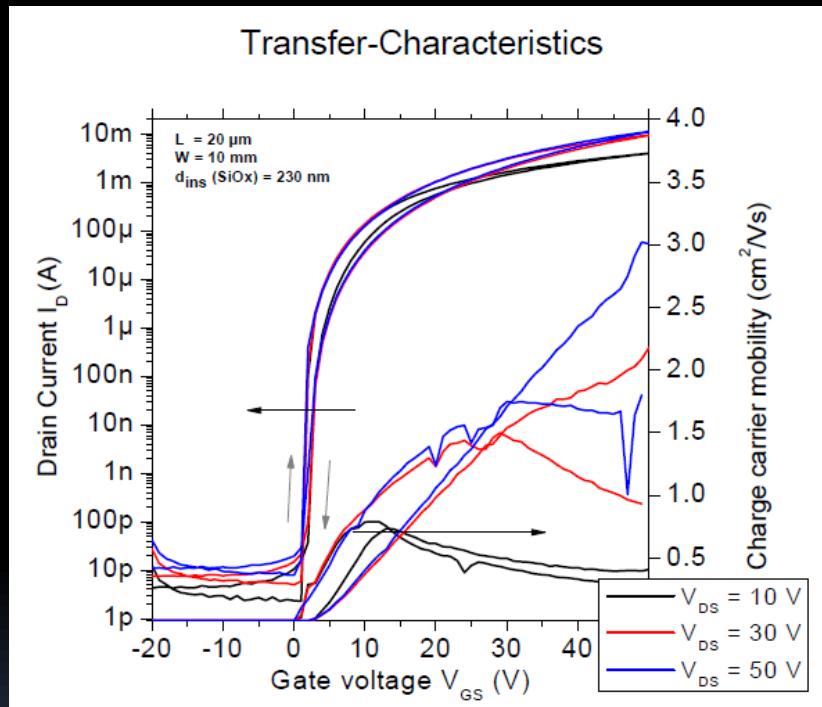
G. Lloyd, OSC'09

challenge:

- High mobility and uniformity
- > trend towards small molecule precursor / amorphous polymer blend

Semiconductors – inorganic example

■ Recent development (H. Thiem, OSC09)

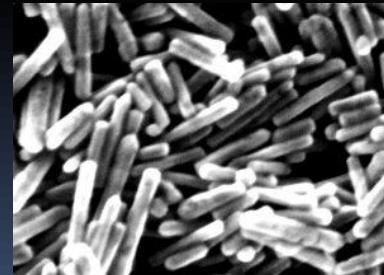
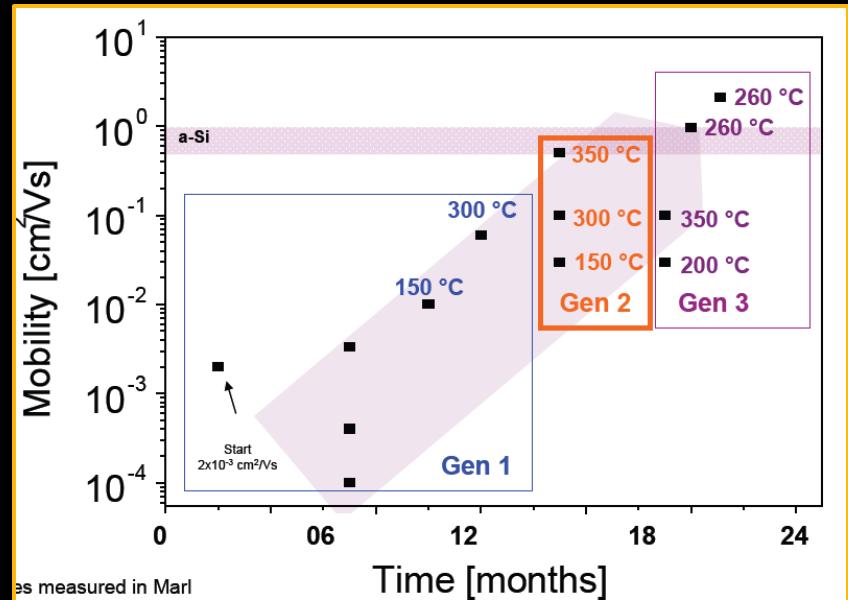


$$\mu_{\text{sat}} = 2.2 \text{ cm}^2/\text{Vs}$$

$$V_{\text{th}} = 2 \text{ V}$$

$$I_{\text{on}}/I_{\text{off}} = 10^7$$

Mobilities up to $5 \text{ cm}^2/\text{Vs}$
(350degC)



ZnO

- Solution processed (jet-printed) inorganics
- Targeting 150degC process temperatures

Challenges in Printed Electronics (with focus on inkjet)

Printing process challenges

- Registration
- Chemical compatibility of materials (orthogonal solvents)
- Physical compatibility (step coverage, work function, etc.)
- Curing/drying of inks within thermal tolerances/time
- Online quality control to optimize process yield
- Controlled atmosphere for sensitive materials

Materials challenges

- Ink viscosity
- Printability
- Ink stability (e.g. particle agglomeration, settling,...)
- Material lifetime (moisture, oxygen, ozone, etc.)
- Cost (e.g. Au, Ag vs Cu)
- Uniformity (e.g. OSC, nanowires, thickness)
- Performance (e.g. OSC mobility)
- Process/solvent compatibility
- Wetting properties (surface tension, surface energy)

Challenges – from the lab to the fab

- Manufacturing challenges:

- Environmental impact / disposability
 - e.g. are nanoparticles ‘safe’ ?, disposable batteries ?
- Health & safety
 - benign solvents
- Process time/ TACT time/ print speed
 - Curing -> UV (photonic curing , laser sintering ?)
- Temperature Budget
 - Low-T curing or photonic curing ?
- Process parameters: substrate tension, layer thickness
- Yield/ uniformity/process monitoring
- Packaging
 - operation in air, elevated temperatures (e.g. in car in summer ?)

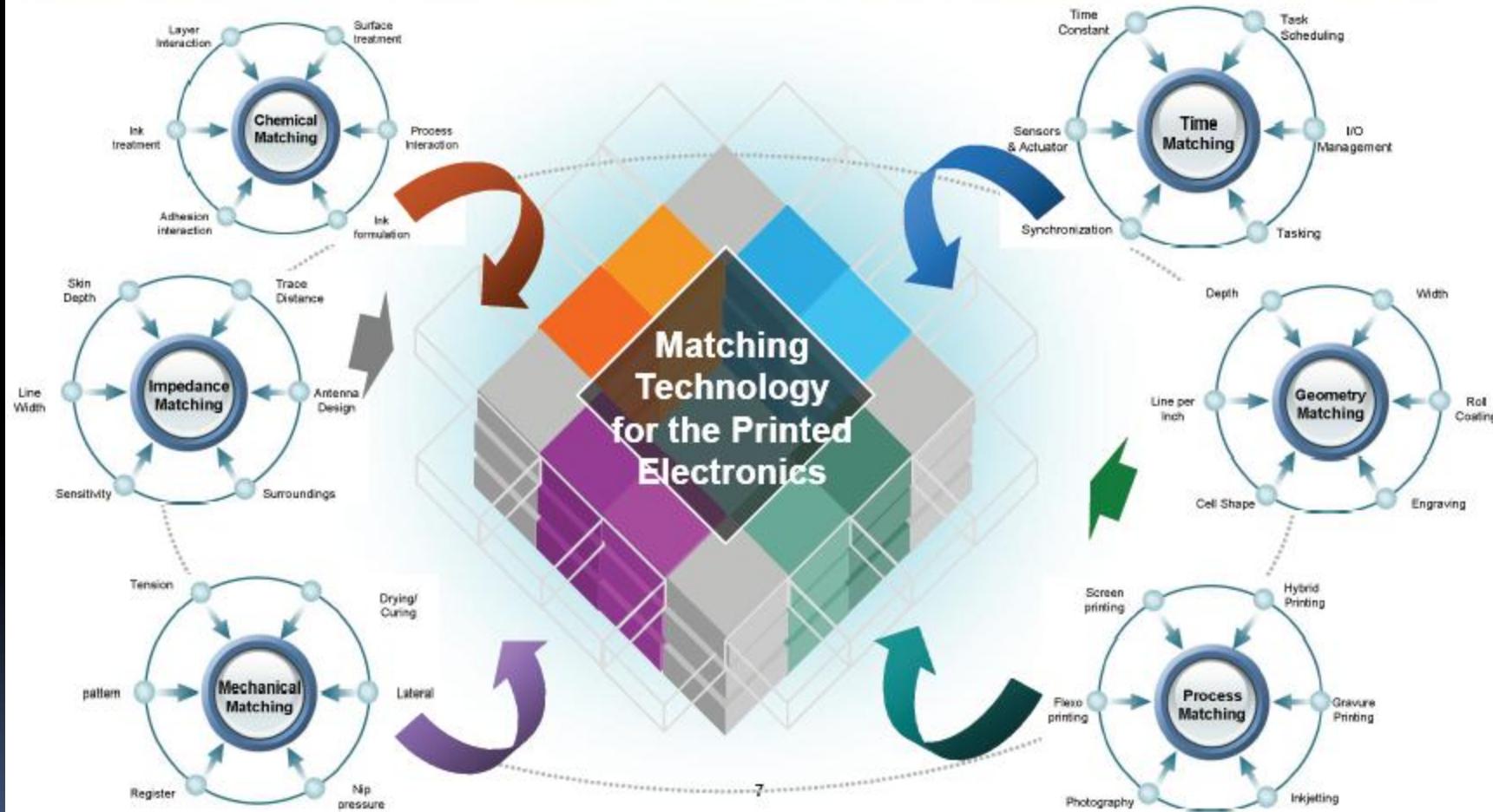
Device challenges

- Parasitic capacitance (affecting device speed)
- Feature size (affecting device area)
- Uniformity (device to device)
- Reliability (pinholes, roughness)
- Yield
- Encapsulation (for longer lifetime)
- Process compatibility of device layers
- Performance tradeoffs (top gate vs bottom gate)

Challenges – from the lab to the fab

Matching : Chemical, Mechanical, Time, Process, Impedance, Geometry etc

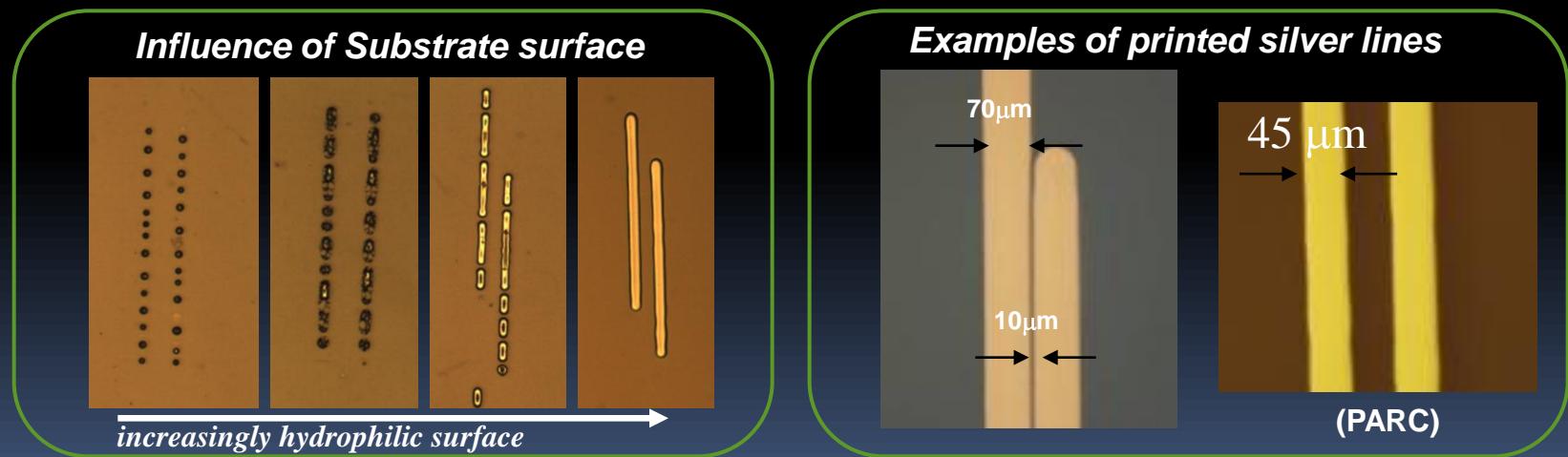
(Nano/Electronic Ink , Patterning, Substrate Handling, Sensors & Actuators, System integration)



K.H. Shin, Konkuk Univ., Seoul, Korea

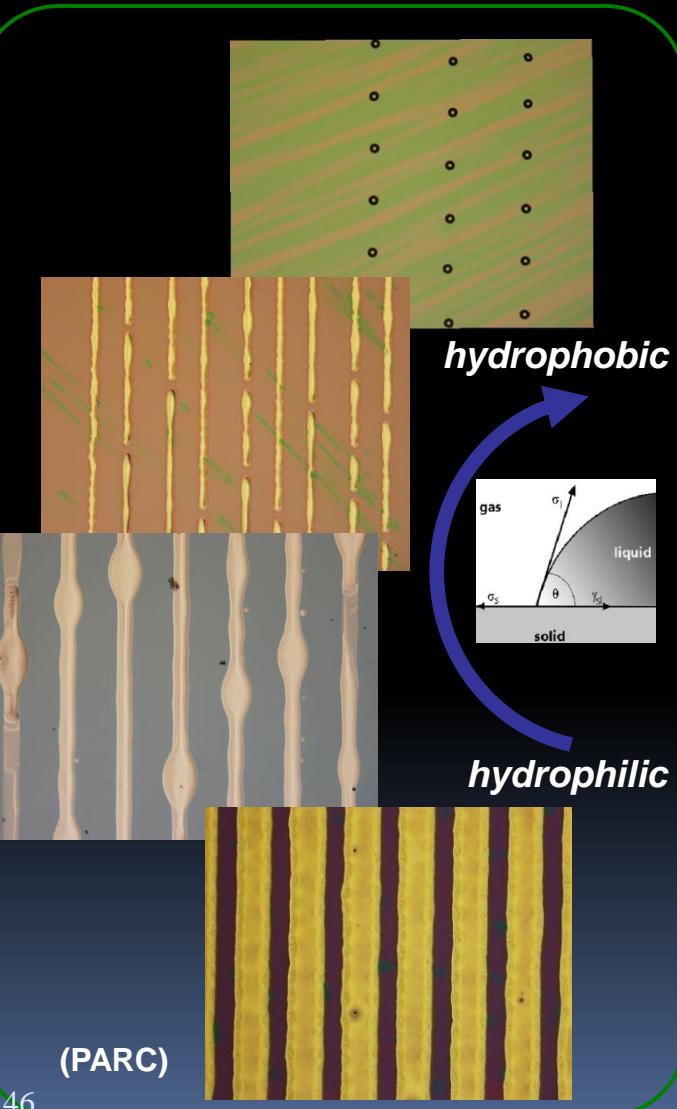
Challenges in (Jet)-Printing Circuits

- Ink formulation (-> adjusted viscosity, surface tension, evaporation rate)
- Substrate quality (-> surface energy, roughness control)
- Layer to layer registration (-> self-alignment)
- Jetting-reliability (-> monitoring, redundancy)
- Ejector-to-ejector variations (-> monitoring, redundancy)
- **Print resolution** (-> surface energy adjustment or patterning, laser assist, novel print-heads)

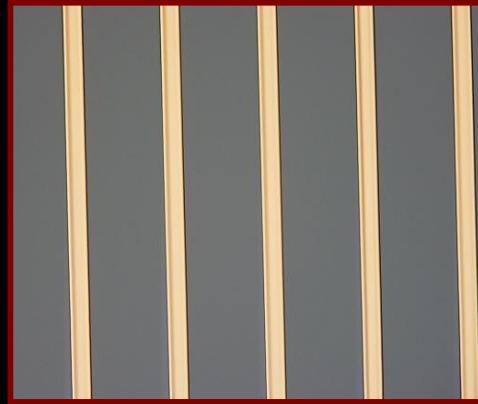


Jet-printing challenges - nanosilver

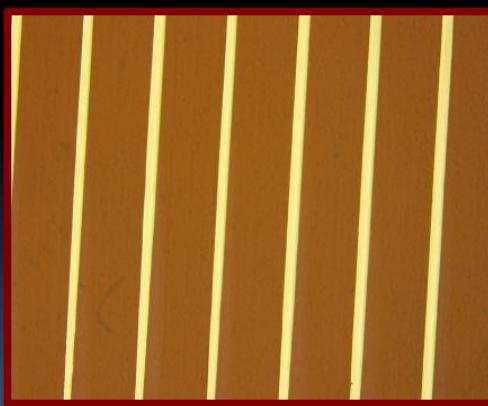
ink-surface interaction



optimized process

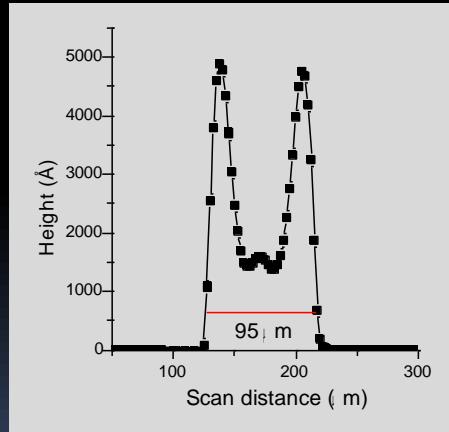
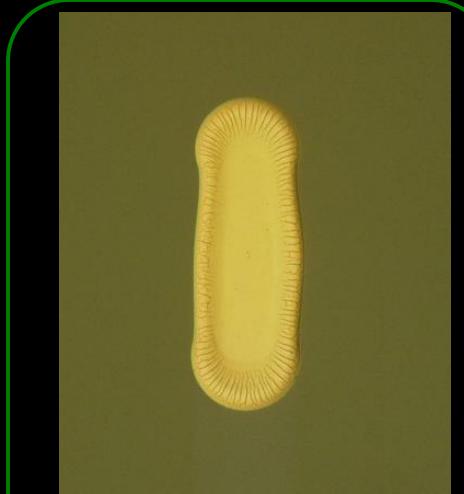


~140 micron lines



~65 micron lines
on polyimide
(smaller drop size)

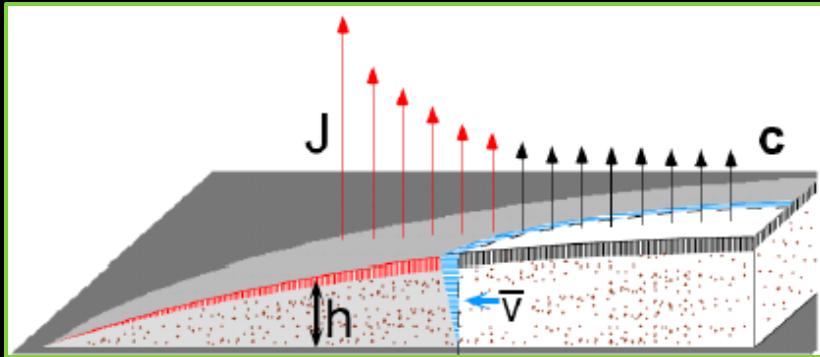
coffee-ring effects



A.C. Arias, et al., J. of the SID, 15/7 (2007) 485
J. Daniel, et al., Jpn. J. Appl. Phys., Vol 46, No 3B (2007) 1363-1369

Coffee Stain Effect

- Typical for inkjet printing: result of the uneven evaporation of a liquid over a droplet's liquid-air interface



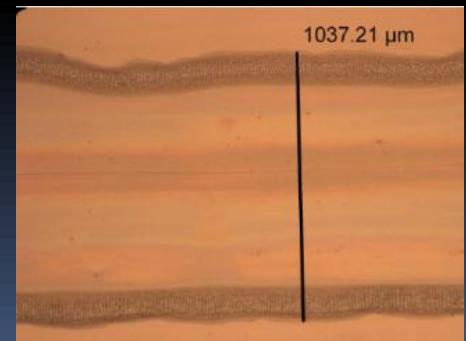
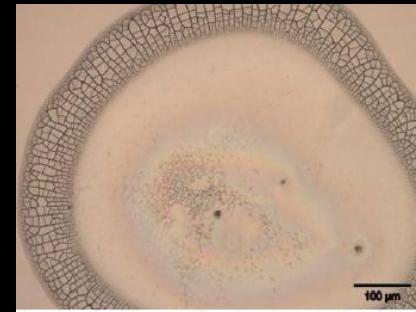
Pinned contact line

- the free surface, constrained by a pinned contact line, squeezes the fluid outwards to compensate for evaporative losses.

'Circulating flows driven by surface-tension gradients (Marangoni flows) can counteract coffee stain: Marangoni flow can be induced by mixing two solvents with different boiling point and different surface tension.'

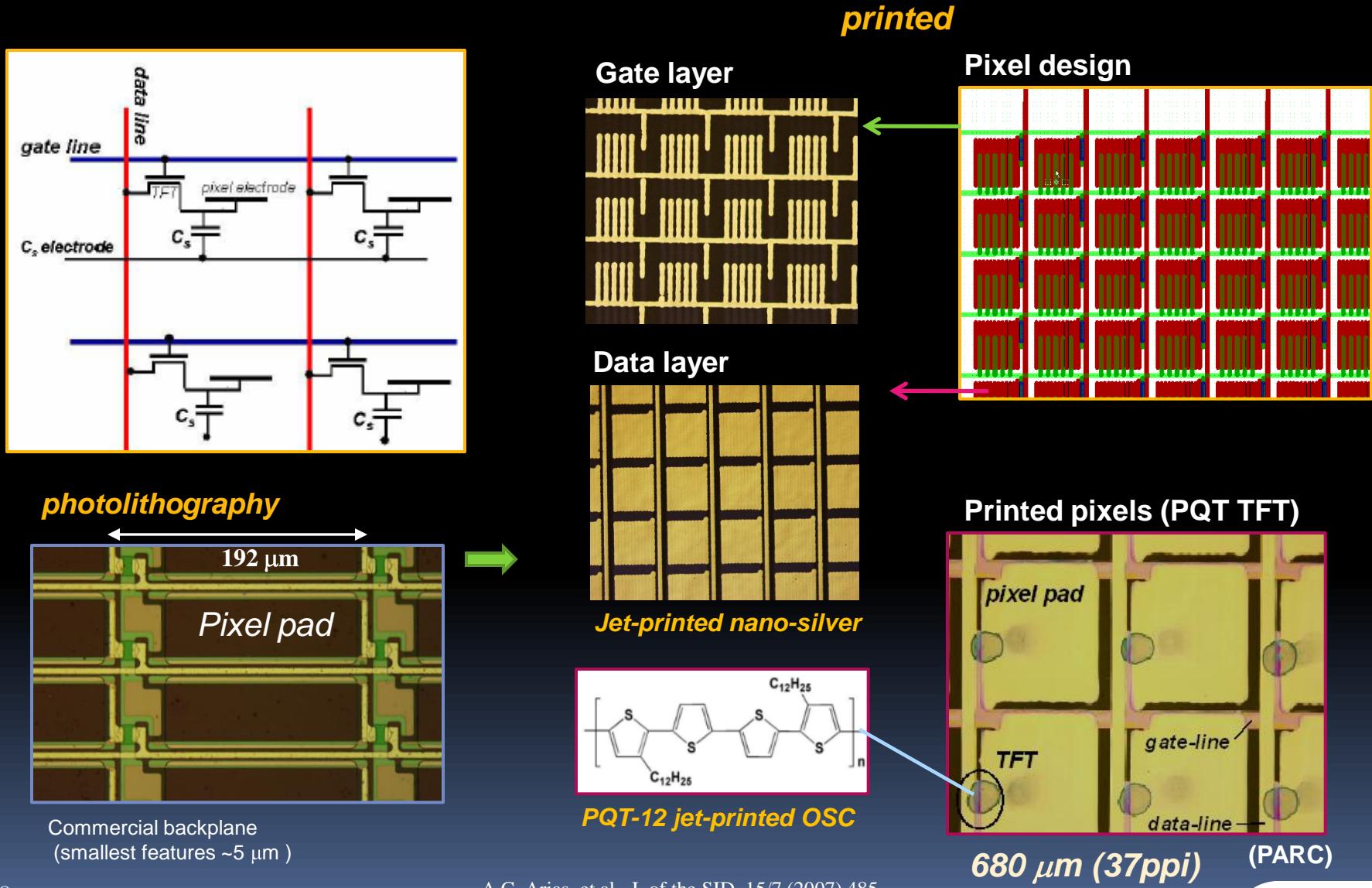
Dynamics of the "coffee stain" effect:

The evaporation rate J is larger at the edge of a droplet due to the curvature of the droplet leading to a flow of fluid and nanoparticles with a velocity v .



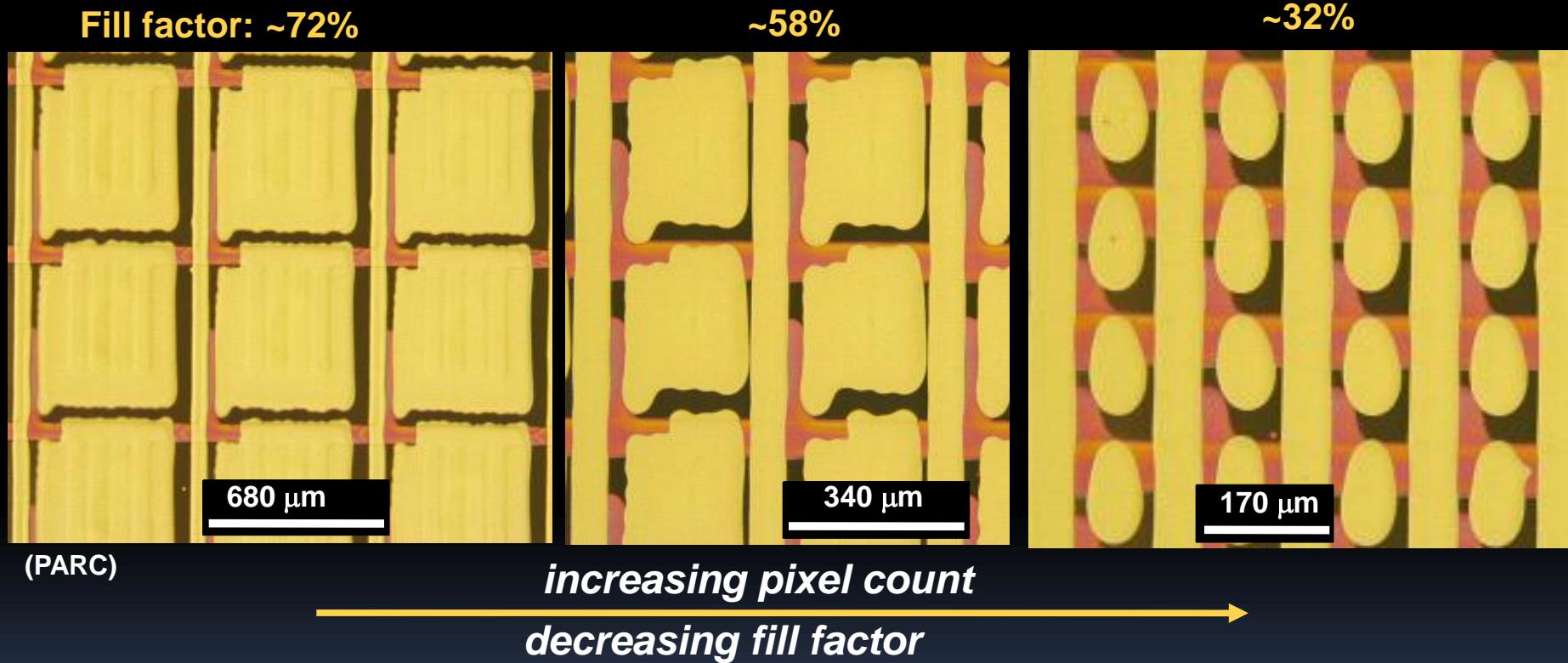
Source: U Illinois

Jet-Printed Active-Matrix Backplanes



Challenge: Higher Resolution

- *Higher display resolution requires smaller pixels*

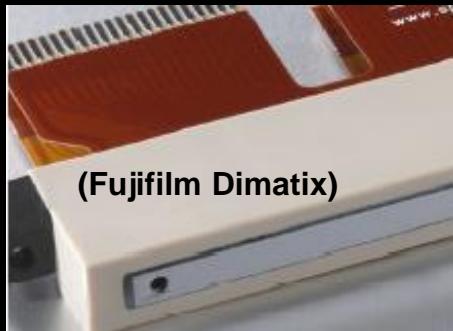


→
solutions

- improve printing process (line width / spaces)
- multi-layer pixel design

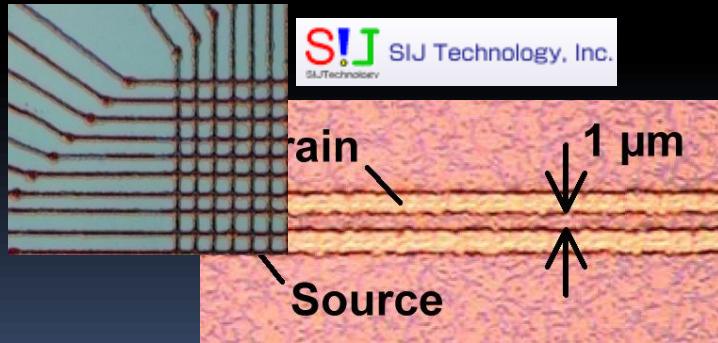
Higher Resolution

Printheads with 1pl drops for
~20 micron features



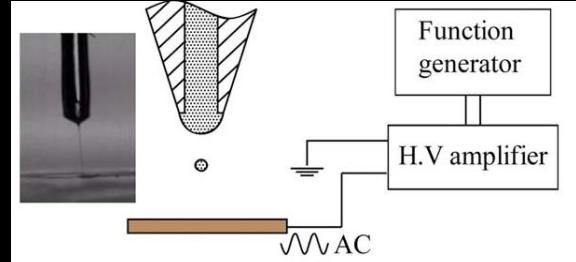
Brownian motion, electrostatics !

Superfine inkjet printing



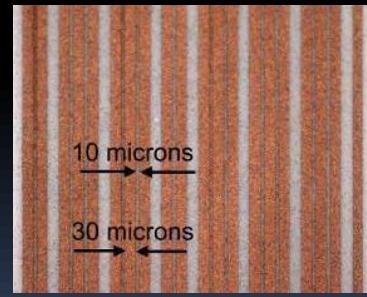
Sekitani, Proc. Nat. Acad. Sci. 105 4976 (2008)

Electrohydrodynamic printing

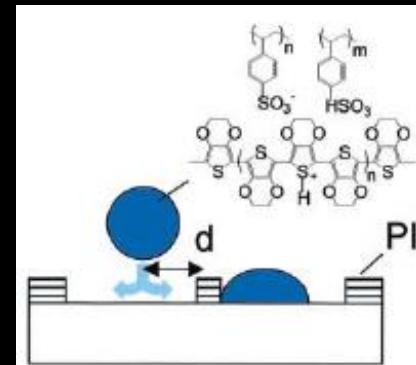


Droplets smaller than nozzle size possible

Laser assisted printing



Surface energy patterning
of substrate



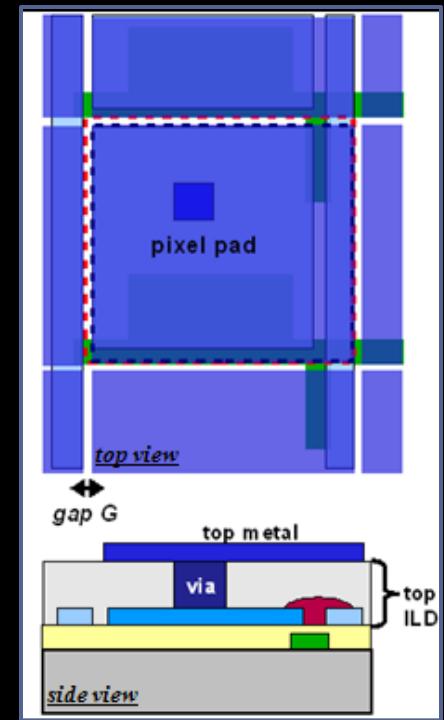
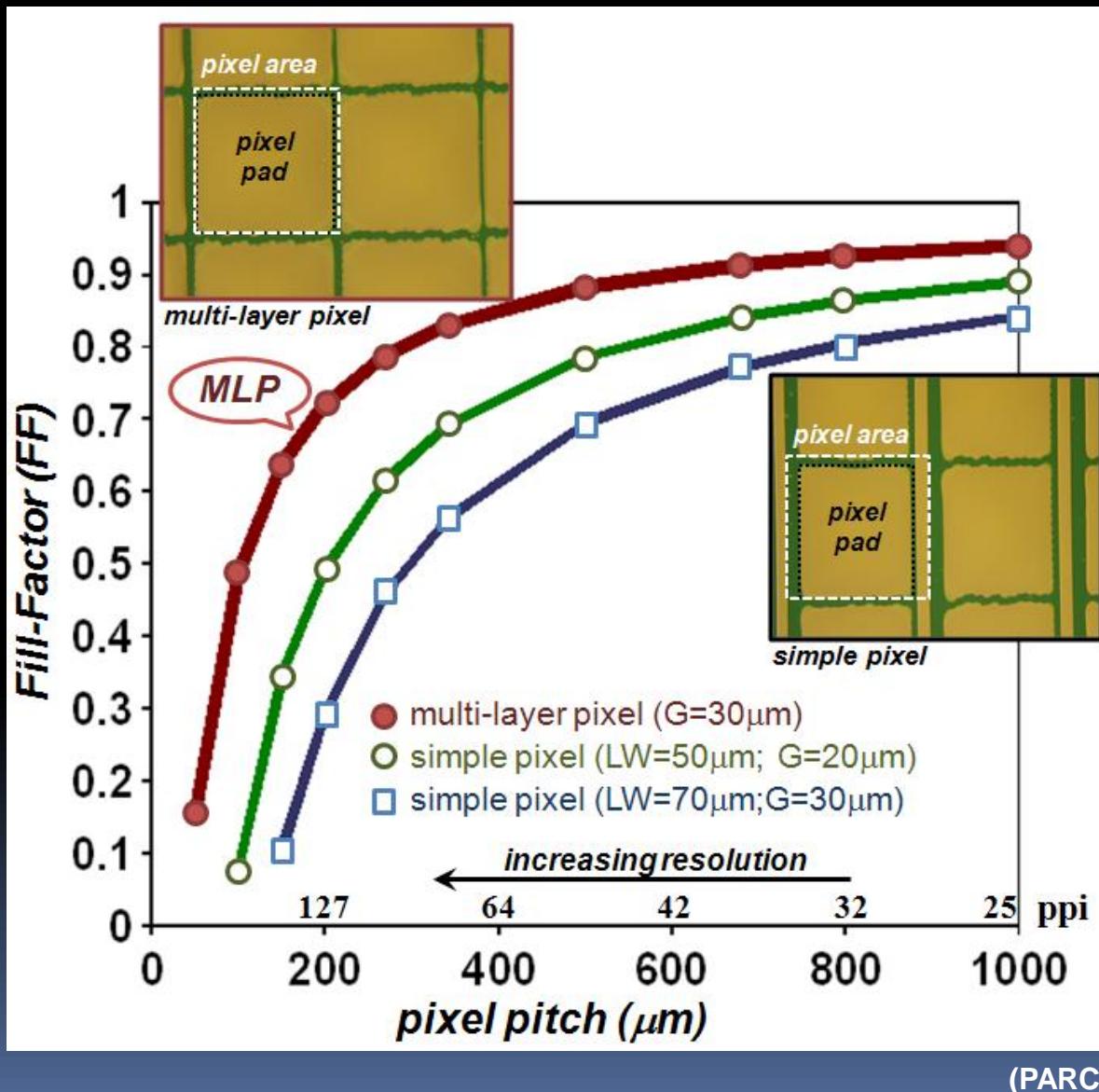
Sirringhaus, et al., Science, 290 (2000), 2123

T. Tano, Ricoh Co., NIP25, '09



Brownian motion and electrostatics are crucial at small drop sizes

Pixel Fill-Factor

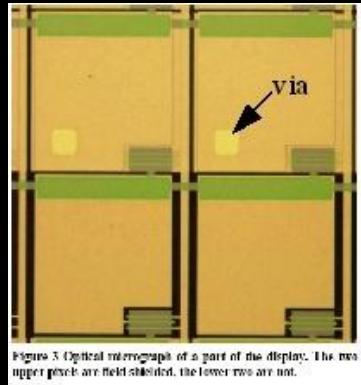


J. Daniel, et al., SID 09 Digest, 44.3, p660-663

Via Formation in Printed Electronics

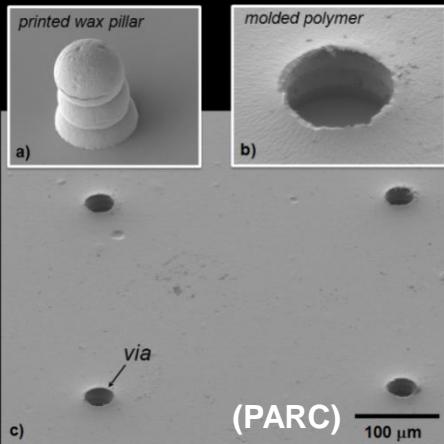
- Vias in dielectrics are essential for multi-layer pixels and for most other electronic circuits

Photolithography



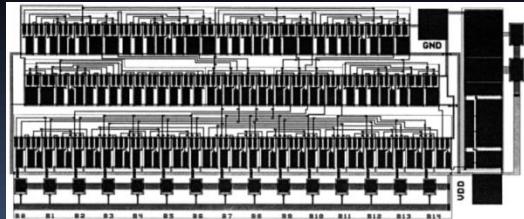
Gelinck, et al., SID 05 Digest, 3.1
(Polymer Vision / Philips)

Molding process



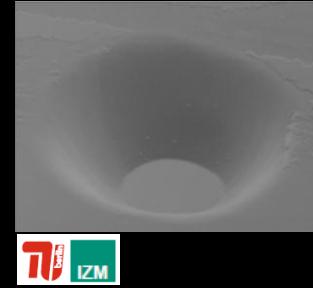
J. Daniel, et al., SID 09 Digest, 44.3, p660-663

Hole Punching

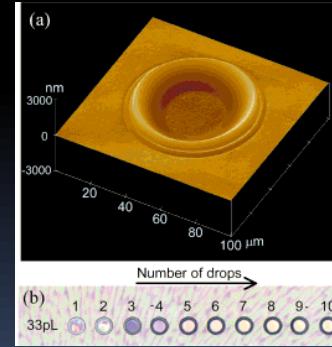


Drury, et al., Appl. Phys. Lett., 73, 1998, 108
(PhilipsRes. Lab)

Laser ablation



Solvent Printing

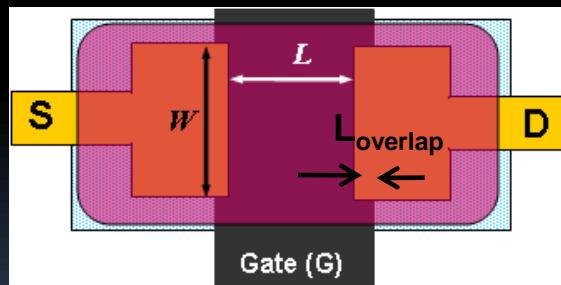
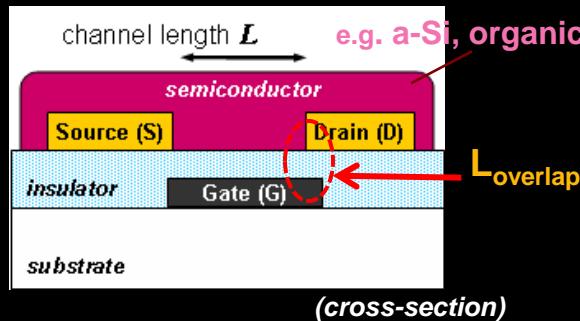
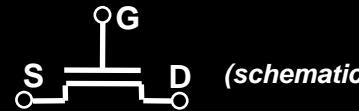


Kawase, Sirringhaus, et al.,
Adv. Mater. 2001, 13, 1601
(Seiko-Epson/ Univ. Cambr.)

Via formation process is still challenging

Overlap capacitance in Printed TFTs

- Larger print tolerances cause greater overlap capacitance



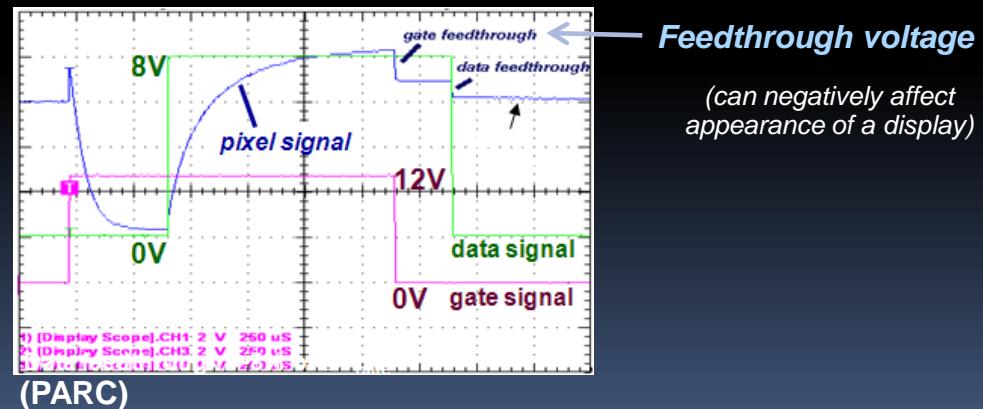
typical bottom-gate TFT

$$f = \mu \cdot (V_{gs} - V_t) / 2\pi L(L + 2L_{overlap})$$

High overlap capacitance -> **low switching frequency**

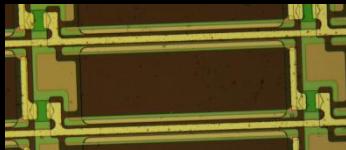
$$\Delta V_{pixel} = \Delta V_{gate} \cdot C_p / (C_p + C_{st})$$

High overlap capacitance -> **high feedthrough voltages**
(unless compensated by large storage capacitor)



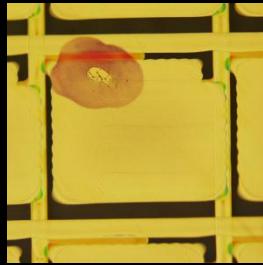
Speed of Display – video rate ?

- Conventional LCD display pixels:



190x64 μm ,
C_{st} ~0.2pF,
C_p ~0.03pF

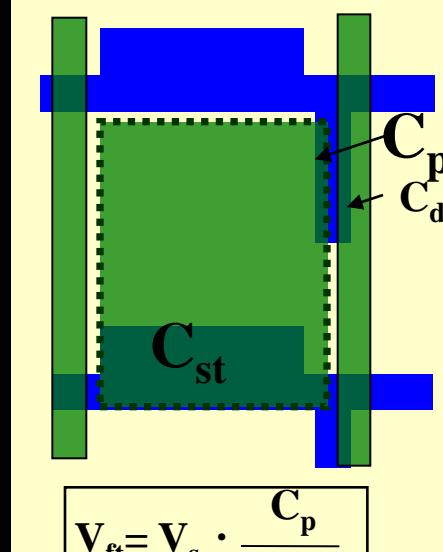
- Printed pixels:



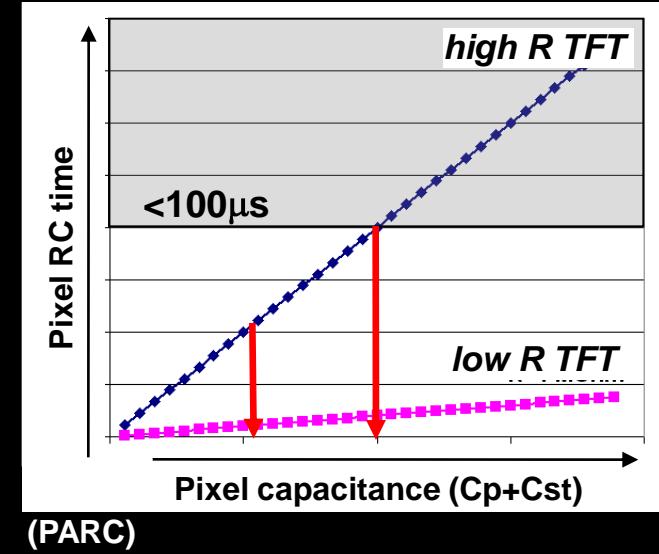
680x680 μm ,
C_{st} ~pF,
C_p ~pF

typically larger feature size !

Pixel capacitances and feedthrough voltage



Pixel charging speed



**“Fast pixel response requires
careful balancing of feedthrough voltage and pixel capacitances”**

- high mobility semiconductor is desirable (+ high W/L)
- good control over printing process to reduce parasitics
- ~ 30 fps may be sufficient for many applications
- for e-paper, faster display media is under development (E-ink, SID'05)

Applications

Printed Battery 'Energy Strip'

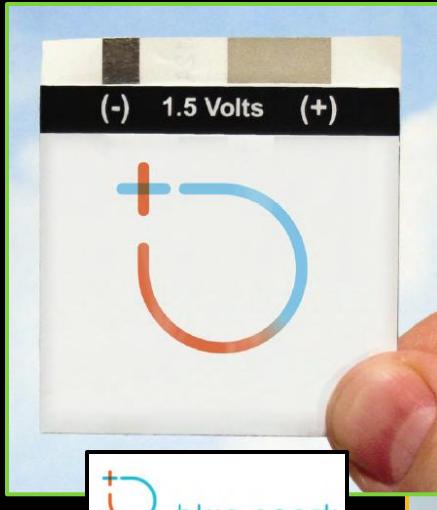


Printed

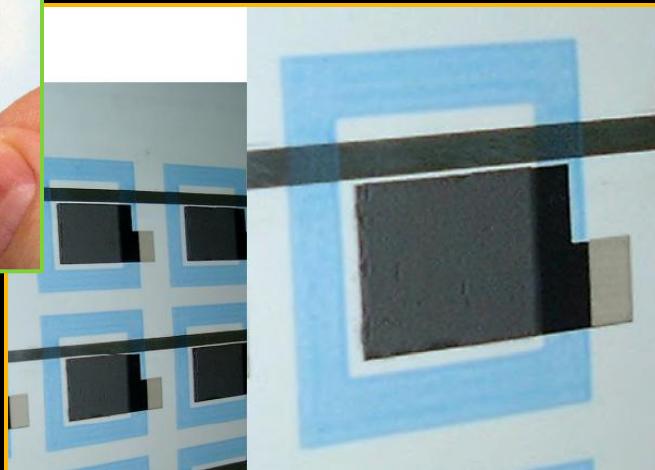
- conductor
- insulator
- thermochromic dye



Printed Battery



- 1.5V/cell carbon-zinc MnO₂ chemistry
- R2R process



potential applications:

- Battery-assisted RFID
- RF-enabled sensor systems and data loggers
- RFID smart cards and ID badges
- Medical care devices
- Cosmetic patches
- Interactive consumer goods packaging



printed Li polymer battery

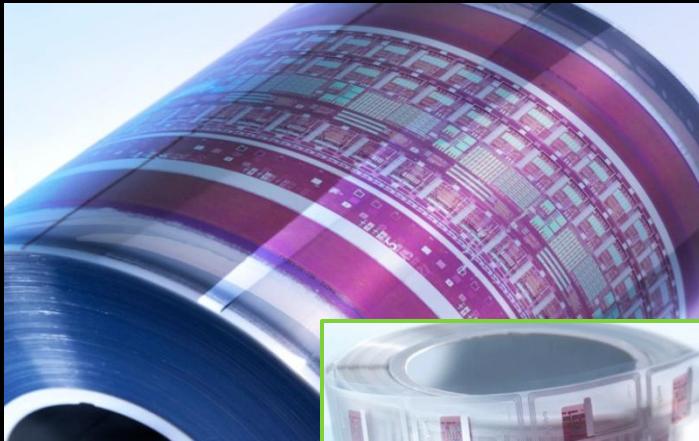


printed Zn/MnO₂
– ZnCl₂ electrolyte

Printed RFID tags



- Organic semiconductor on plastic foil

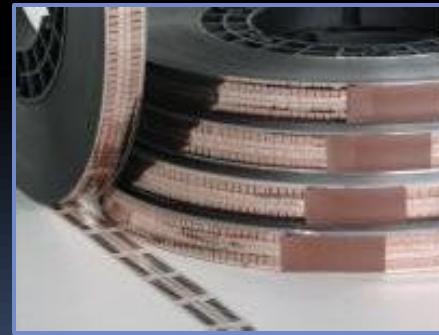
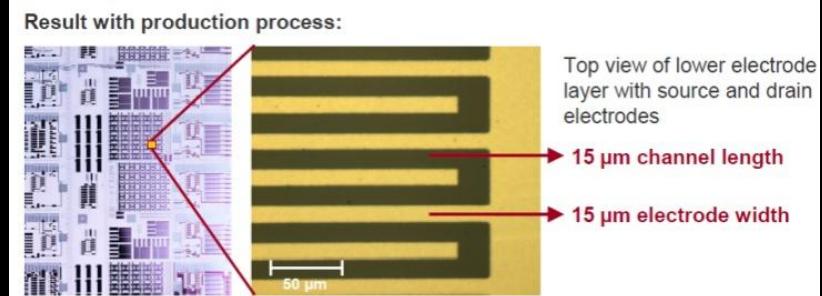


TFT mobilities <0.1 cm²/Vs

Web speed: ~30m/min

- 13.56 MHz voltage rectifier
- ~100Hz ring oscillator,
- 4 bit Manchester chip (64-bit tags in lab)
- Antenna is not printed

- Item-Level Tracking
- Smart Packaging for Healthcare Applications
- Brand Protection
- Inventory and Tracking
- Transit Cards and Smart Cards

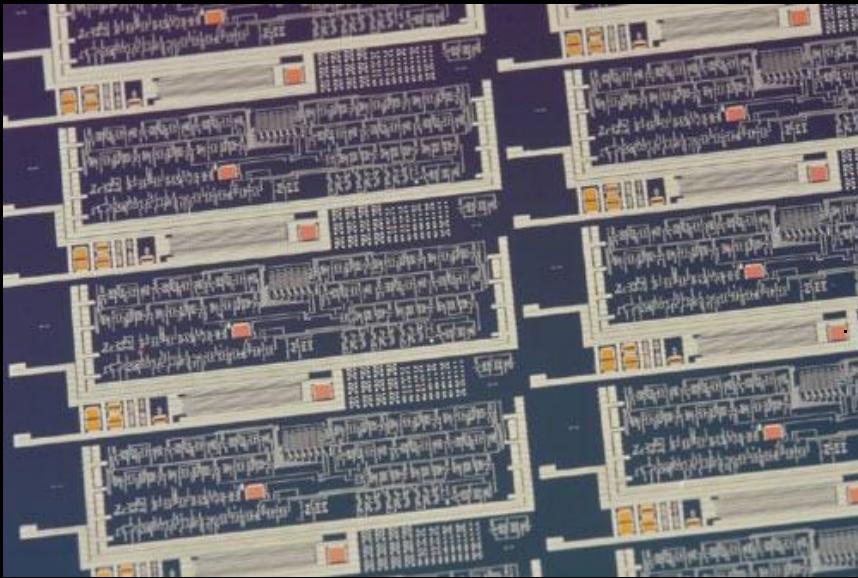


re-writable memory (for toys/games):
PolyIC and Thin Film Electronics ASA

Printed RFID tags

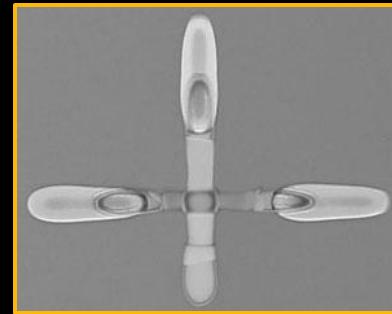


- Silicon ink semiconductor on metal substrate

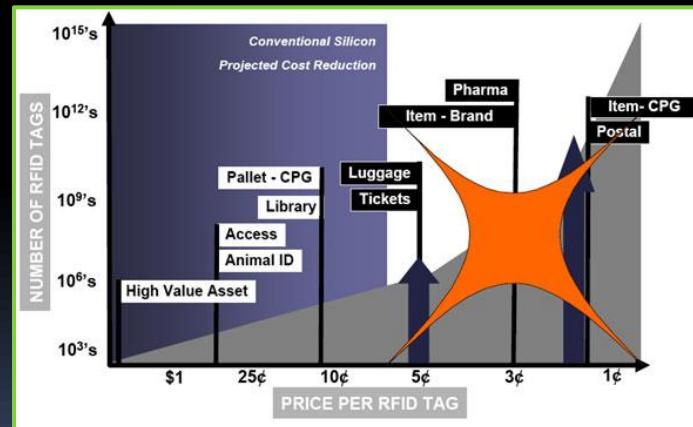


Kovio's RFID chips are printed on a 16 x 16 in. sheet of metal.

IDTechEx: market size for item-level RFID tags by 2015 will grow to 163 billion units, valued at \$5.3B. By then, the average sales price will be cents per tag.



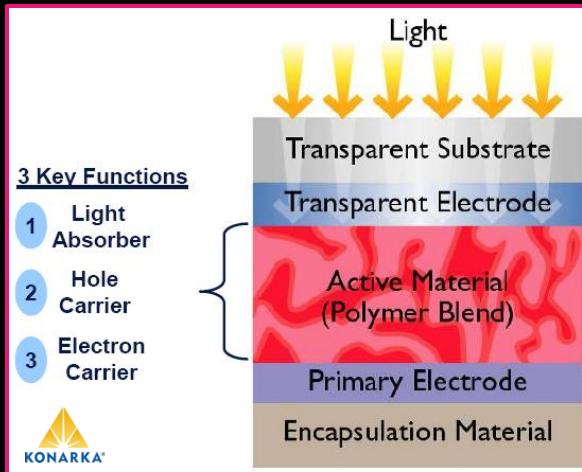
nFET thin-film transistors (TFT) with electron mobilities of $80 \text{ cm}^2/\text{Vs}$.



(source: Semiconductor International)

Printed Organic PV

Bulk Heterojunction Polymer / Fullerene



J. Hauch, OSC09

- 6.39% efficiency (0.8cm^2)
- T80 lifetime ~8000h (>3yrs)
- commercial OPV modules: ~3-5%

100feet/min; 250-1500mm width

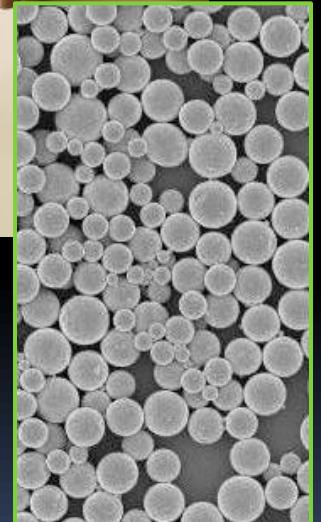
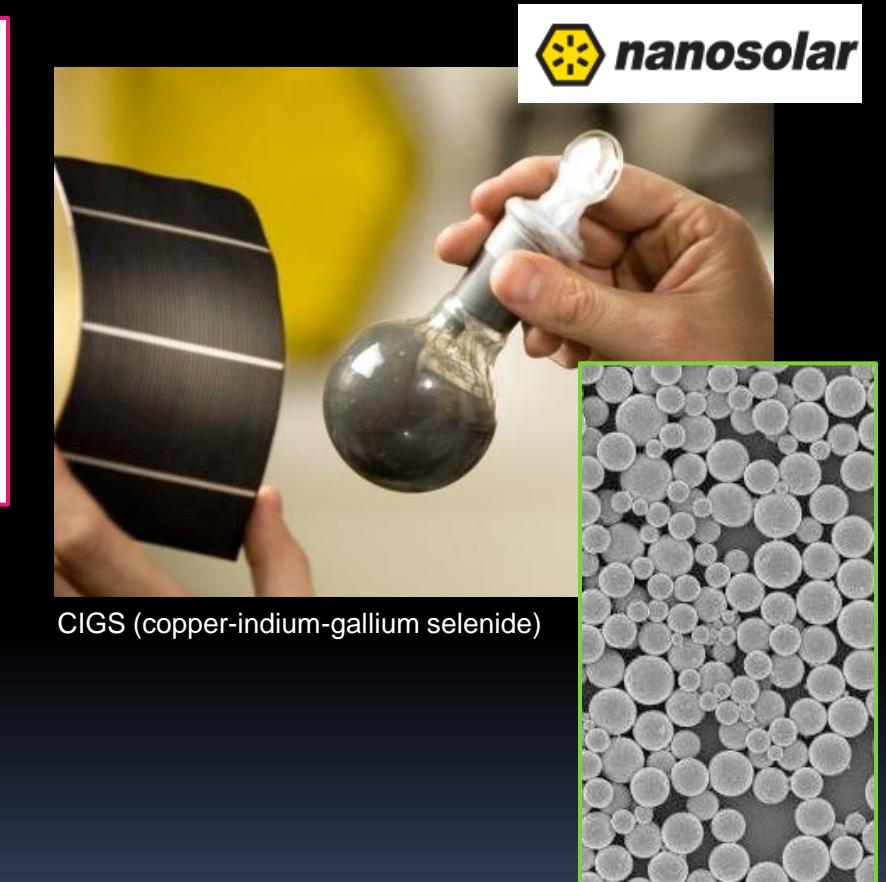
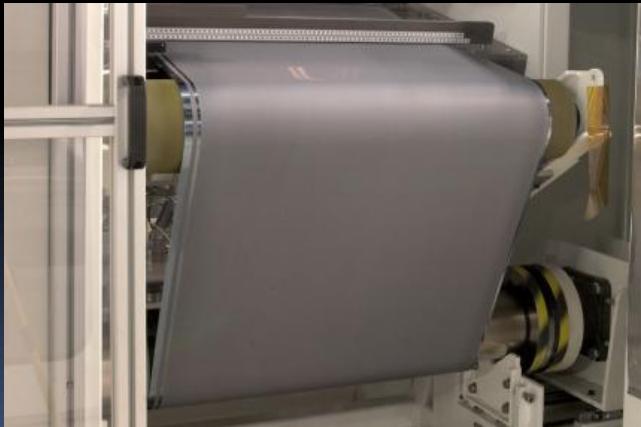
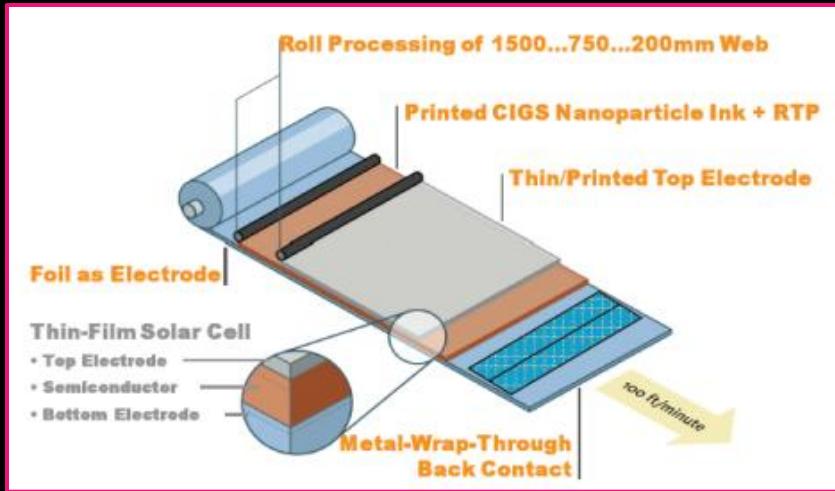


8.13% record efficiency (size ?)

Konarka/Lundberg Design

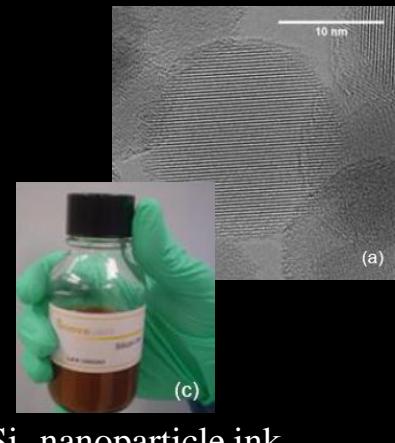
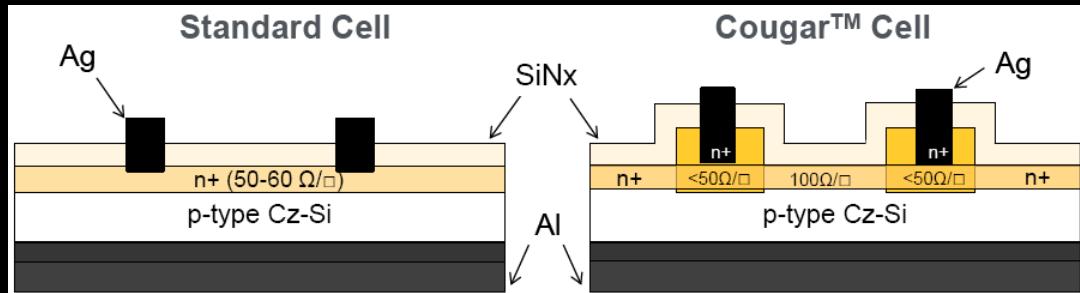
Printed Inorganic PV

- ‘Printed’ CIGS solar cell



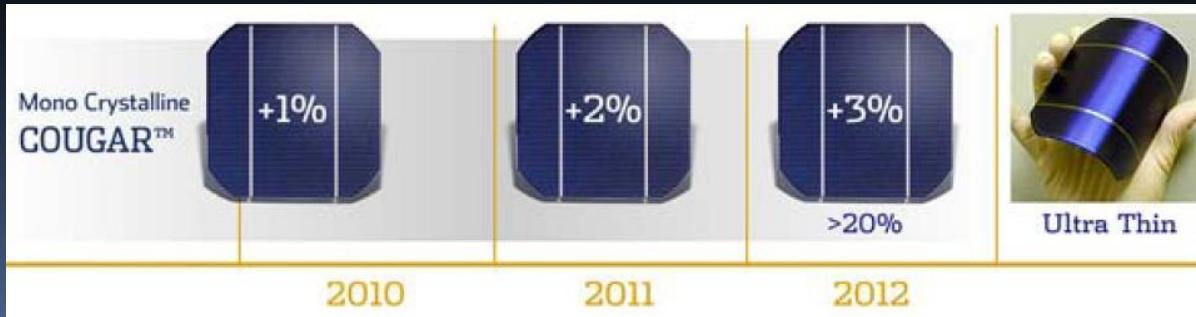
Printed Selective Emitter Process

- Innovalight



Si -nanoparticle ink

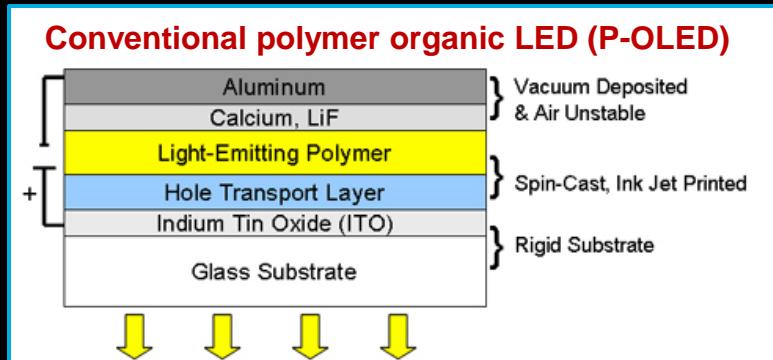
Printed n-type Silicon ink (screen or inkjet)
One step added to a conventional cell line (retrofitable)



Efficiency improvement

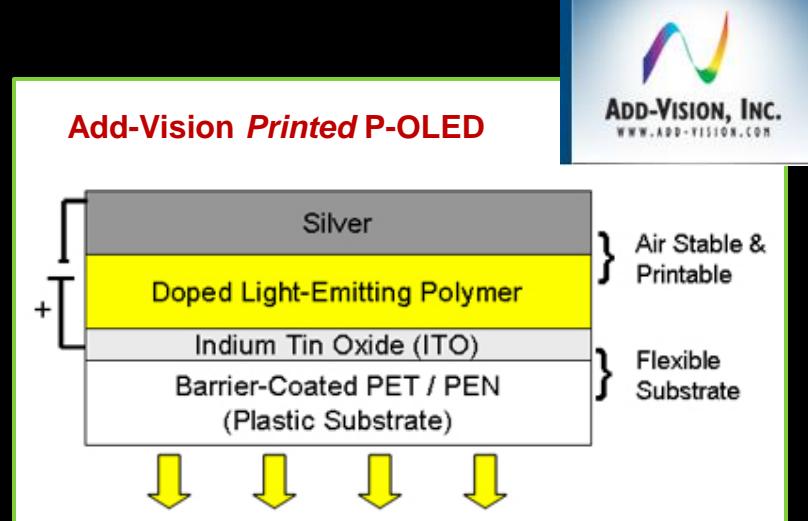
OLED Displays / Lighting

- Fully printed OLED



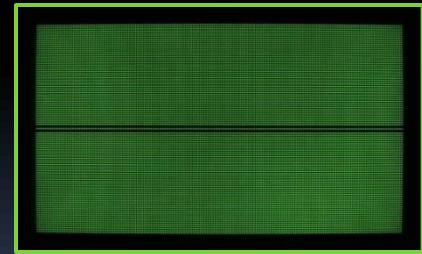
evaporated cathode material !

(Superior power efficiency brightness and lifetime)



screen printed air-stable printed cathode material

Seiko Epson, CDT, ...



panel with ink volume correction

S.Skai, et al., NIP 25, 2009 (Seiko Epson)

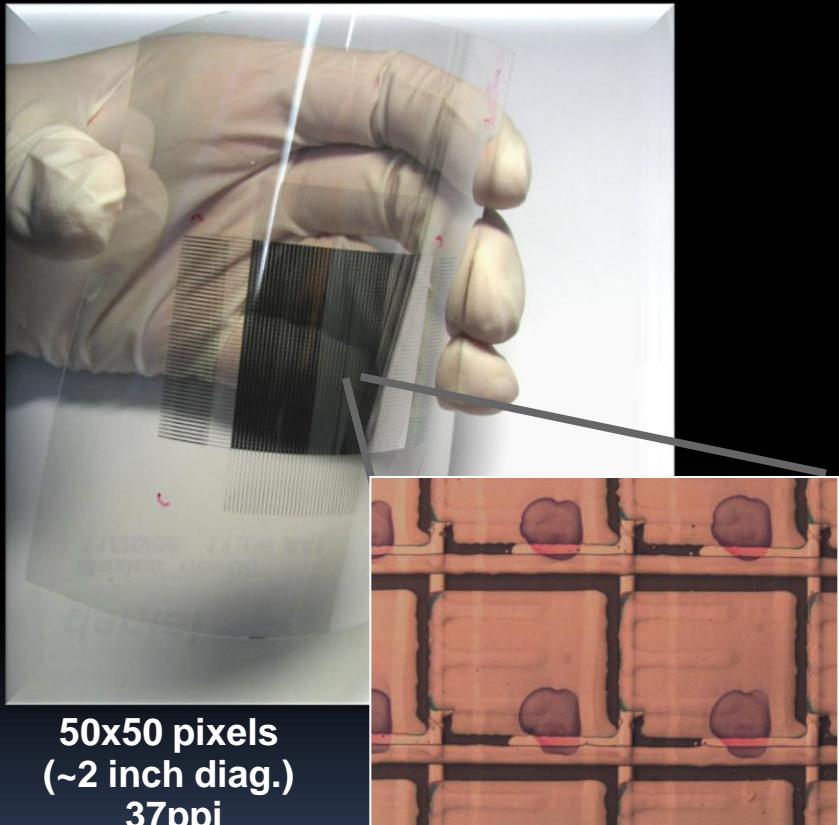
inkjet printed displays – volume averaging required
for uniformity (Mura reduction)



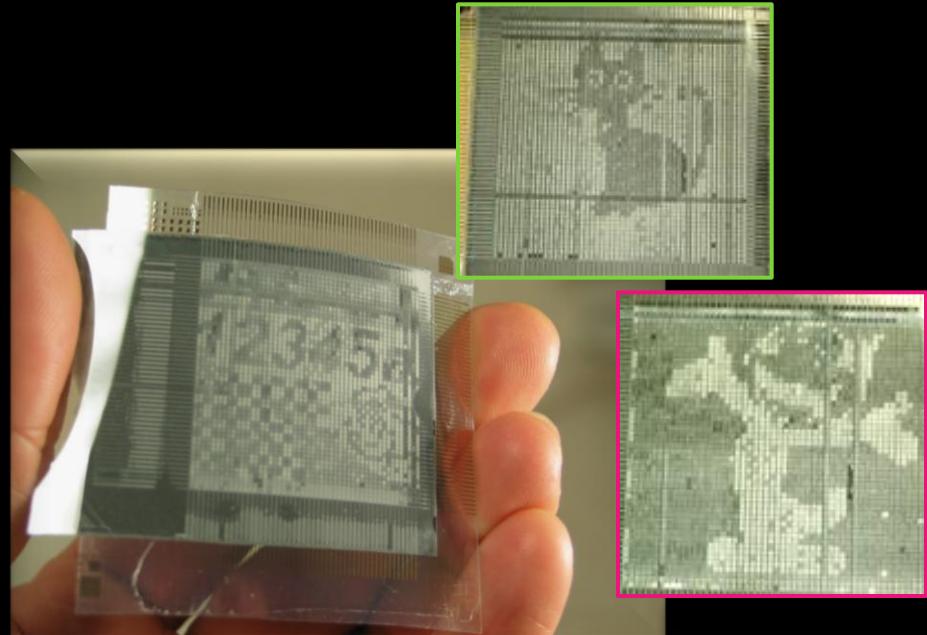
<http://www.add-vision.com/>

Flexible Displays with Printed Backplane

'active-Matrix pixel circuits for electrophoretic media (PARC)'



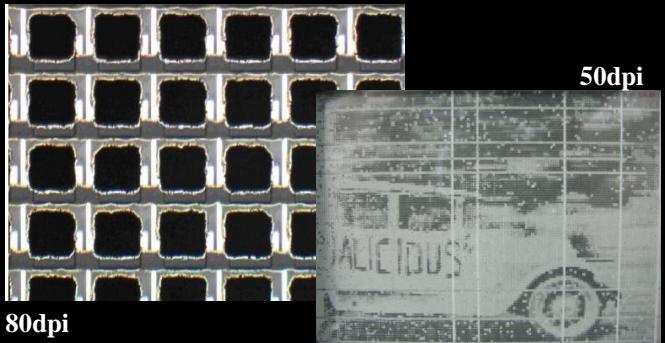
680 μm pixels; PQT-12 jet-printed
Semiconductor



- **PEN substrate**
- **PVP gate dielectric**
- **Printed Ag**
- **PQT semiconductor**

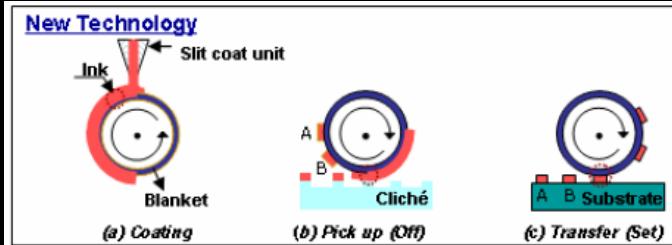
Printed Displays - further examples

Dai Nippon Printing



- screen printing+ solution processing
- pentacene OSC
- QR-power display

LG



- roll printed resist for all layers
- 15 inch display

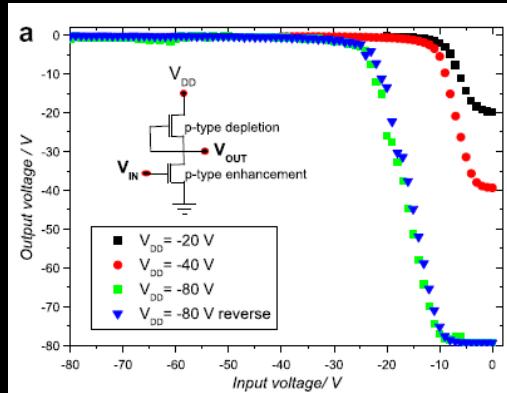


Samsung



Printed Electronics Circuits

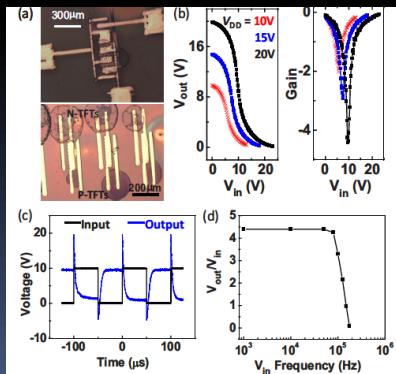
Gravure/Flexo printed inverter



(Chemnitz Univ./Johns Hopkins Univ)
K. Reuter, et al., Organic Electronics 11
(2010) 95-99

- Contact charging of Cytop dielectric for depletion/accumulation TFTs

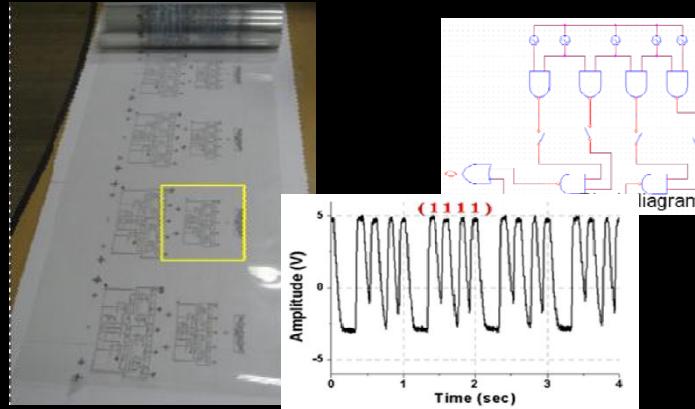
Jet printed inverter



(PARC)

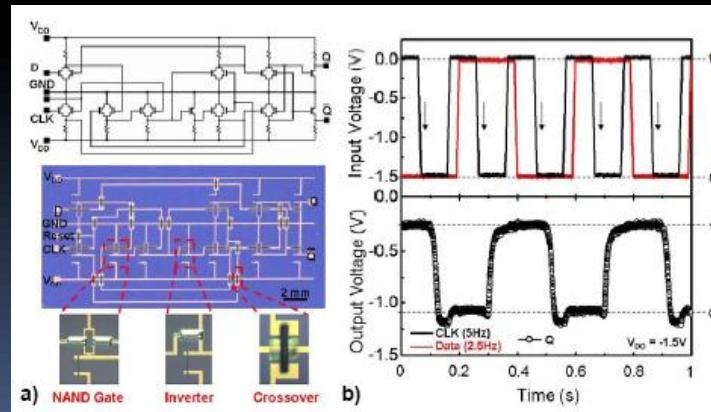
T. Ng, et al., Appl. Phys. Lett., 94, 233307 (2009)

Gravure printed 4-bit logic



(Sunchon National Univ.)
M. Jung, G. Cho, et al. ICFPE09

Printed D-Flipflop (printed ion gel gated circuit)



Y. Xia, D.Frisbie, U. Minnesota

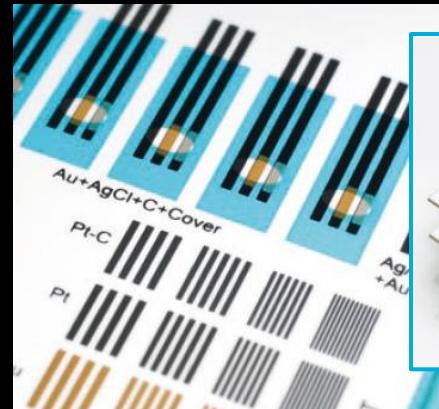
Other Printed Electronic Devices

Antennas

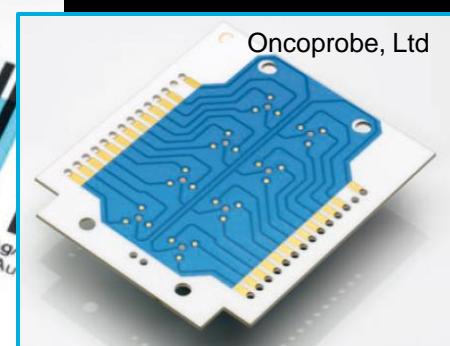


Source: Dupont

Biosensors



Printed sensors for glucose meters:
~2.2 billion sold each year



Source: Dupont

Electroluminescent Lamps



GSI technologies

Electrochromic Displays

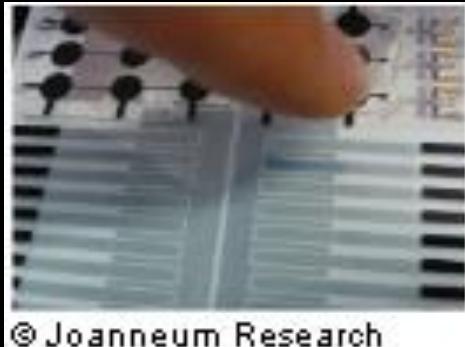


GSI technologies + NTera

Relatively simple printed devices are already on the market

Examples of Printed Sensors

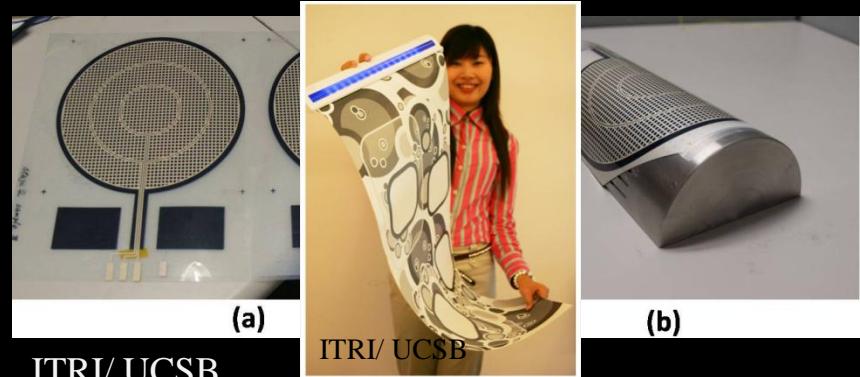
screen printed piezo/pyro sensors



© Joanneum Research

3PLAST project

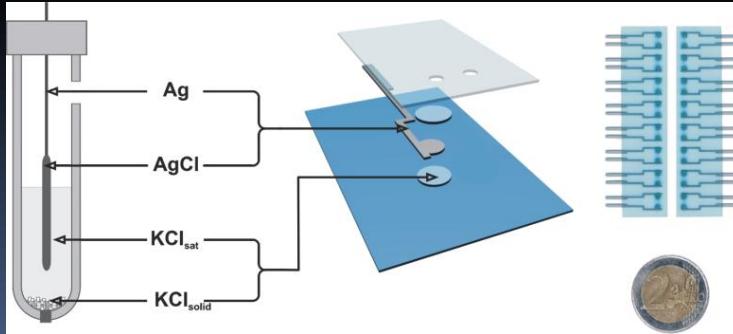
screen printed piezoresistive pressure sensor (e-Drum)



(a)
ITRI/ UCSB

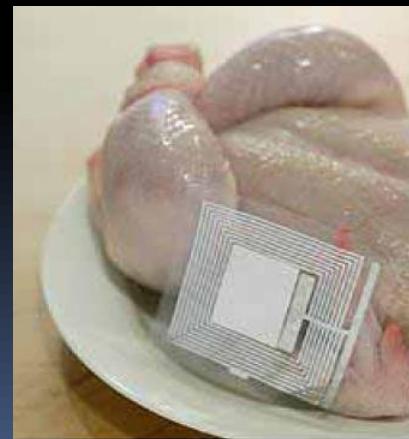
(b)
ITRI/ UC\$B

Screen printed ion-selective sensor electrodes



L. Tymecki, et al, Sensors 2006, 6, 390-396

detection of H₂S gas



VTT

Demonstrators

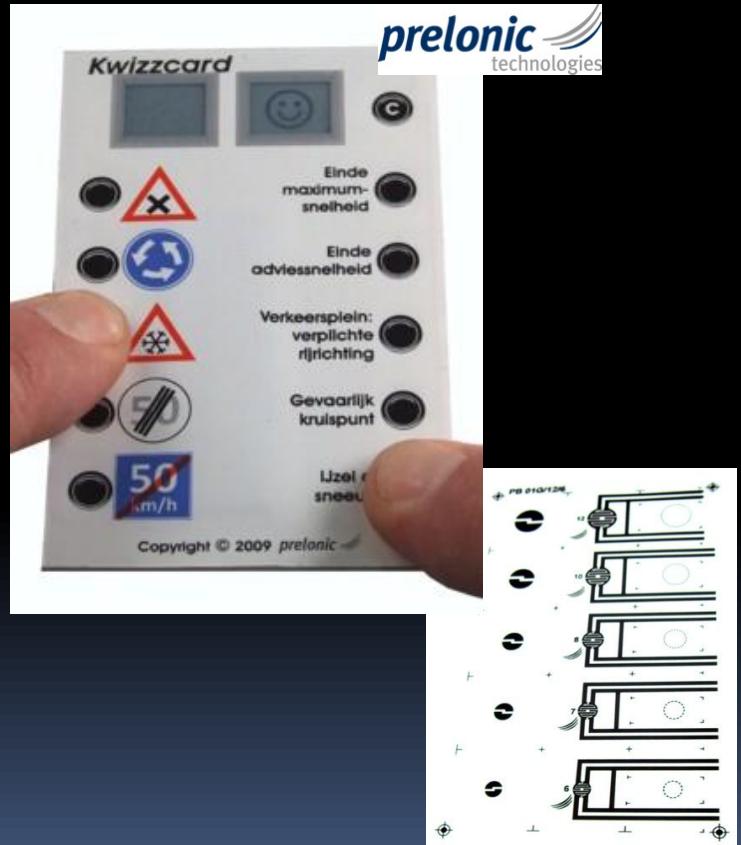
Interactive package:

Conductors, dielectrics, thermochromic ink



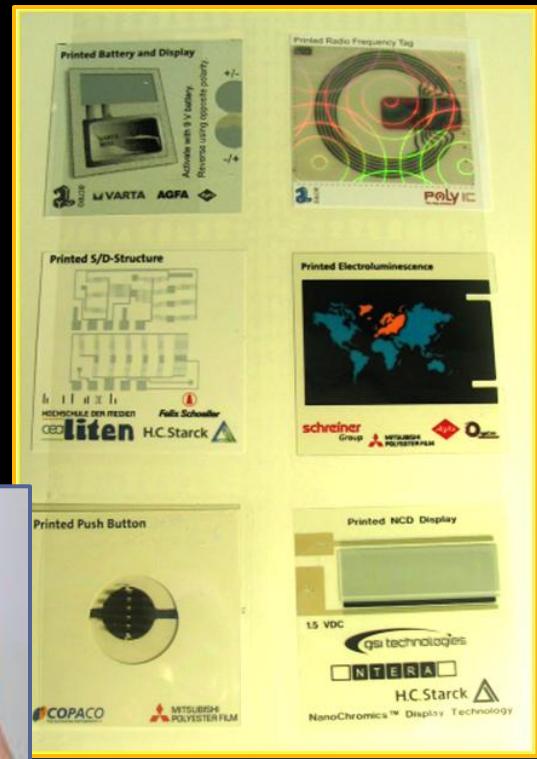
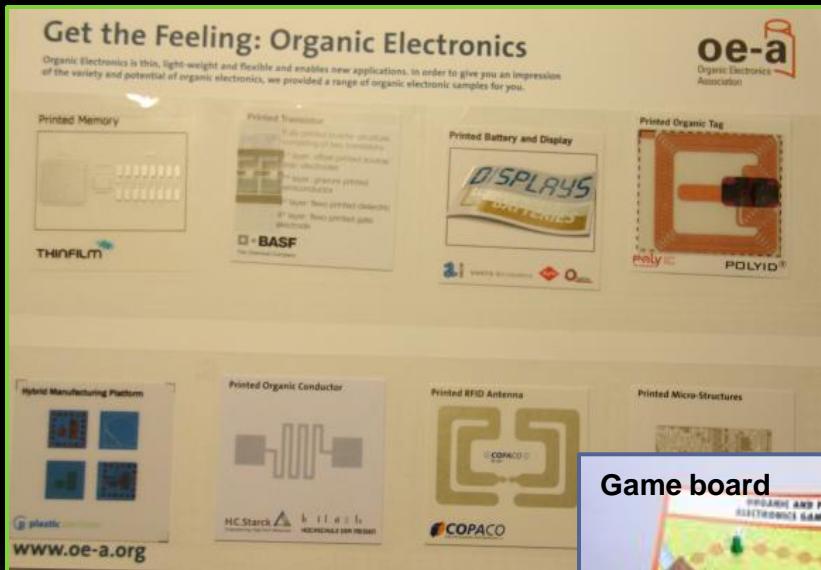
Interactive cards (gaming cards) –

batteries, conductors, push buttons



Printed Electronics - demonstrators

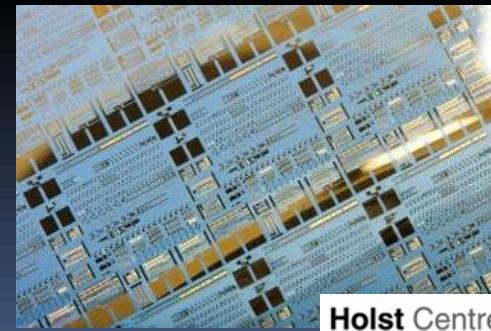
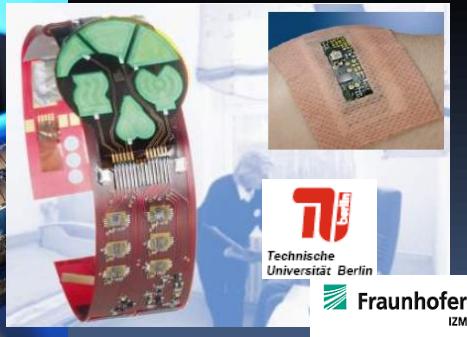
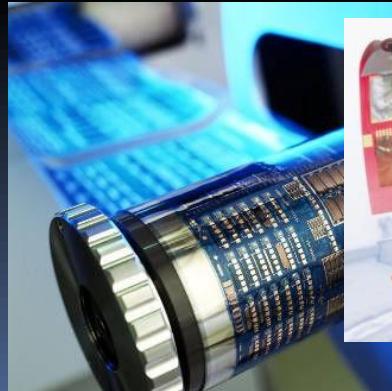
- Demonstrator kits by OE-A



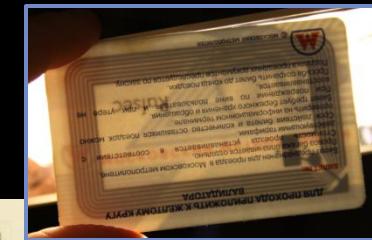
Demonstrators may enable designers to have new application ideas

Alternatives to Printed Circuits

- SAIL (HP)
- Lift-off processes (PVI)
- Hetero-integration – pick and place (Muehlbauer)
- Fluidic self-assembly (Alien Technology)
- R2R vacuum deposition and patterning
- Lithography + OSC (Holst Centre, ITRI, ...)



Holst Centre



Acknowledgment

- For work performed at PARC:
 - A.C. Arias, T. Ng, S.E Ready, R.A. Street, B. Krusor, B. Russo
 - Xerox Research Center of Canada
 - DuPont Teijin
 - NIST
 - DARPA (contr. # W81XWH-08-C-0065)

Thank you !