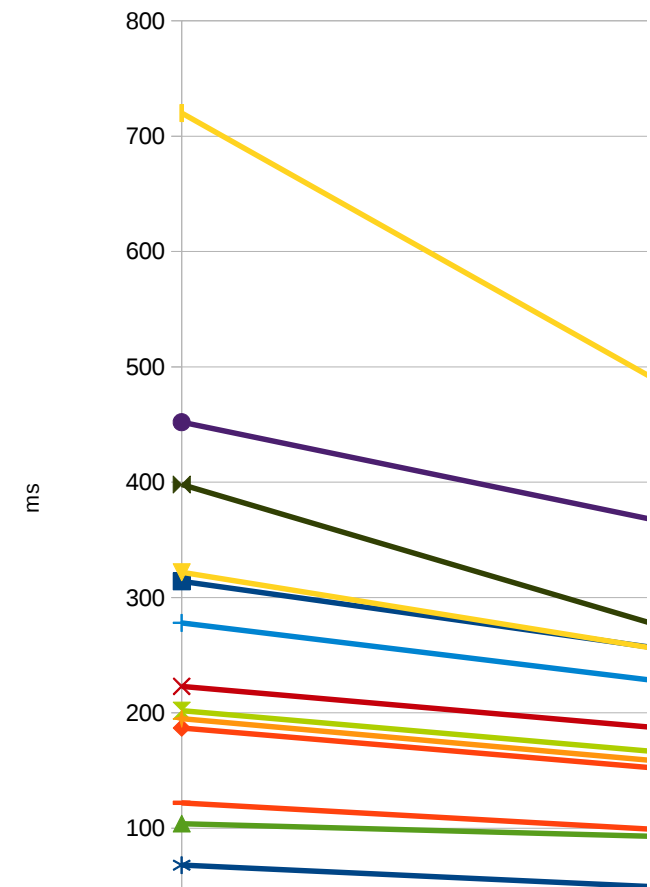


Sheet1

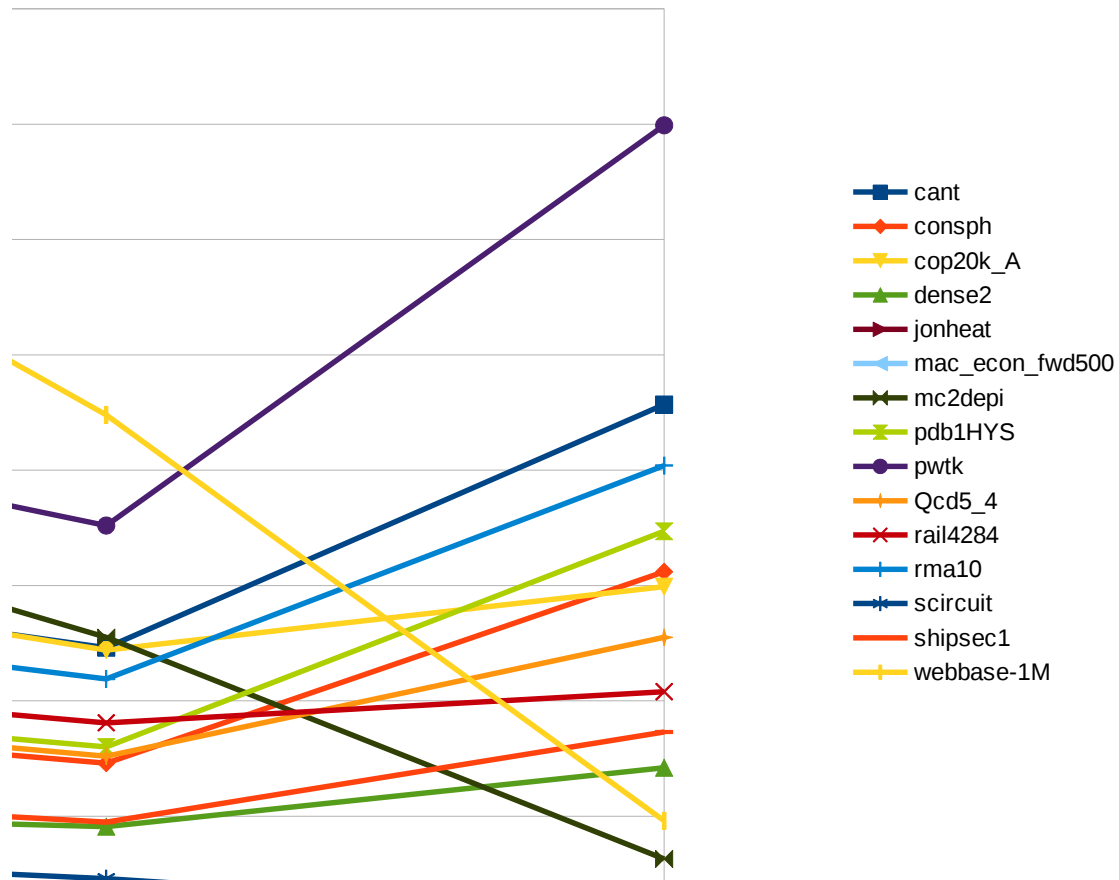
	1 thread in a wrap	2 threads in a wrap	32 threads in a wrap
cant	314	246	457
consph	187	146	312
cop20k_A	322	244	299
dense2	104	91	142
jonheat	6	4	3.9
mac_econ_fwd500	35	24	8
mc2depi	398	255	63
pdb1HYS	202	160	347
pwtk	452	352	699
Qcd5_4	195	152	255
rail4284	223	181	208
rma10	278	219	404
scircuit	68	46	15
shipsec1	122	95	173
webbase-1M	720	448	96

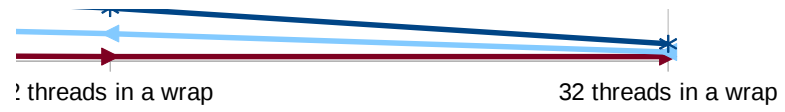


Sheet1



Naive GPU implementation

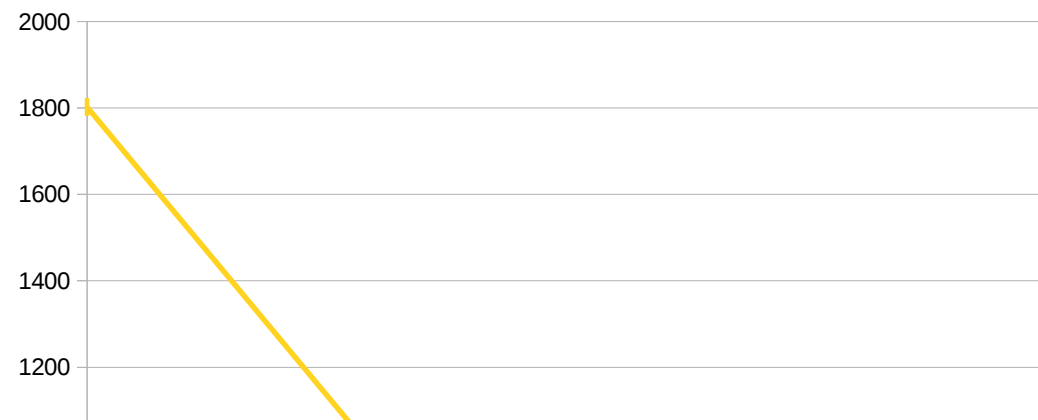


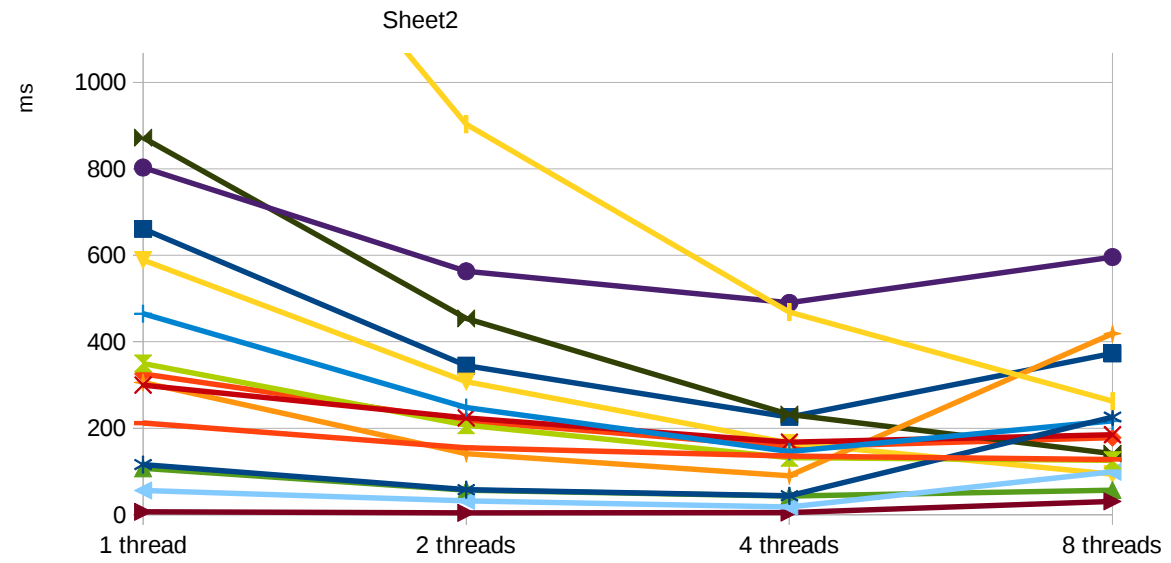










Sheet2

	1 thread	2 threads	4 threads	8 threads	
cant	661	345	226	373	
consph	326	217	157	178	
cop20k_A	589	308	166	94	
dense2	107	57	43	57	
jonheat	7	4.3	5	31	
mac_econ_fw	56	31.9	18	99	
mc2depi	872	454	232	141	
pdb1HYS	350	207	132	126	
pwtk	803	563	490	596	
Qcd5_4	306	141	90	419	
rail4284	300	224	168	185	
rma10	465	248	146	217	
scircuit	116	58	44	226	
shipsec1	212	155	136	128	
webbase-1M	1802	903	469	263	

CPU implementation





 cant
 consph
 cop20k_A
 dense2
 jonheat
 mac_econ_fwd500
 mc2depi
 ndh1HYS

