

# Programming assignment 3 – Shared Memory

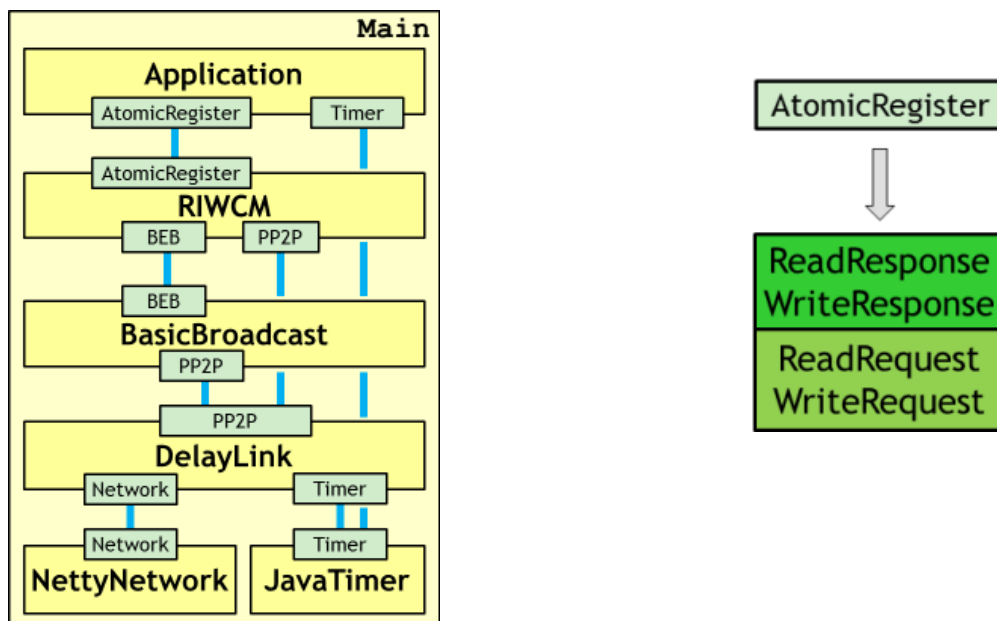
## Introduction

In this programming assignment you shall implement the Read-Impose Write-Consult-Majority component that provides the Atomic Register service. You shall use the algorithm from the textbook (algorithm 4.10 and 4.11). The algorithm is also reproduced at the end of this document.

## Installation

Download id2203-ass3-shm.zip from the course website, unpack and import into Eclipse in the same way as for previous assignments.

## Architecture



The AtomicRegister port is defined in `se.kth.ict.id2203.ports.ar`.

## Code to write

The component shall be implemented in the `ReadImposeWriteConsultMajority.java` file in the `se.kth.ict.id2203.components.riwcm` package. You will have to add files for internal events as you see fit.

## Exploration

You shall do the following:

- There are a number of scenarios in `Executor.java`; run each of them and make sure that you understand how/why it works. Write down a valid linearization for each execution.
  - If too many log messages makes it hard to see the relevant ones, remember that you can suppress log messages (typically uncomment line 18 of `log4j.properties`).

- Play around with executing reads and writes as you like (either interactively using the `R` and `Wn` commands, or as a scenario).
  - You can change the topology (number of processes and link delays) in `Executor.java`, and also add normal distributed variance to the link delay by changing the `sigma` parameter (currently set to 0 ms) at line 72 of `Main.java`.

### Automatic correction

When everything is working you run the `AutomaticCorrection.java` file to test the component and submit the assignment to the <http://cloud7.sics.se:11700/> server. Remember to change the email and password strings before running.

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**Algorithm 1** Read-Impose Write-Consult-Majority (part 1, read and consult)

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**Implements:**AtomicRegister, **instance** *nnar*.**Uses:**BestEffortBroadcast, **instance** *beb*;PerfectPointToPointLink, **instance** *pp2p*.

```
1: upon event  $\langle nnar, Init \rangle$  do
2:    $(ts, wr, val) := (0, 0, 0)$ ;
3:    $acks := 0$ ;
4:    $writeval := \perp$ ;
5:    $rid := 0$ ;
6:    $readlist := [\perp]^N$ ;
7:    $readval := \perp$ ;
8:    $reading := \text{FALSE}$ ;

9: upon event  $\langle nnar, Read \rangle$  do
10:   $rid := rid + 1$ ;
11:   $acks := 0$ ;
12:   $readlist := [\perp]^N$ ;
13:   $reading := \text{TRUE}$ ;
14:  trigger  $\langle beb, Broadcast \mid [\text{READ}, rid] \rangle$ ;

15: upon event  $\langle beb, Deliver \mid p, [\text{READ}, r] \rangle$  do
16:  trigger  $\langle pp2p, Send \mid p, [\text{VALUE}, r, ts, wr, val] \rangle$ ;

17: upon event  $\langle pp2p, Deliver \mid q, [\text{VALUE}, r, ts', wr', v'] \rangle$  such that  $r = rid$  do
18:   $readlist[q] := (ts', wr', v')$ ;
19:  if  $\#(readlist) > N/2$  then
20:     $(maxts, rr, readval) := \text{highest}(readlist)$ ;
21:     $readlist := [\perp]^N$ ;
22:    if  $reading = \text{TRUE}$  then
23:      trigger  $\langle beb, Broadcast \mid [\text{WRITE}, rid, maxts, rr, readval] \rangle$ ;
24:    else
25:      trigger  $\langle beb, Broadcast \mid [\text{WRITE}, rid, maxts + 1, rank(self), writeval] \rangle$ ;
```

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**Algorithm 2** Read-Impose Write-Consult-Majority (part 2, write and write-back)

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```
26: upon event  $\langle nnar, Write \mid v \rangle$  do
27:    $rid := rid + 1$ ;
28:    $writeval := v$ ;
29:    $acks := 0$ ;
30:    $readlist := [\perp]^N$ ;
31:   trigger  $\langle beb, Broadcast \mid [READ, rid] \rangle$ ;

32: upon event  $\langle beb, Deliver \mid p, [WRITE, r, ts', wr', v'] \rangle$  do
33:   if  $(ts', wr')$  is larger than  $(ts, wr)$  then
34:      $(ts, wr, val) := (ts', wr', v')$ ;
35:   trigger  $\langle pp2p, Send \mid p, [ACK, r] \rangle$ ;

36: upon event  $\langle pp2p, Deliver \mid q, [ACK, r] \rangle$  such that  $r = rid$  do
37:    $acks := acks + 1$ ;
38:   if  $acks > N/2$  then
39:      $acks := 0$ ;
40:     if  $reading = \text{TRUE}$  then
41:        $reading := \text{FALSE}$ ;
42:       trigger  $\langle nnar, ReadReturn \mid readval \rangle$ ;
43:     else
44:       trigger  $\langle nnar, WriteReturn \rangle$ ;
```

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