

Virtio on early kernels Nahim 'Naam'

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Solution

Discovery

Virtio internals

147.1

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Virtio on early kernels How does it work & implementation

Nahim 'Naam' El Atmani

EPITA

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Problem

Solution

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Virtio internal

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Conclusion

Problem



What you have: basic lvl1 weapon

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Problem

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With

Benchmarks





What you want: WaRI0rDz m4ch1ne

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Renchmark

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Conclusion

Solution: Virtio



Time comparaison

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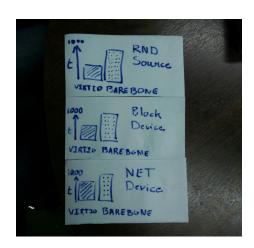
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Definition

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libvirt wiki's

Virtio is a virtualization standard for network and disk device drivers where just the guest's device driver "knows" it is running in a virtual environment, and cooperates with the hypervisor.



Context

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Great variety of solutions for doing virtualization under Linux:

- KVM (Kernel-based Virtual Machine, kvm.ko)
- QEMU (emulator with device virtualization)
- LXC (OS level)
- Iguest (paravirtualization, Ig.ko)
- **.** . . .

Idea

- Each of these solutions may virtualize devices
- "OS is taxed on the base of their independent needs"



Another layer of abstraction?

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HOW STANDARDS PROLIFERATE: (SEE: A/C CHARGERS, CHARACTER ENCODINGS, INSTANT MESSAGING, ETC.)

SITUATION: THERE ARE 14 COMPETING STANDARDS.

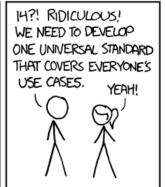




Figure: xkcd 927



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What does Virtio provide?

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- Common front end
- Code reuse

How

One wants to implement generic front end guest drivers for the Virtio's API.



Handled devices

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Subsystem device ID	Virtio Device	Specification	
1	network card	Appendix C	
2	block device	Appendix D	
3	console	Appendix E	
4	entropy source	Appendix F	
5	memory ballooning	Appendix G	
6	ioMemory	-	
7	rpmsg	Appendix H	
8	SCSI Host	Appendix I	
9	9P transport	Appendix J	
10	mac80211 wlan	-	
11	rproc serial	-	
12	virtio CAIF	-	



Exemple with STOS

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STOS Support	Before	After
Block device	X	~
Network	X	~
Console	v	V
Random source	X	~
Memory ballooning	х	X



You said paravirtualization?

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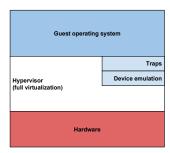
With

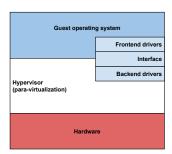
Benchmark

Conclusion

Virtio provide a way to standardize the communication interface between:

- hypervisor: backend drivers / emulated devices
- guest: front end drivers







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Architecture (1/2)

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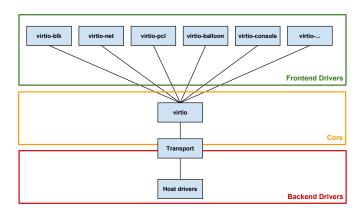
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Architecture (2/2)

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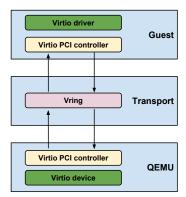
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Buffers

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Communication between host and guest are made through Virtio ring buffers (vrings).

- Predefined number of buffers
- Some are optionnals, others are mandatory

Usage of optional buffers are subject to the rules of common sense.



Configuration

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Can be summarized as:

- 1 R/W features bits
- **2** R/W the configuration space
- **3** R/W status bits
- 4 Reset



Feature ack

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There is a kind of handshake phase used to sync host and guest on used/available features. The host must now what feature bits are acked by the guest.

Host	Λ	Guest	
VIRTIO_NET_F_CSUM	~	VIRTIO_NET_F_GUEST_CSUM	
VIRTIO_NET_F_HOST_UFO	X	0	
VIRTIO_NET_F_HOST_TSO4	~	VIRTIO_NET_F_GUEST_TSO4	
	?		



Configuration space

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Properties:

- device specific-information
- R/W acces from guest

VIRTIO_NET_F_MAC feature bit specify if the host have a particular MAC address for the guest. The address is specified in this configuration space.

Linux example



Status bit

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Represent internal state of the device probe.

./include/uapi/linux/virtio_config.h

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virtio_config.h

```
/* We have seen device and processed generic fields */
#define VIRTIO_CONFIG_S_ACKNOWLEDGE 1
/* We have found a driver for the device. */
#define VIRTIO_CONFIG_S_DRIVER 2
/* Driver has used its parts of the config, and is happy */
#define VIRTIO_CONFIG_S_DRIVER_OK 4
/* Driver has finished configuring features */
#define VIRTIO_CONFIG_S_FEATURES_OK 8
```



Reset

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Does what you think it does:

- reset configuration
- reset status bit
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Needed by modular drivers.



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Prerequistes

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■ Work with not so dumb kernel

- Get some libs
- Review your roadmap
- PCI devices support



Guidelines

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■ Cut the specs

■ Organize your work

Subsystem device ID	Virtio Device	Order	Specification
1	network card	4	Appendix C
2	block device	3	Appendix D
3	console	2	Appendix E
4	entropy source	1	Appendix F



Difficulties

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- Cut does not means dumb cut
- Framework constraints
- Arch dependent features



Furtherwork

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- virtio-gpu / vga & Co
- virtio-input





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Network

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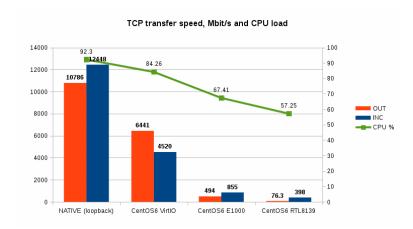
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Hard disk drive

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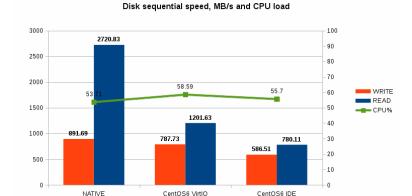
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TL;DR

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- Best of the full and para virtualization
- Dev boost
- Cool features



References

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VC-st. to see a

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Here is some links used for this talk:

- 1 Virtio: An I/O virtualization framework
- Virtio: Towards a De-Facto Standard For Virtual I/O Devices
- 3 Benefits of paravirtualization
- 4 Network driver example
- **5** Benchmarks
- 6 STOS kernel