
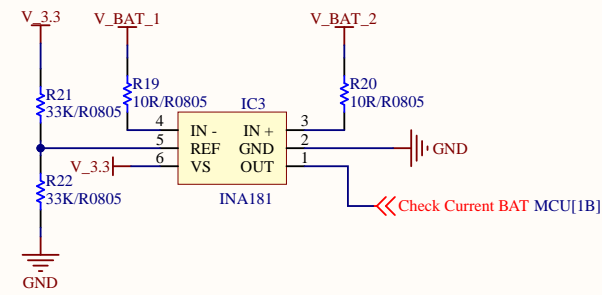
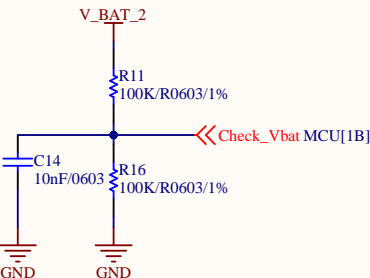
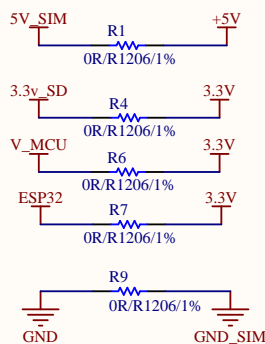

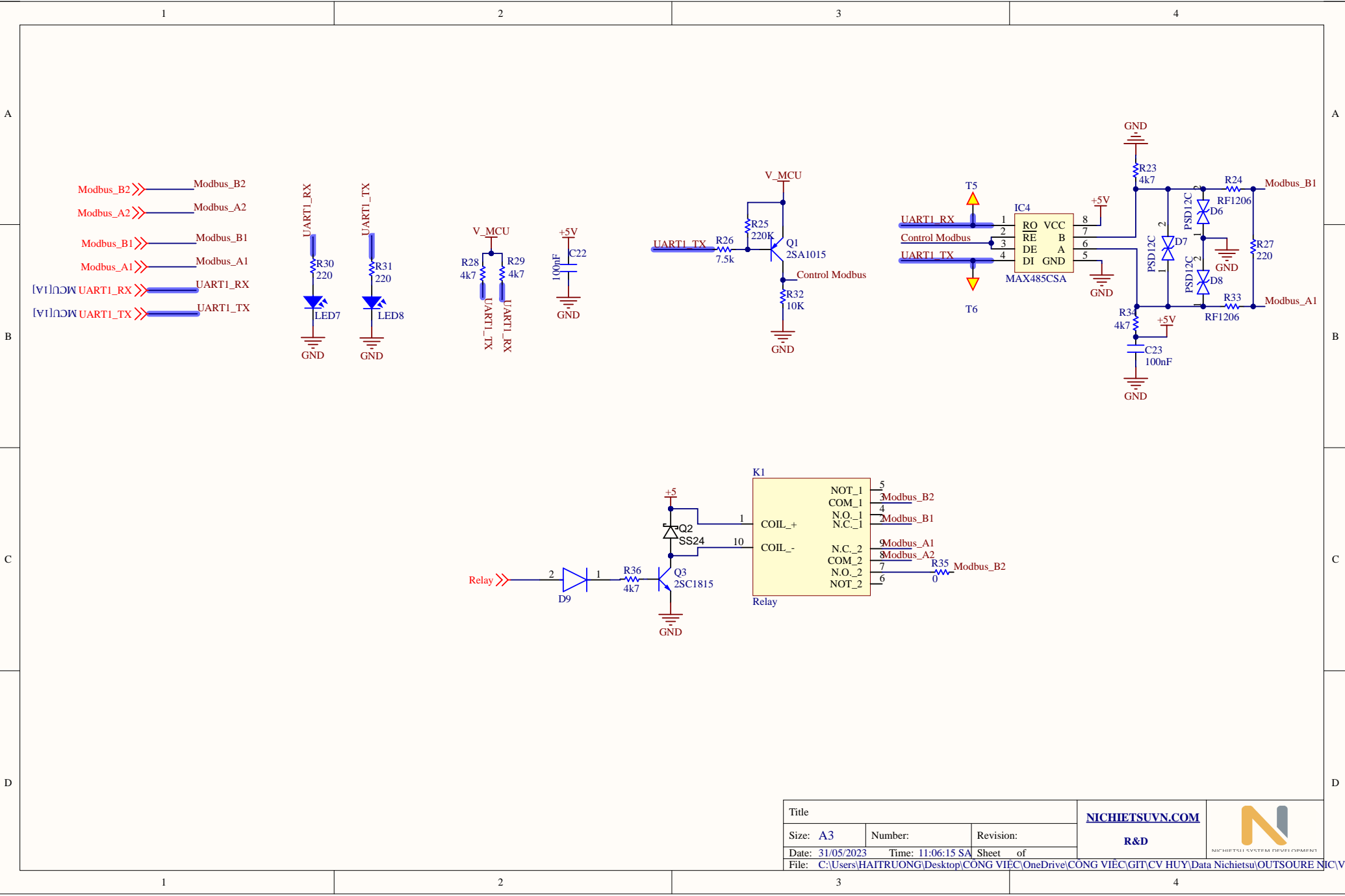



	1	2	3	4
A				
B				
C				
D				
	1	2	3	4

Title *			NICHETSUVN.COM R&D 
Size: A3	Number:*	Revision:*	
Date: 31/05/2023	Time: 11:06:14 SA Sheet 1 of 9		
File: C:\Users\HAITRUONG\Desktop\CONG VIỆC\OneDrive\CONG VIỆC\GIT\CV HUY\Data Nichietsu\OUTSOURCING\VNPN			



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1

2

3

4

A

B

C

D

A

B

C

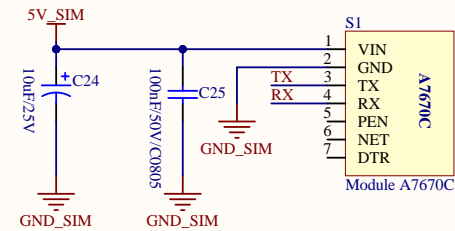
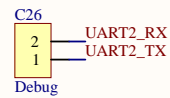
D


[v1]UART2_RX >> UART2_RX

[v1]UART2_TX >> UART2_TX

UART2_RX R37 TX
0R/R1206/1%

UART2_TX R38 RX
0R/R1206/1%



Title			NICHETSUVN.COM	 NICHETSU SYSTEM DEVELOPMENT
Size: A3	Number:	Revision:		
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File: C:\Users\HAITRUONG\Desktop\CONG VIEC\OneDrive\CONG VIEC\GIT\CV HUY\Data Nichietsu\OUTSOURCING\NICHETSU\				

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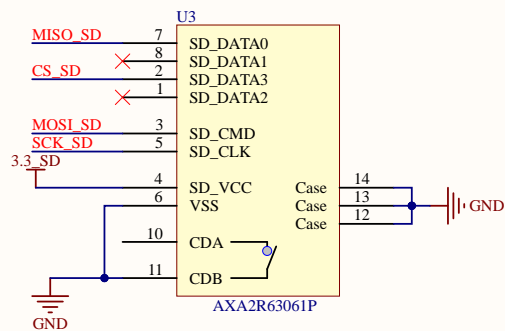
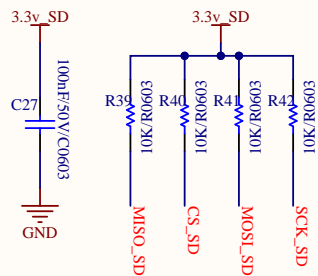
D

A


B

C

D



[B1]MCU1B SCK_SD >> SCK_SD
[B1]MCU1B MOSI_SD >> MOSI_SD
[B1]MCU1B CS_SD >> CS_SD
[B1]MCU1B MISO_SD >> MISO_SD

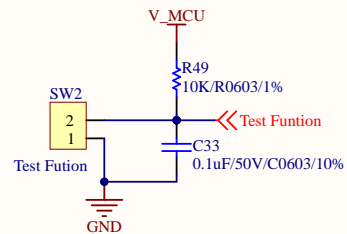
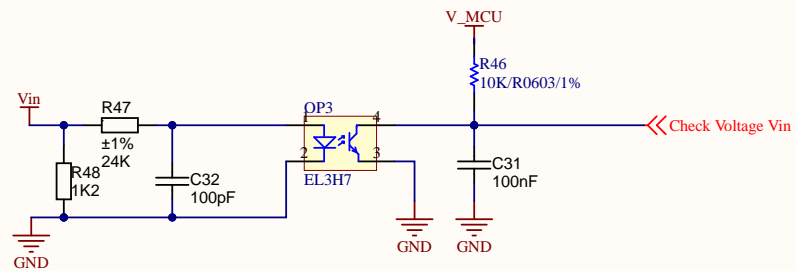
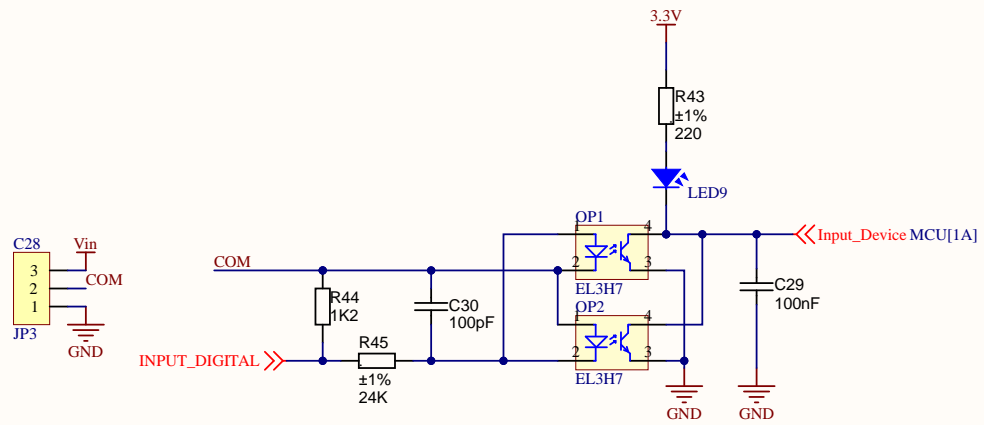
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Size: A3	Number:	Revision:		
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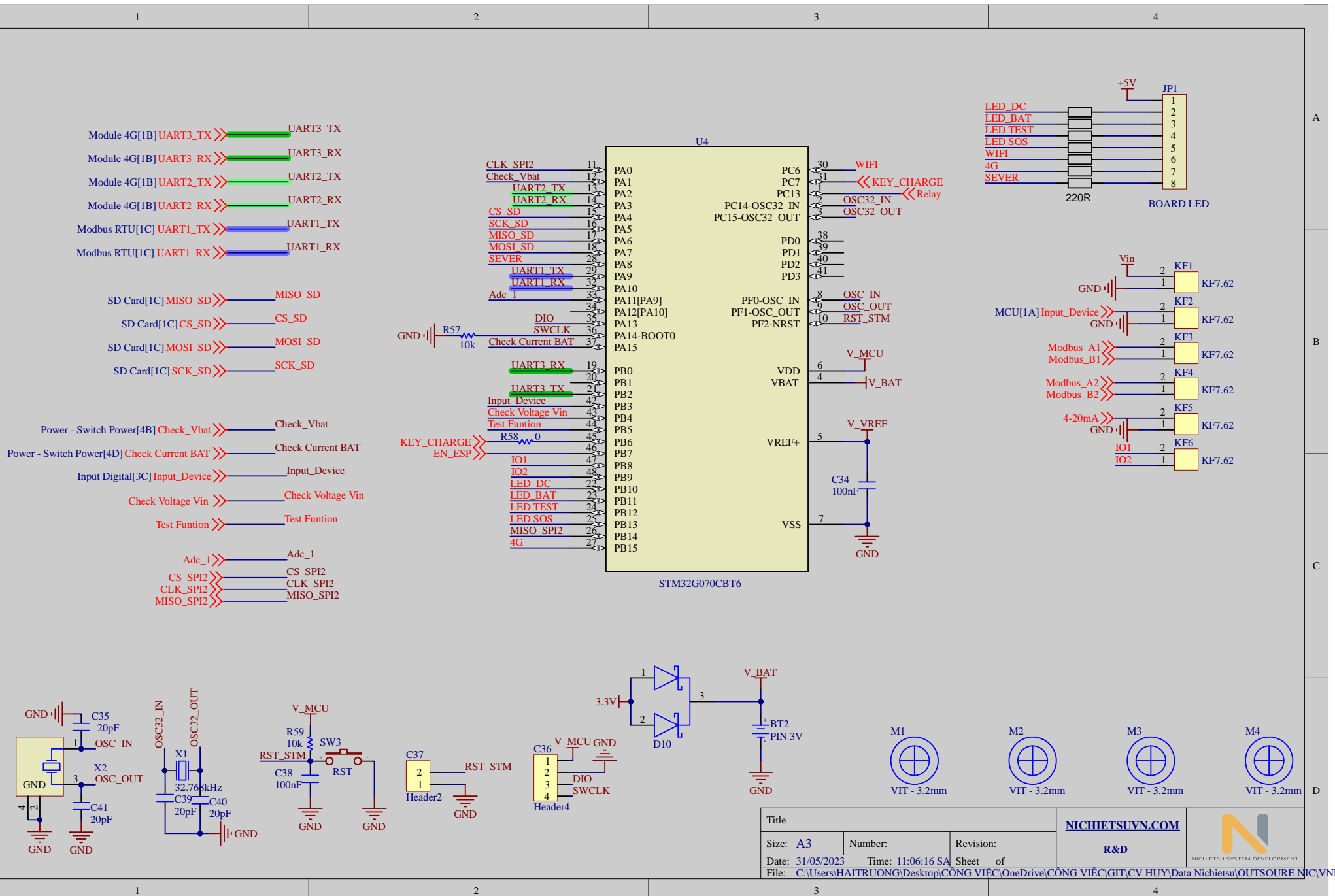
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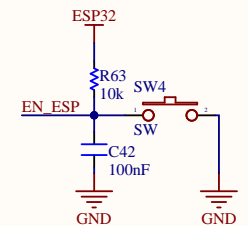
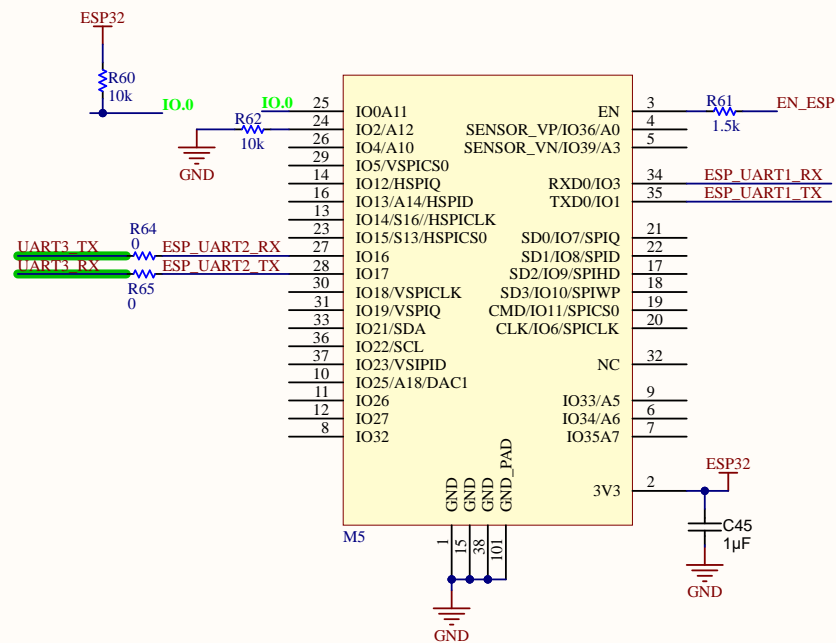
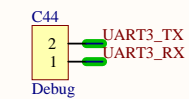
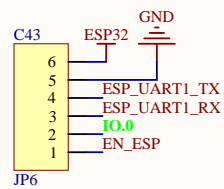
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Title			NICHETSUVN.COM R&D	 NICHETSU SYSTEM DEVELOPMENT
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File: C:\Users\HAITRUONG\Desktop\CONG VIET\OneDrive\CONG VIET\GIT\CV HUY\Data Nichietsu\OUTSOURC NICHETSU				



Module 4G[1B] UART3_TX >> UART3_TX
 Module 4G[1B] UART3_RX >> UART3_RX
 EN_ESP >> EN_ESP



Adc_1 << Adc_1
 CS_SPI2 << CS_SPI2
 CLK_SPI2 << CLK_SPI2
 MISO_SPI2 << MISO_SPI2

