

CubeSatSim battery board vb1.3.2.1
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KU2Y-KI6ZUM-WD6DRI



GPIO Bus

plug STEM Payload board on top

F1

S/N

R1 Only 12 pins on J1 need to be soldered

zip tie hole

BAT- JP3

BAT+ JP2

U2

BT1
BAT+

C1

JP9 JP11

JP12 JP10

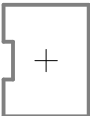
Default I2C
address 0x44

zip tie hole

JP7

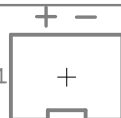
BT2

JP8



Parallel
battery
pack
connection

JP1



To Solar Board

zip tie hole