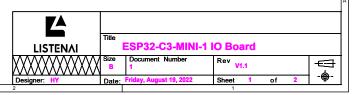
VERSION HISTORY

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01 Modify note 02 ESP32-C3-MINI-1 IO Board

Revision	Description	Date	Author
V1.0	First Edit	2022-2-2	НХ
V1.1	Modify	2022-8-15	НХ



ESP32-C3-MINI-1 IO Board

ESP32_C3 TO CSK6 GPIO

ESP32_RESET	ESP32_C3_CHIP_EN	R1 0R R0201	GPIO_A_10
DATA READY	ESP32_C3_GPIO4/ADC1_CH4	R2 QR R0201	GPIO_A_11
HAND SHAKE	ESP32 C3 GPIO5/ADC2 CH0	R3 0R R0201	GPIO_A_16

ESP32_C3 TO CSK6 SPI1

ESP32_C3_SPI0_MOSI_GPI07	R4 OR R0201	GPIO_A_19/SPI1_MOSI
ESP32_C3_SPI0_CS_GPI010	R5 OR R0201	GPIOA_17/SPI1_CS
ESP32_C3_SPI0_MISO_GPI02	R6 OR R0201	GPIOA_18/SPI1_MISO
ESP32 C3 SPI0 CLK GPI06	R7 .0R R0201	GPIOA 20/SPI1 CLK

