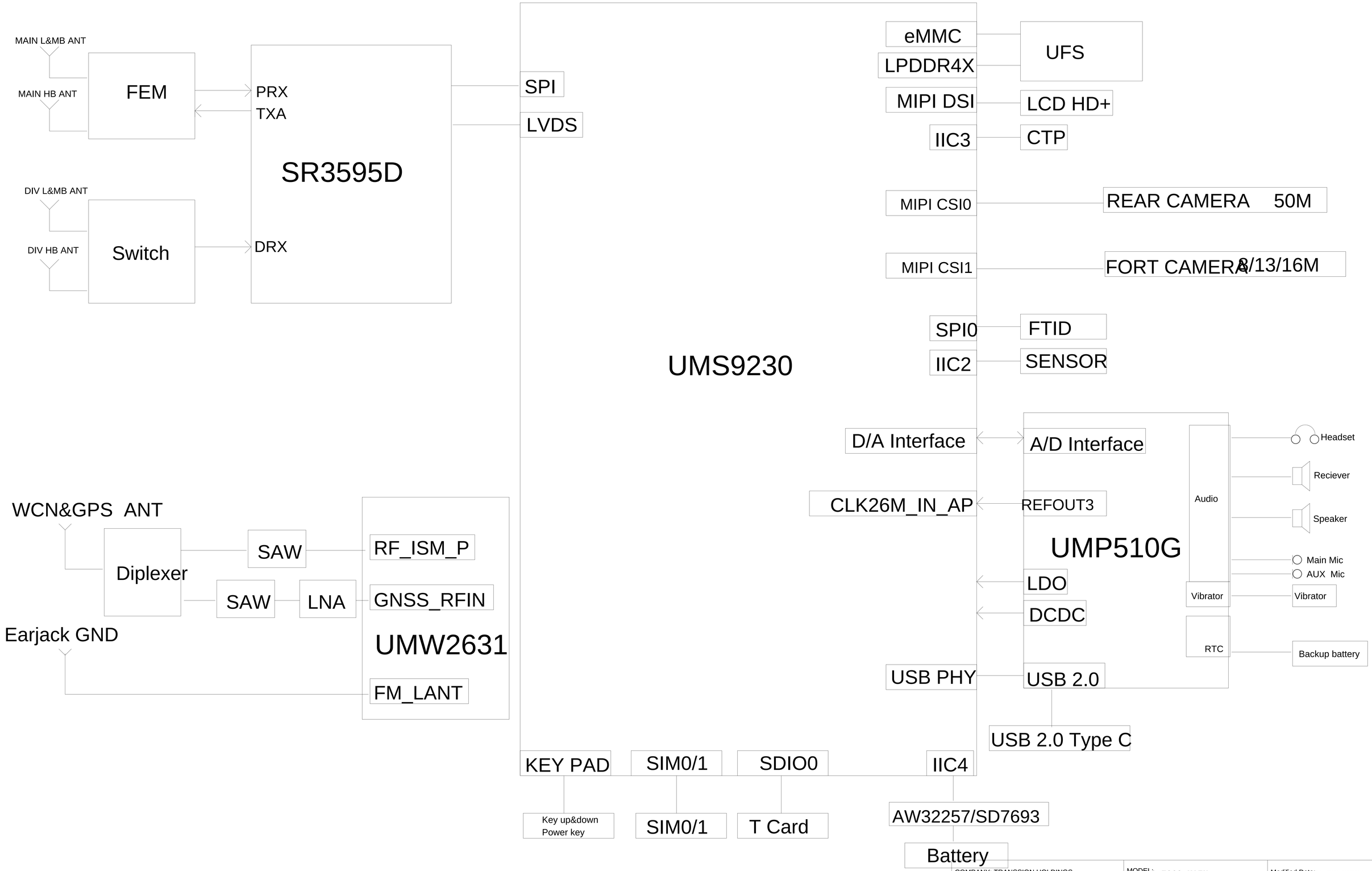
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

BLOCK DIAGRAM



COMPANY: TRANSSION HOLDINGS		MODEL: J8503_F069_MAIN	Modified Date: 21/02/2024:20:07
DRAWN	DATED	TITLE:	
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RF BLOCK DIAGRAM

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COMPANY: TRANSSION HOLDINGS				MODEL: K6735_F201_MAIN		Modified Date: 08/03/2024:15:12
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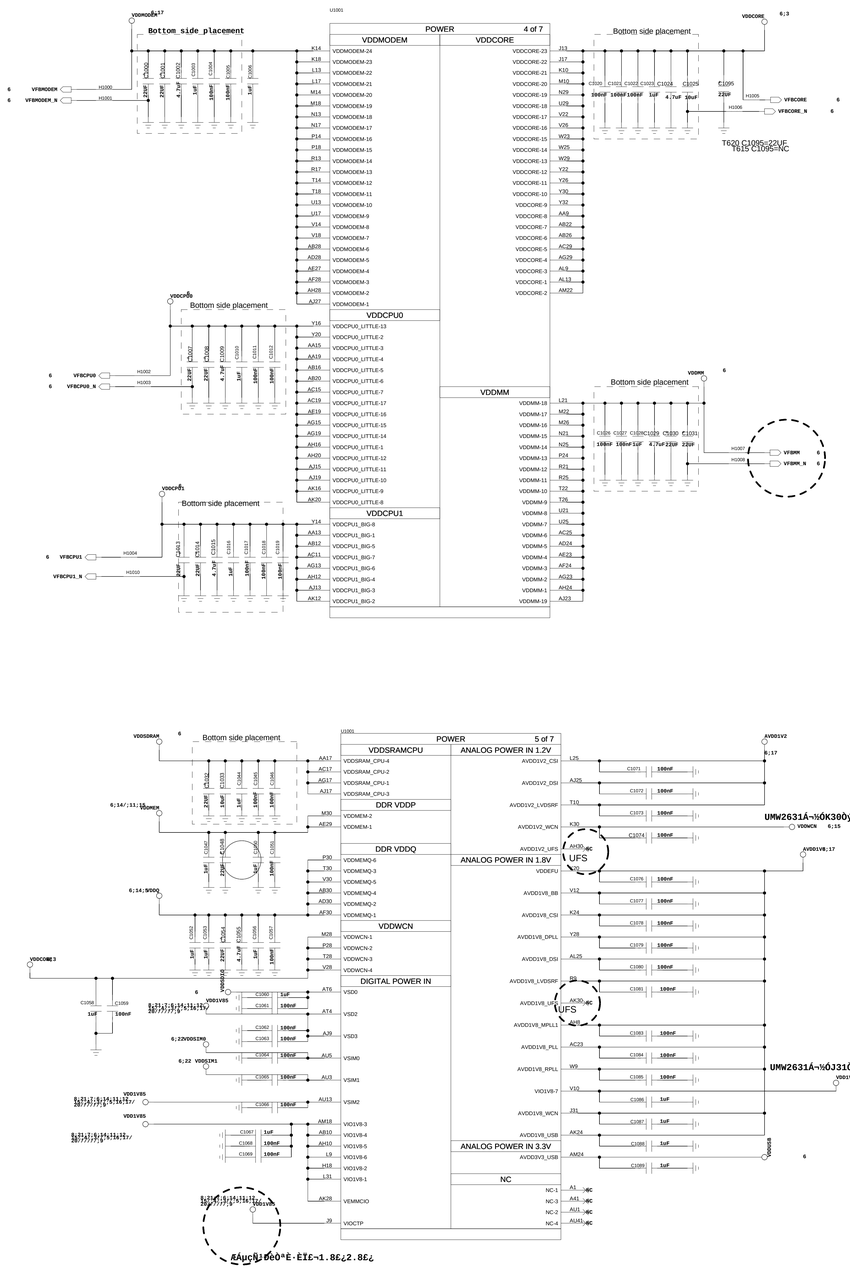
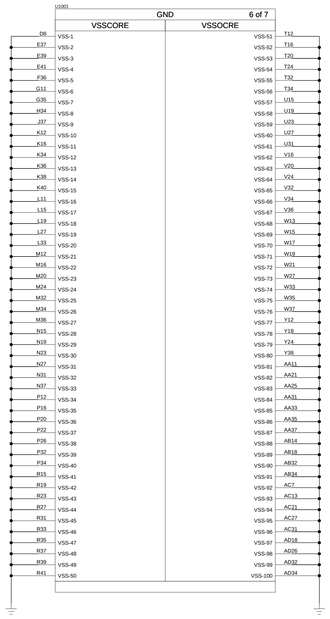
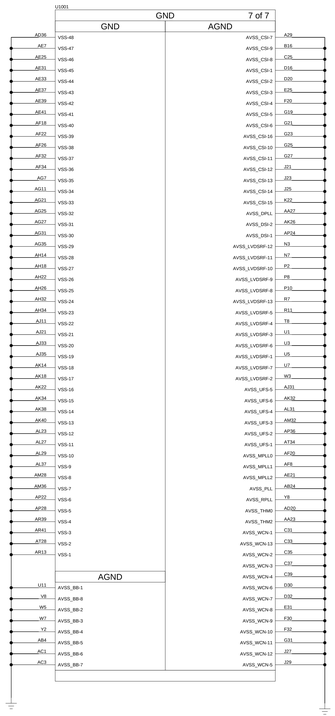
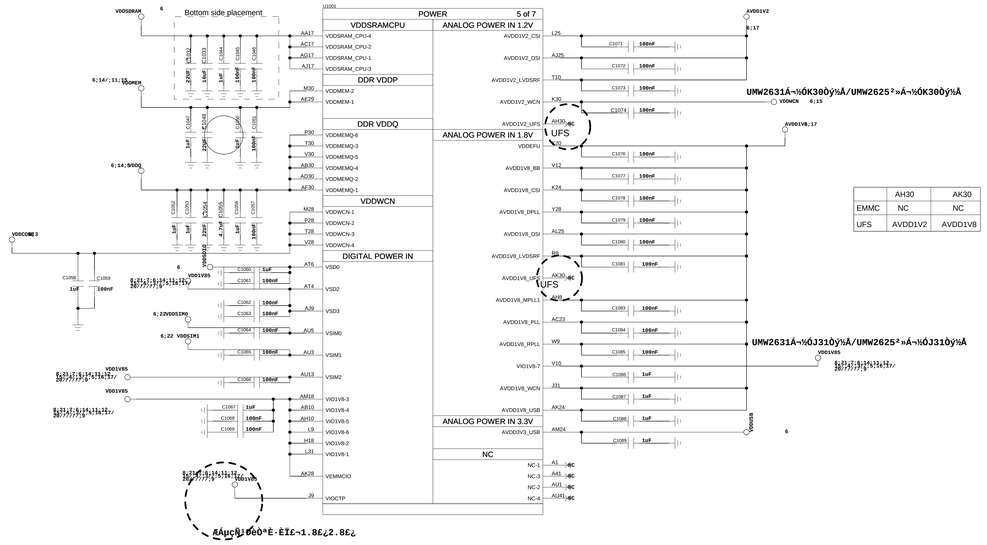
POWER









	AH30	AK30
EMMC	NC	NC
UFS	AVDD1V2	AVDD1V8

COMPANY: TRANSSION HOLDINGS		MODEL:	Modified Date:
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DIGITAL

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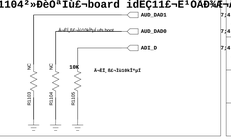
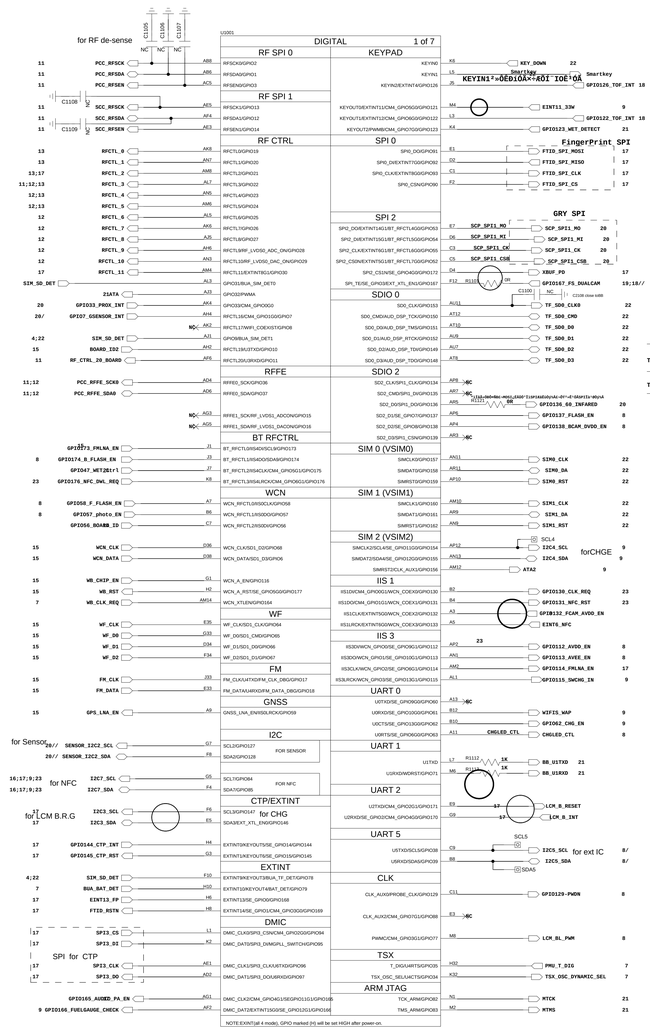
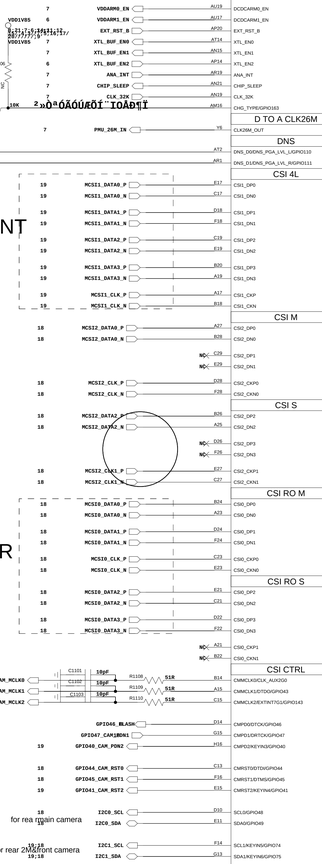
emmc,R1104²»ÐèÒª³ÌùÉ¬board idÊÇ11£¬Ê¹ÓÃÐ¾Æ¬ÁÚ²¿ÉÏÀ-

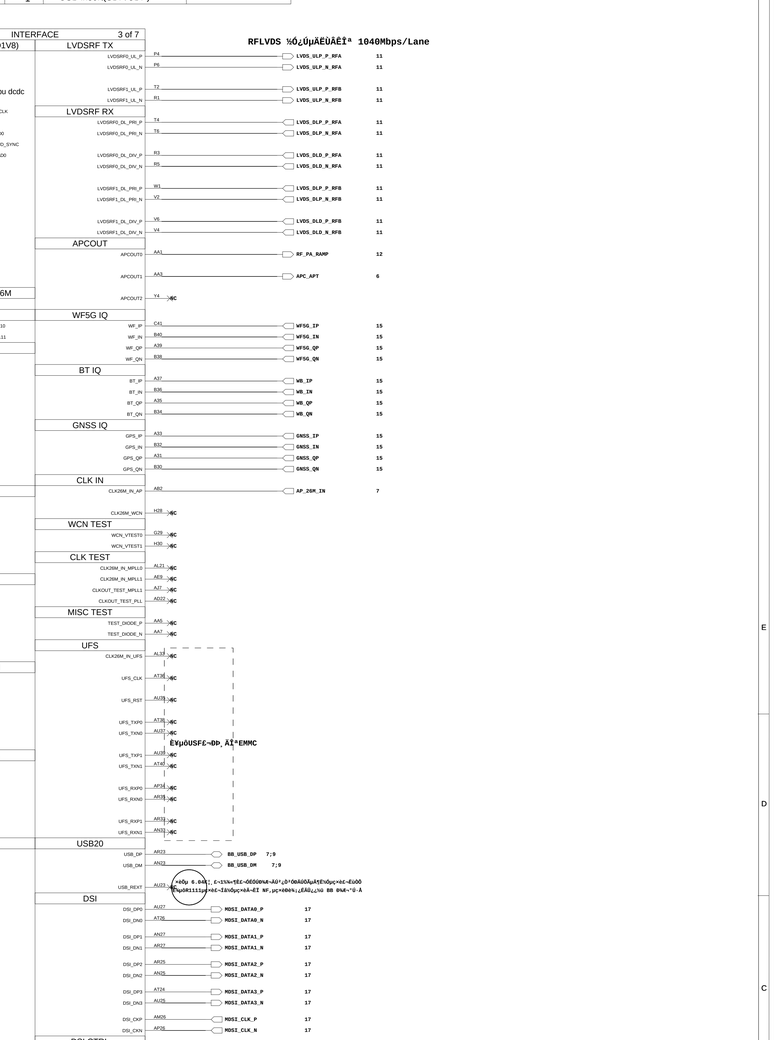


Ball name	Signal name in boot code	Value	Descriptions	Default vale
AUD_DAD0	ARM_BOOT_MD1	00	Reserved	11(ie=1,wpu) (MSB=MD1, LSB=MD0)
		01	UFS boot	
AUD_DAD1	ARM_BOOT_MD0	10	SD boot	
		11	eMMC 6.5Mhz	
ADI_D	ARM_BOOT_MD3	0	NON-KEY Download(With timeout) mode	
		1	Normal mode	1(ie=1,wpu)
CHG_TYPE	ARM_BOOT_MD4	0	Other(DCP/other)	0(ie=1,wpd) Ìb³¨¼µÍÃÕØØ ºÍN6ProÓÐÊ±±È¬3s time out
		1	USB insert(SDP/CDP)	



	GPIO111 (IN0_D1)	GPIO110 (IN0_D0)
²»Ê¹ÓÃÇÀÅÖÍC	0	0
Ê¹ÓÃÇÀÅÖÍC UMB9230S	1	0
Ê¹ÓÃÆæÕÇÀÅÖÍC	0	1





COMPANY: TRANSSION HOLDINGS				MODEL: K6735_F201_MAIN		Modified Date: 29/03/2024:14:37
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MEMORY

×€×€ÕâÈMCPÊ˜umcpÉ©ÊÊ±°ò DDRµÄDQÏß°´ÕÕµÚµÚÕÕ»ÄÐPINÃû°ÄÃ»ò

×€×€ÕâÈEMMCÊ˜uf±È©·ÕâÈµÄÊÊ±°ò DDRµÄDQÏß°´ÕÕµÚ†ÁÐPINÃû°ÄÃ»ò

µÚÕ»Ä°´µÚ†ÁÐ

U0201
MT53B512M32D2NP-053:H0:B
MEMORY Order by Func:2 D.LPA(s)

2 of 7

DATA LANE 0 A

14	EMDQ0_A	F40	EMDQ0_A/EMDQ0_A	
14	EMDQ1_A	G41	EMDQ7_A/EMDQ1_A	
14	EMDQ2_A	H40	EMDQ6_A/EMDQ2_A	
14	EMDQ3_A	J41	EMDQ5_A/EMDQ3_A	
14	EMDQ4_A	J39	EMDM0_A/EMDQ4_A	
14	EMDQ5_A	H38	EMDQ3_A/EMDQ5_A	
14	EMDQ6_A	G39	EMDQ2_A/EMDQ6_A	
14	EMDQ7_A	F38	EMDQ0_A/EMDQ7_A	
14	EMDMI0_A	G37	EMDQ1_A/EMDM0_A	
14	EMDQS0_A_P	H36	EMDQS0_T_A/EMDQS0_T_A	
14	EMDQS0_A_N	J36	EMDQS0_C_A/EMDQS0_C_A	

DATA LANE 1 A

14	EMDQ8_A	V38	EMDQ10_A/EMDQ8_A	
14	EMDQ9_A	U39	EMDM1_A/EMDQ9_A	
14	EMDQ10_A	T38	EMDQ13_A/EMDQ10_A	
14	EMDQ11_A	U37	EMDQ11_A/EMDQ11_A	
14	EMDQ12_A	W41	EMDQ8_A/EMDQ12_A	
14	EMDQ13_A	V40	EMDQ12_A/EMDQ13_A	
14	EMDQ14_A	U41	EMDQ14_A/EMDQ14_A	
14	EMDQ15_A	T40	EMDQ15_A/EMDQ15_A	
14	EMDMI1_A	W39	EMDQ9_A/EMDM1_A	
14	EMDQS1_A_P	T36	EMDQS1_T_A/EMDQS1_T_A	
14	EMDQS1_A_N	U35	EMDQS1_C_A/EMDQS1_C_A	

DATA LANE 0 B

14	EMDQ0_B	AP40	EMDQ4_B/EMDQ0_B	
14	EMDQ1_B	AN41	EMDQ7_B/EMDQ1_B	
14	EMDQ2_B	AM40	EMDQ5_B/EMDQ2_B	
14	EMDQ3_B	AL41	EMDQ6_B/EMDQ3_B	
14	EMDQ4_B	AL39	EMDM0_B/EMDQ4_B	
14	EMDQ5_B	AK38	EMDQ3_B/EMDQ5_B	
14	EMDQ6_B	AK39	EMDQ2_B/EMDQ6_B	
14	EMDQ7_B	AP38	EMDQ0_B/EMDQ7_B	
14	EMDMI0_B	AN37	EMDQ1_B/EMDM0_B	
14	EMDQS0_B_P	AL35	EMDQS0_T_B/EMDQS0_T_B	
14	EMDQS0_B_N	AK36	EMDQS0_C_B/EMDQS0_C_B	

DATA LANE 1 B

14	EMDQ8_B	AB38	EMDQ10_B/EMDQ8_B	
14	EMDQ9_B	AC39	EMDM1_B/EMDQ9_B	
14	EMDQ10_B	AC37	EMDQ11_B/EMDQ10_B	
14	EMDQ11_B	AD38	EMDQ13_B/EMDQ11_B	
14	EMDQ12_B	AA41	EMDQ9_B/EMDQ12_B	
14	EMDQ13_B	AB40	EMDQ12_B/EMDQ13_B	
14	EMDQ14_B	AC41	EMDQ14_B/EMDQ14_B	
14	EMDQ15_B	AD40	EMDQ15_B/EMDQ15_B	
14	EMDMI1_B	AA39	EMDQ8_B/EMDM1_B	
14	EMDQS1_B_P	AB36	EMDQS1_T_B/EMDQS1_T_B	
14	EMDQS1_B_N	AC35	EMDQS1_C_B/EMDQS1_C_B	UMS9230

NOTE: using alternate to change order

CA A

EMCK_T_A/EMCK_T_A	P36	EMCLK_A_P	14
EMCK_C_A/EMCK_C_A	N35	EMCLK_A_N	14
EMCKE0_A/EMCKE0_A	M38	EMCKE0_A	14
EMCKE1_A/EMCKE1_A	L39	EMCKE1_A	14
EMCS0_A/EMCS0_A	N39	EMCS0_A	14
EMCS1_A/EMCS1_A	P38	EMCS1_A	14
EMCA0_A/EMCA0_A	N41	EMCA0_A	14
EMCA1_A/EMCA1_A	P40	EMCA1_A	14
EMCA2_A/EMCA2_A	L38	EMCA2_A	14
EMCA4_A/EMCA4_A	M40	EMCA3_A	14
EMCA5_A/EMCA5_A	L37	EMCA4_A	14
EMCA3_A/EMCA3_A	L41	EMCA5_A	14

CA B

EMCK_T_B/EMCK_T_B	AE35	EMCLK_B_P	14
EMCK_C_B/EMCK_C_B	AF36	EMCLK_B_N	14
EMCKE0_B/EMCKE0_B	AH38	EMCKE0_B	14
EMCKE1_B/EMCKE1_B	AJ39	EMCKE1_B	14
EMCS0_B/EMCS0_B	AG39	EMCS0_B	14
EMCS1_B/EMCS1_B	AF38	EMCS1_B	14
EMCA0_B/EMCA0_B	AG41	EMCA0_B	14
EMCA1_B/EMCA1_B	AF40	EMCA1_B	14
EMCA2_B/EMCA2_B	AG37	EMCA2_B	14
EMCA4_B/EMCA3_B	AH40	EMCA3_B	14
EMCA5_B/EMCA4_B	AJ37	EMCA4_B	14
EMCA3_B/EMCA5_B	AJ41	EMCA5_B	14

REF ZQ

EMRESET_N	Y40	EMRSTN	14	
EMZQ	D40	R1200 NC / R1201 240R	VDD1V8_3 / GND	

VDD1V85

R1203 NC, R1204 NC

EMMC

EMMC_CLK	AT30	R1202 0R	EMMC_CLK	14
EMMC_CMD	AN31		EMMC_CMD	14
EMMC_RST	AP32		EMMC_RST	14
EMMC_DS/GPIO105	AU31		EMMC_RCLK	14

×¢ÕâÈ°CPUÐÑÆ¬eMMC²¿·ÖµÄµÃCLK/RST/CMD³ÎCMMCLK0µÄGPIO¹¦ÂÜ²»ÄÜÕÃÇ¬ëäÉÚ¹¦ÂÜÕÕ³É

EMMC_D0/BT_RFCTL0G1/GPIO98	AR31	EMMC_D0	14
EMMC_D1/BT_RFCTL0G1/GPIO108	AR29	EMMC_D1	14
EMMC_D2/BT_RFCTL2G1/GPIO109	AU33	EMMC_D2	14
EMMC_D3/BT_RFCTL3G1/GPIO106	AW30	EMMC_D3	14
EMMC_D4/BT_RFCTL4G1/GPIO104	AP30	EMMC_D4	14
EMMC_D5/BT_RFCTL5G1/GPIO103	AT32	EMMC_D5	14
EMMC_D6/BT_RFCTL6G1/GPIO102	AU29	EMMC_D6	14
EMMC_D7/BT_RFCTL7G1/GPIO101	AN29	EMMC_D7	14

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PMU_POWER

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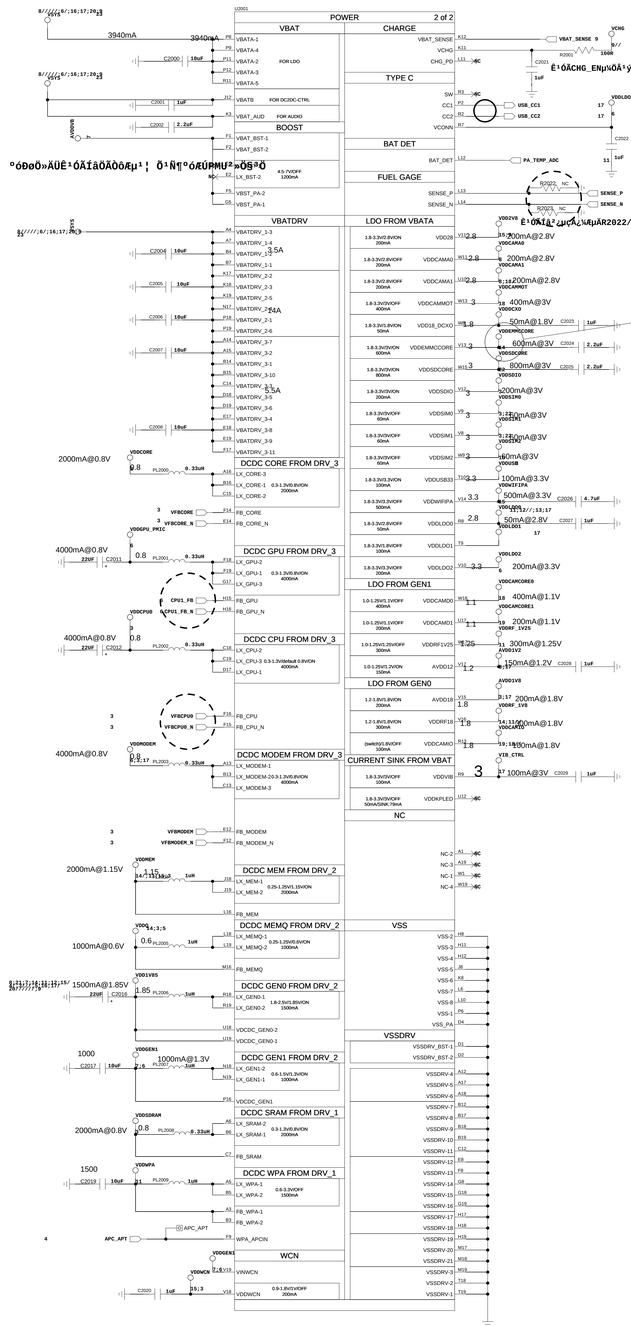


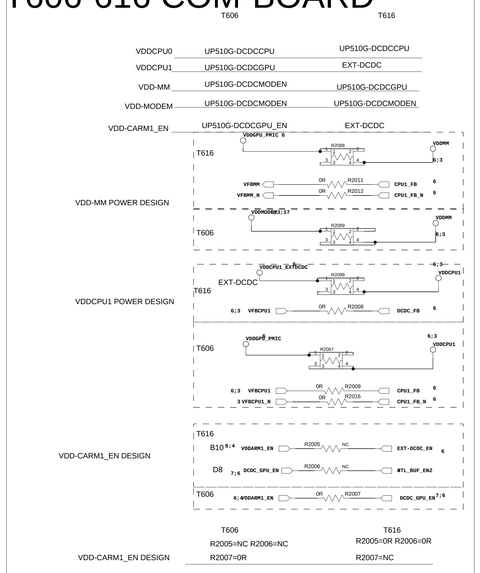
UMP510G DCDC output

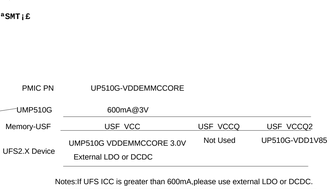
Input Power	Power Name	Output Voltage (V)	Output Current (mA)	Default Voltage (V)	Default
VBAT	DCDC CORE	0.3-1.3	2000	0.8	ON
	DCDC GPU	0.3-1.3	4000	0.8	ON
	DCDC CPU	0.3-1.3	4000	0.8	ON
	DCDC MODEM	0.3-1.3	4000	0.8	ON
	DCDC MEM	0.25-1.25	2000	1.15	ON
	DCDC MEMQ	0.25-1.25	1000	0.6	ON
	DCDC GEN0	1.8-2.5	1500	1.85	ON
	DCDC GEN1	0.6-1.5	1000	1.3	ON
	DCDC SRAM	0.3-1.3	2000	0.8	ON
	DCDC WPA	0.6-3.3	1500	/	OFF

UMP510G LDO output

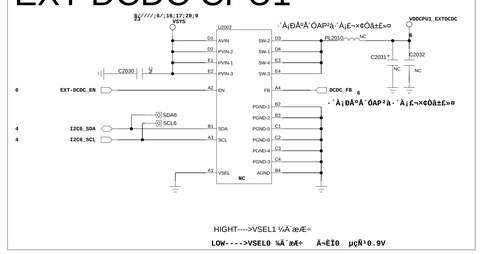
Input Power	Power Name	Output Voltage (V)	Output Current (mA)	Default Voltage (V)	Default
VBAT	VDD28	1.8-3.3	200	2.8	ON
	VDDCAMA0	1.8-3.3	200	2.8	OFF
	VDDCAMA1	1.8-3.3	200	2.8	OFF
	VDDCAMMOT	1.8-3.3	400	3	OFF
	VDD18_DCXO	1.8-3.3	50	1.8	ON
	VDDEMMCCORE	1.8-3.3	600	3	ON
	VDDSDCORE	1.8-3.3	800	3	ON
	VDDSDIO	1.8-3.3	200	3	ON
	VDDSIM0	1.8-3.3	60	3	OFF
	VDDSIM1	1.8-3.3	60	3	OFF
	VDDSIM2	1.8-3.3	60	3	OFF
	VDDUSB33	1.8-3.3	100	3.3	ON
	VDDWIFIPA	1.8-3.3	500	3.3	OFF
	VDDLDO0	1.8-3.3	50	2.8	OFF
	VDDLDO1	1.8-3.3	100	3.3	OFF
	VDDLDO2	1.8-3.3	200	3.3	OFF
GEN1	VDDCAMD0	1.0-1.25	400	1.1	OFF
	VDDCAMD1	1.0-1.25	200	1.1	OFF
	VDDRF1V25	1.0-1.25	300	1.25	OFF
	AVDD12	1.0-1.25	150	1.2	ON
GEN0	AVDD18	1.2-1.8	200	1.8	ON
	VDDRF18	1.2-1.8	300	1.8	ON
	VDDCAMIO	1.2-1.8	100	1.8	OFF
GEN1	WCN	1.0-1.6	200	1	OFF

T606-616 COM-BOARD





EXT DCDC CPU1



Èí¼þÊ¹ÓÃ PMIC µÄ OPTION1/2 À´Çø·Ö BB µÄ ARM1 µÄ©µç·½Ê½
OPTION1/2=00£¬PMIC DCDC GPU ø BB µÄ´ó°Ê©µç£¬DCDC GPU ÓÉ BB µÄ DCDCARM
¿ØÆ¿ªÆôÊ¹¦ø´ó°Ê©µçÊÇÓÛÍµÌ³ÖÊÐÐµ½ kernel ºó¿ªÆôµÄÊ¹¦ø´ó¿ª¾ÍÊÇÓã¸ öÓ·Óó¡£Óãµã°I OPTION1/2=01 ÉÌµçÊ±Ðò²»I¬£¬²âÀ¿Ê±Ðè×¢Óã¡

COMPANY: TRANSSION HOLDINGS			MODEL: K6735_F201_MAIN		Modified Date: 08/03/2024:15:12
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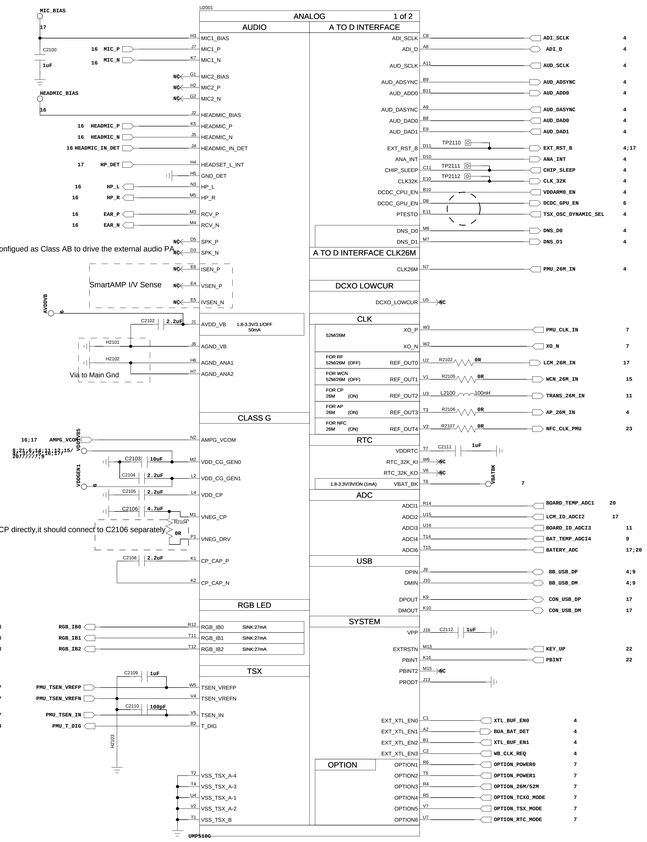
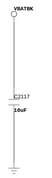
PMU_ANALOG



Internal audio PA must be configued as Class AB to drive the external audio PA

.VNEG_DRV cannot connect to VNEG_CP directly,it should connect to C2106 separately

BKBT



TSX

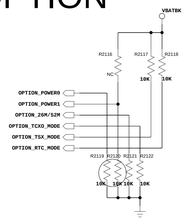
IDistance from TCXO/TSX to heat source should be 3mm/46mm separately at least..

Minimum 0.25mm-clearance route out area around TCXO/TSX to avoid heat conductivity effects..

Recommend no-copper area under TCXO/TSX from Layer L1 to Layer L(n-1)

Keep ¡Y1mm away from the PCB edge, don¡¯t refer to the PCB Main GND, refer PCB design guide for detail

OPTION



	Item	Scenario	Option1	Option2
-->	0	Qogir L6+UMP510G	0	0
	1	Qogir L6+UMP510G+DCDC	0	1
	2		1	0
	3		1	1

	Scenario	Option3(26M/52M)	Option4(TCXO_MODE)	Option5(TSX_MODE)	Option6(RTC_MODE)
	26M TCXO+32K Crystal	0	1	0	0
	26M TSX+32K Crystal	0	0	1	0
-->	26M TSX+32K-less from PMIC internal	0	0	1	1
	26M DCXO+32K-less from PMIC internal	0	1	1	1
	26M DCXO+32K Crystal	0	1	1	0

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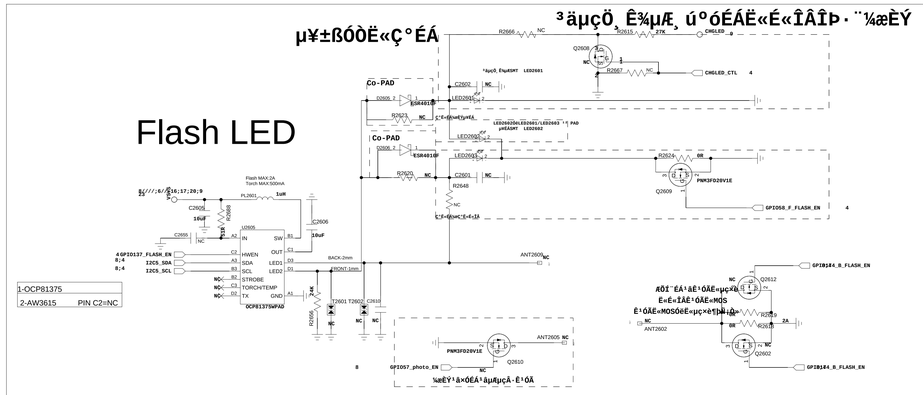
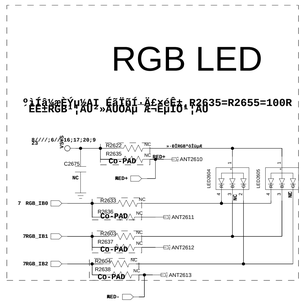
Flash LED



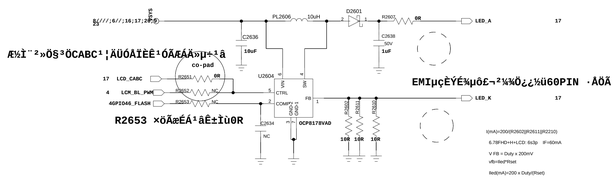
1-OCP81375	
2-AW3615	PIN C2=NC

RGB LED



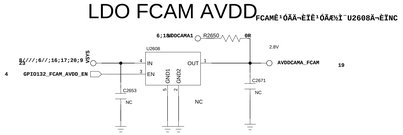
Back Light Driver

1uF£¬50V£¬0603

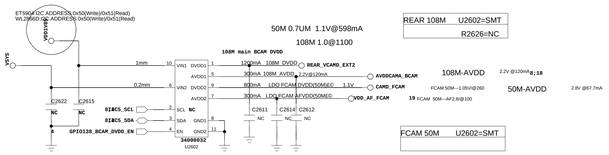


	PIN2	C2643	R2651	R2653
HARDWARE SIGNOTOR OUTPUT	NC	220nF	0R	NC
HARDWARE OUTPUT	COMP	220nF	0R	100R
HARDWARE OCP81709	STROBE	NC	0R	0R

LDO FCAM AVDD



LDO BCAM AVDD



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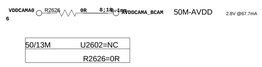
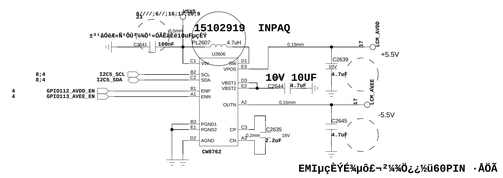


50/13M	U2602=NC
	R2626=0R

LDO Êä³öµçÈÝÉ¾µôÉ¬²¼¾ÖCAMÊäÈëµçÈÝ¿¿½üLDO·ÂÖÃ

BIAS OCP2131:I2C Address= write:0x7C read:0x7D



1-OCP2130WPAD	
2-CW8762	
3-SM5109C-K	

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BATTERY

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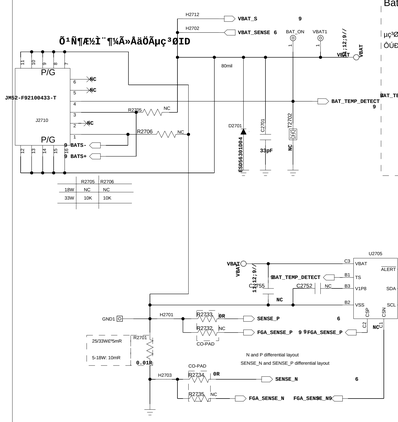

Battery temperature monitor

VDD2V8

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COMPANY: TRANSSION HOLDINGS				MODEL:**K6735_F201_MAIN**		Modified Date: **08/03/2024:15:12**
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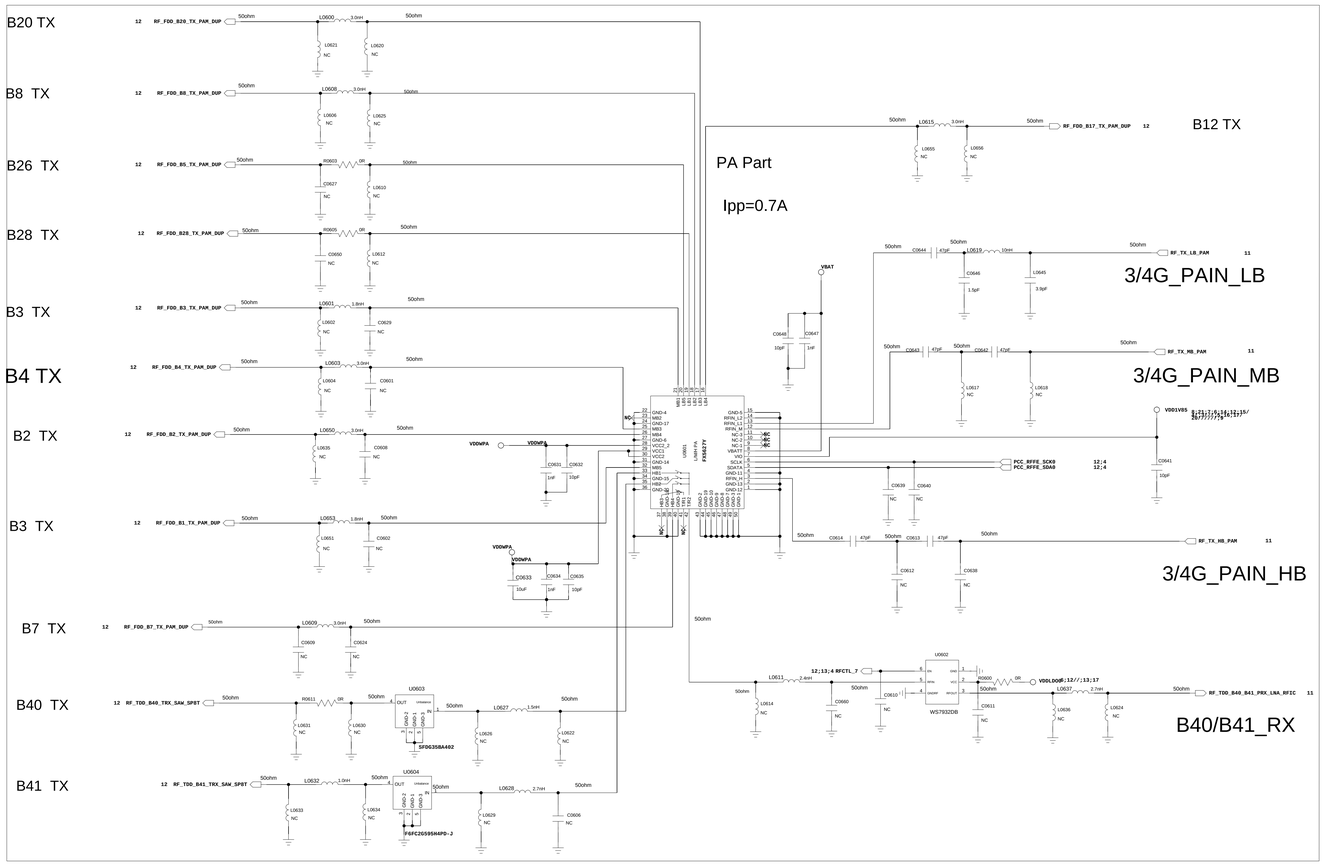
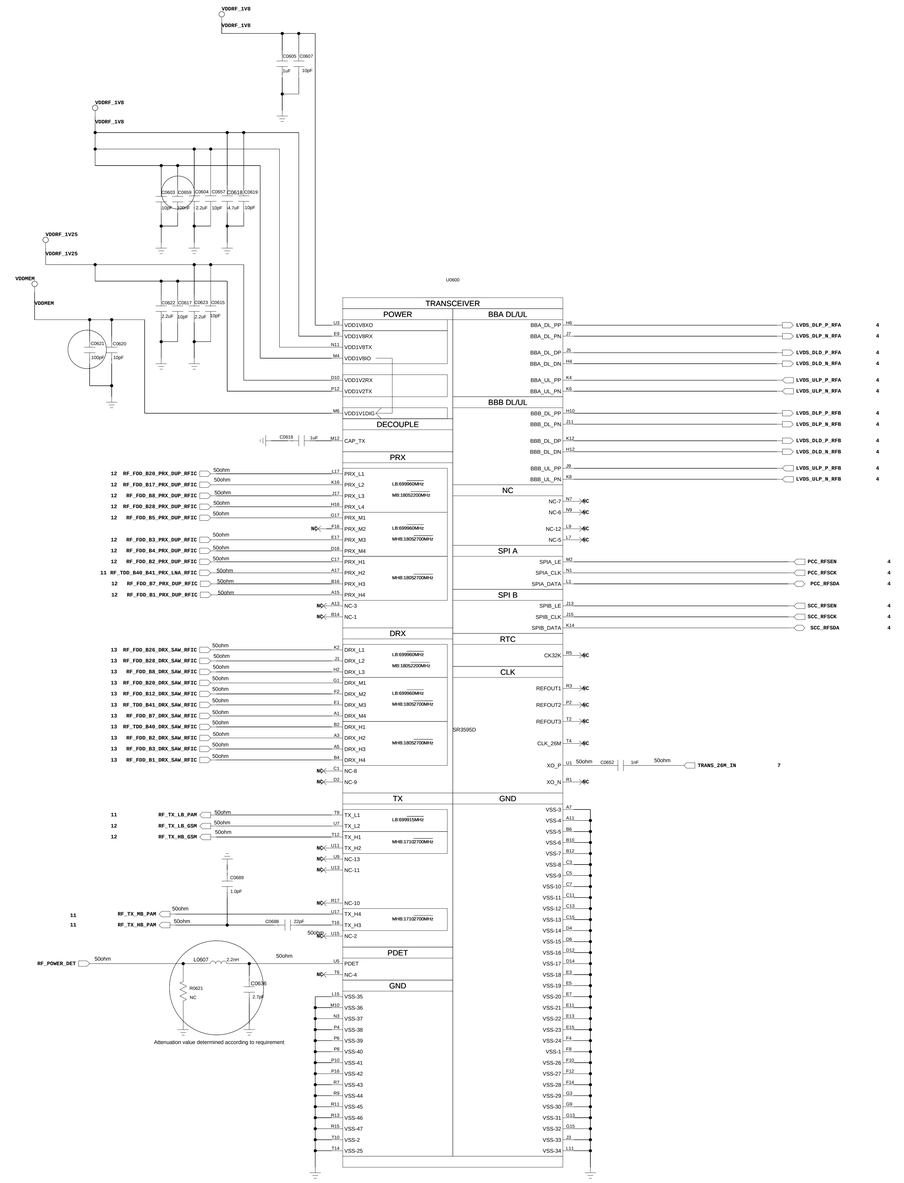
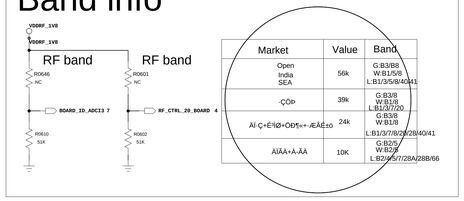
LTE/WCDMA PAM



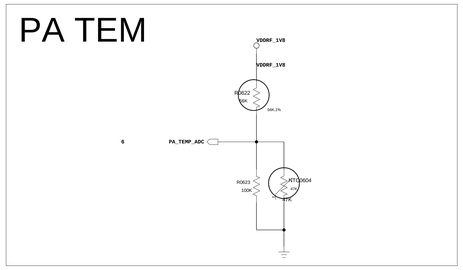


Band info



Market	Value	Band
Open India SEA	56k	G:B3/B8 W:B1/5/8 L:B1/3/5/8/40/41
ÇÖÞ	39k	G:B3/8 W:B1/8 L:B1/3/7/20
ÃÍ Ç÷È'IØ÷ÖÐ'Ì®® ÆÃÊ±Ò	24k	G:B3/8 W:B1/8 L:B1/3/7/8/28/40/41
ÃÍÃÃ+À-ÀÀ	10K	G:B2/5 W:B2/8 L:B2/4/5/7/28A/28B/66

PA TEM



COMPANY: TRANSSION HOLDINGS				MODEL: K6735_F201_MAIN	Modified Date: 28/03/2024:14:48
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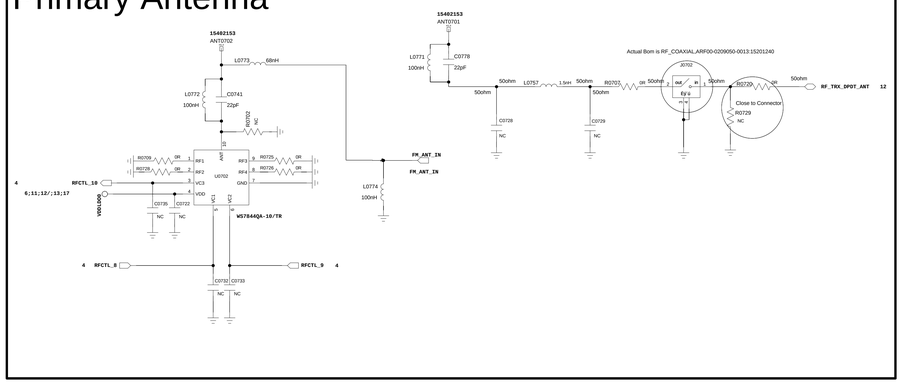
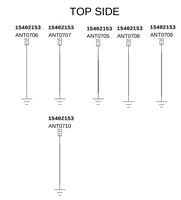
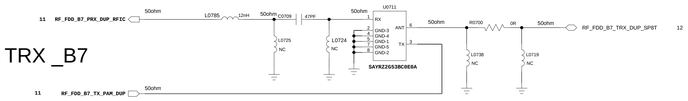


HOLE

GND Spring



TRX _B7



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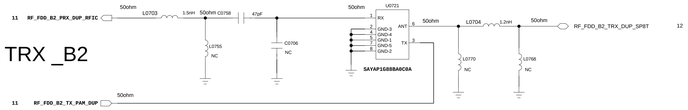

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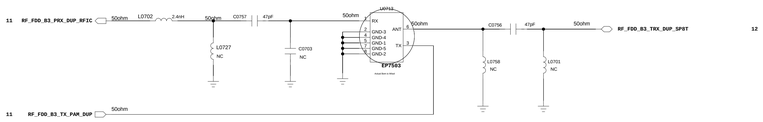

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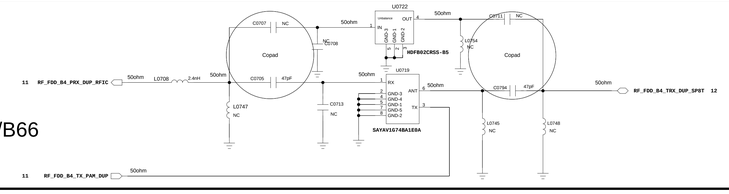

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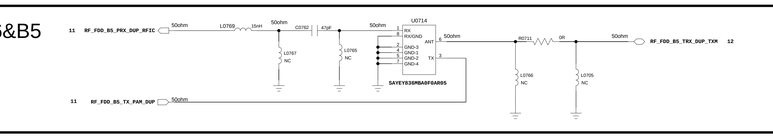

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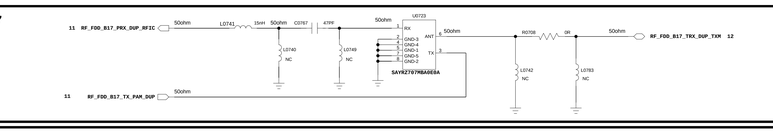

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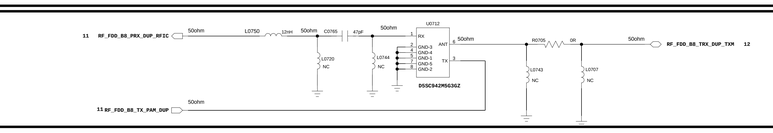

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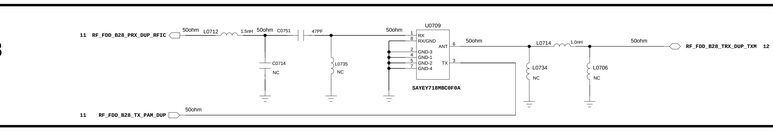

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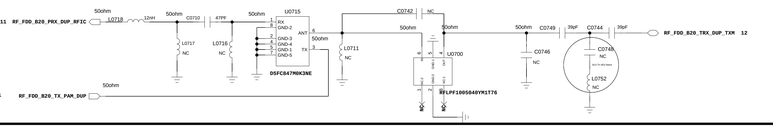

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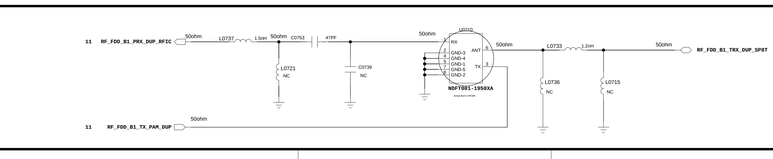

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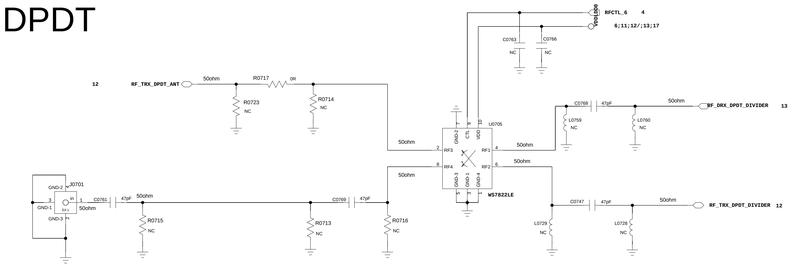

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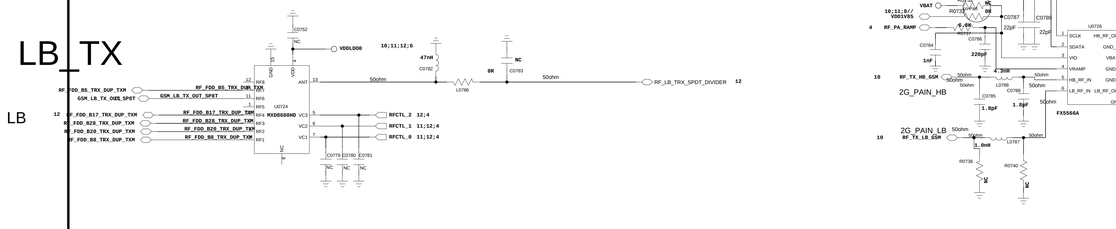

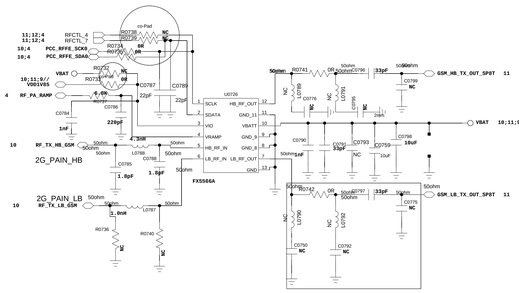

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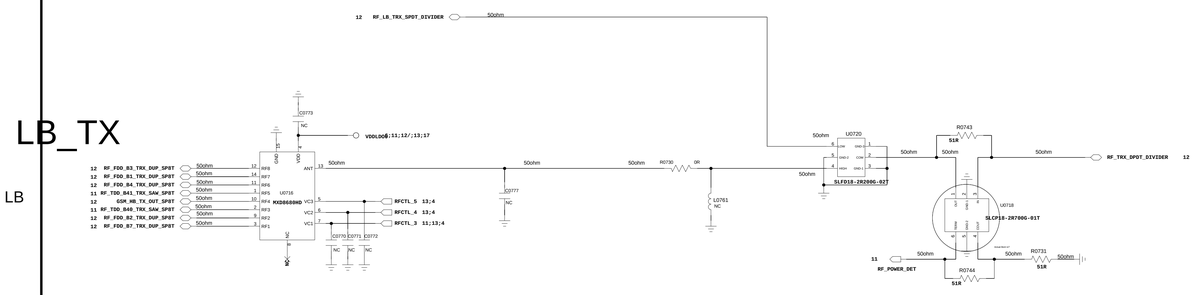

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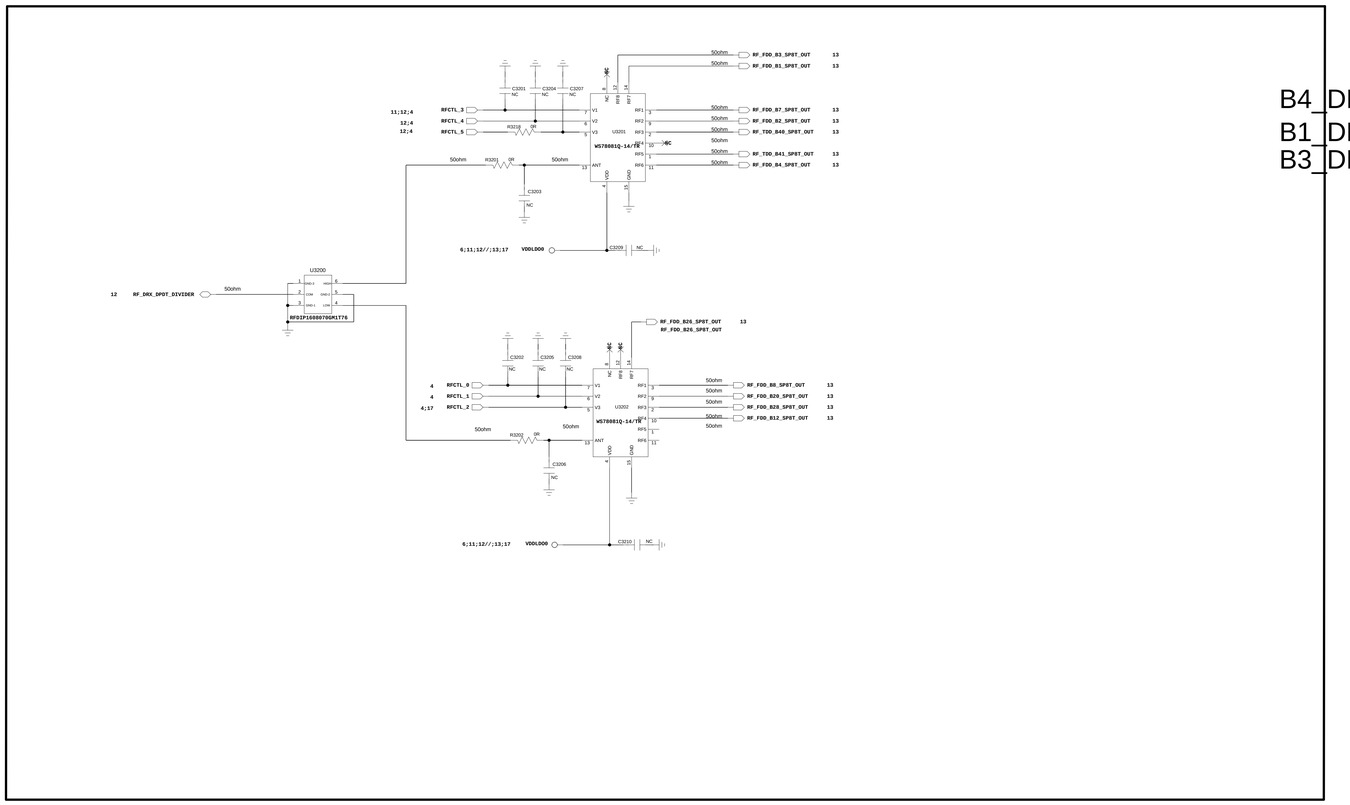

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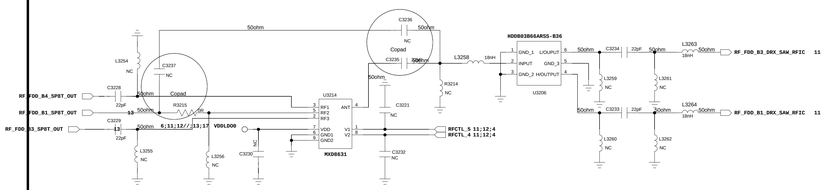
B4_DRX
B1_DRX
B3_DRX



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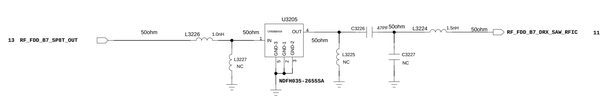

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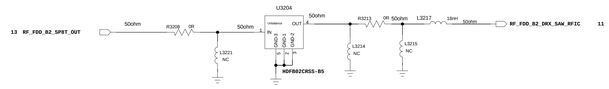

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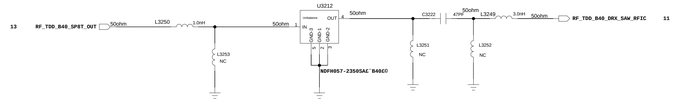

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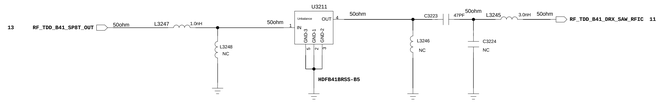

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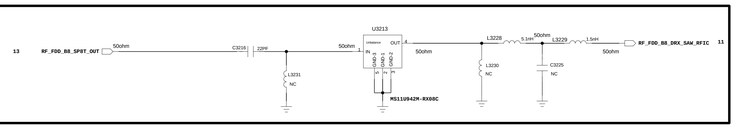

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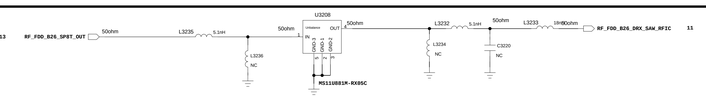

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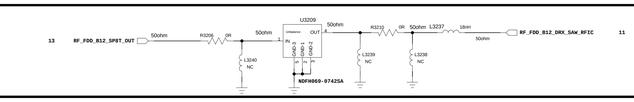

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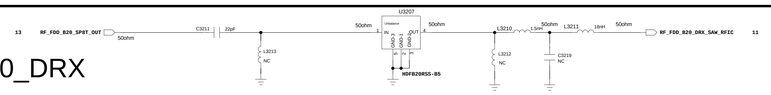

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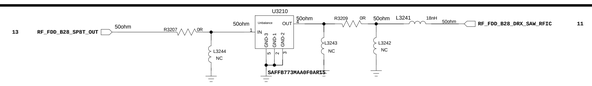

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EMMC

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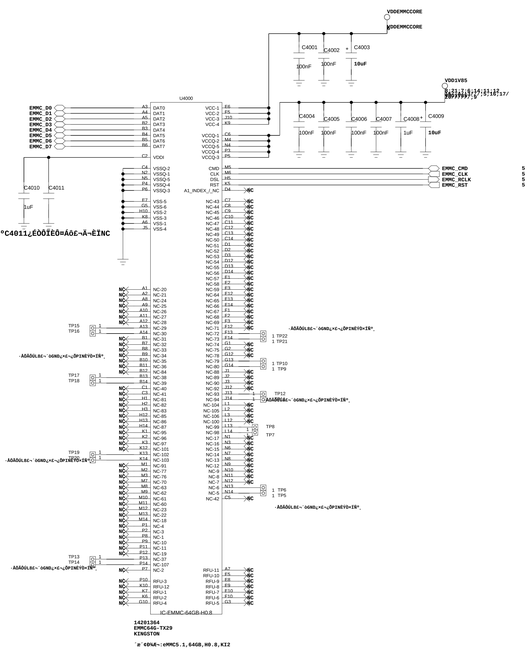

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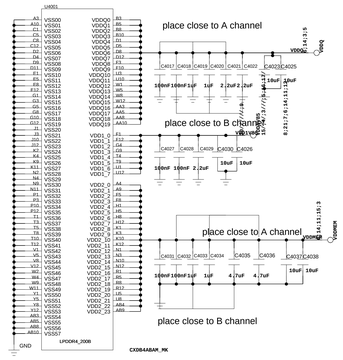

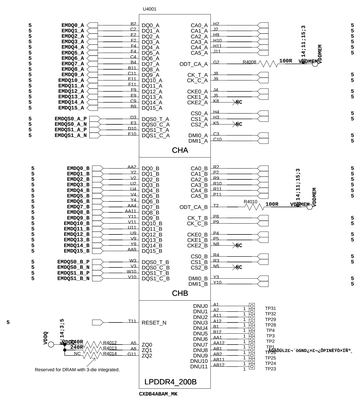

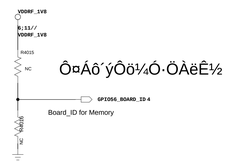

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COMPANY: TRANSSION HOLDINGS				MODEL: J6563_F069_MAIN		Modified Date: 14/03/2024:10:22	
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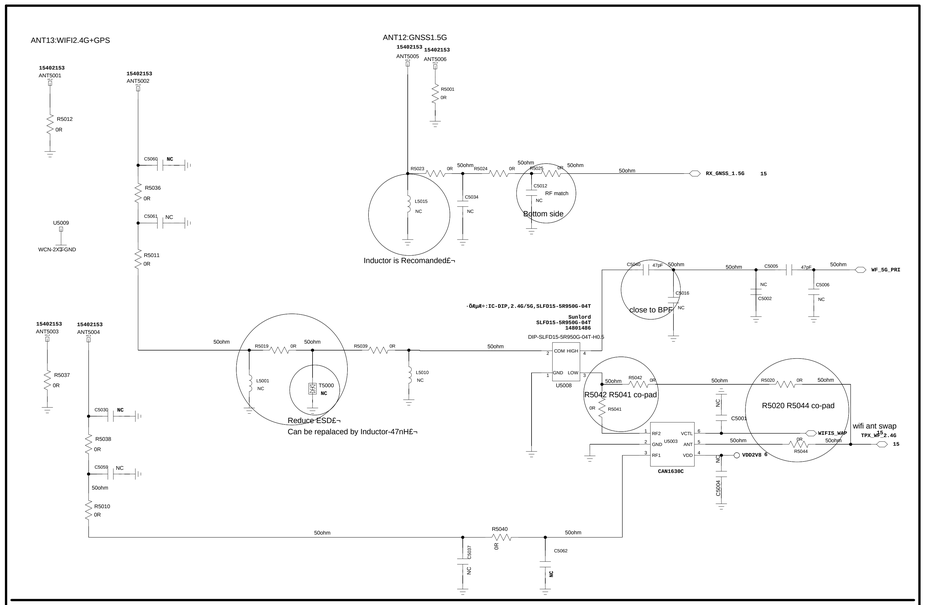
WIFI&BT ANT



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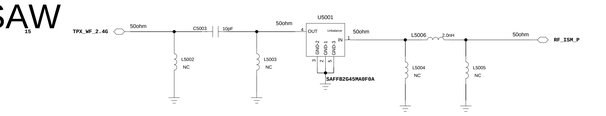
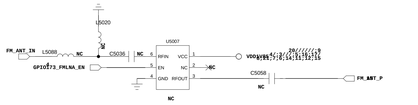


FM LNA



FMÍâ·ÄÄ¬ÈÏÊ¹ÓÃ·Ö¼¶Íâ·Å

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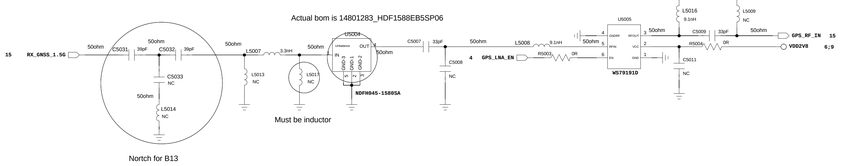
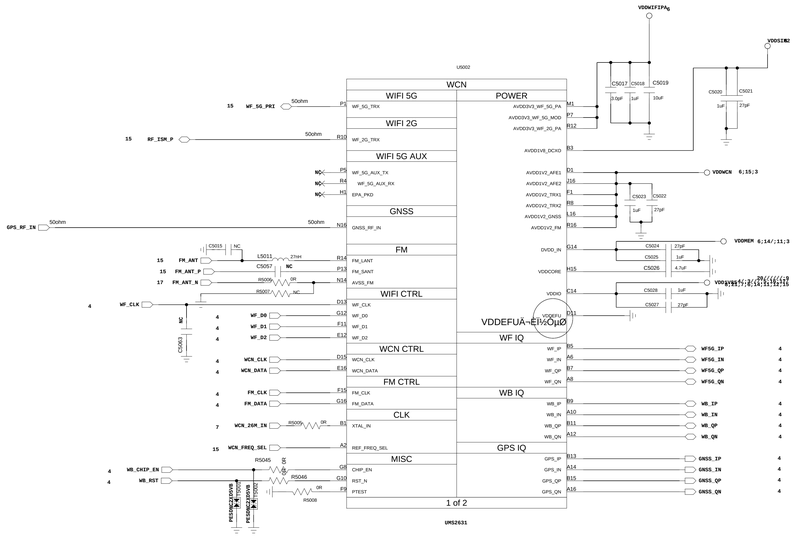
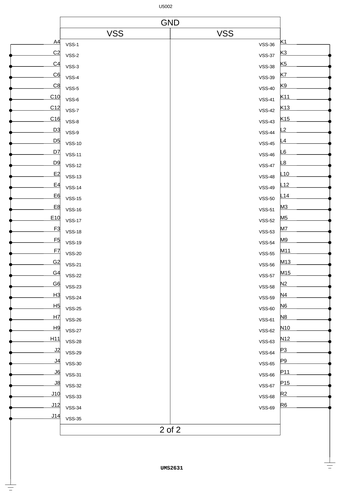
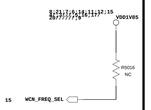




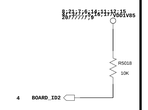


Bonding Option



²»ÒªÓÃVDD1V85ÉÏÀ-£¬ÕâÑùdeep sleep»áÂ©µç£¬Ö±½ÓÊ¹ÓÃÄÚ²¿ÉÏÀ-

FREQ_SEL---Internal Pull-up	
0	52MHz
1	26MHz



BOARD_ID2	WCN CLOCK
0	TCXO
1	TSX

COMPANY: TRANSSION HOLDINGS			MODEL:		Modified Date:
DRAWN		DATED	TITLE: K6735_F201_MAIN		28/05/2024:15:53
CHECKED		DATED < >	Confidentiality	CONFIDENTIAL	VERSION: SHEET: OF

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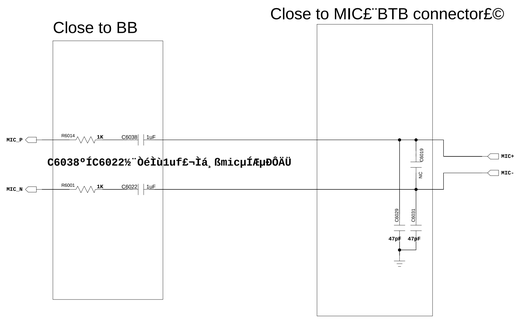
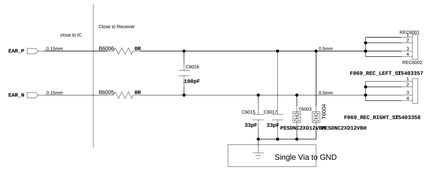
LTR	ECO NO:	APPROVED:	DATE:

MAIN MIC

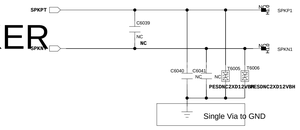


RECEIVER

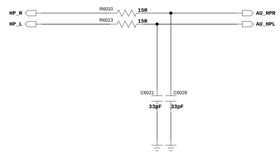


Manufacture	Part Number_Transsion
ÐÅÎ¾ÐÉ	15403357
	15403358
½«Ä¦Ì¨	15403455
	15403456

UP SPEAKER



3.5mm Ear Jack



Ear Jack suggest support:

Earphone	Pin 4 & Pin 5
Plug in	Connect
Pull out	Disconnect

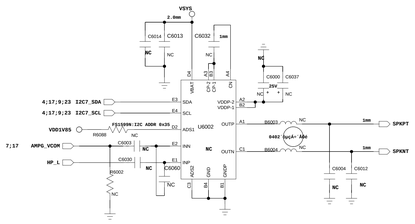
L,R,GND,MIC CTIA

C3153 B3105
Close to Ear Jack's Pin 2(GAD)

T3125 is reserved for better ESD defense of FM_ANT IO

UP SPEAKER

µÝÄ®°È	OCA72371	14701066
	FS1512N	14701053
È«Ä®°È	FS1599N	14701054
	AW87390	14701055

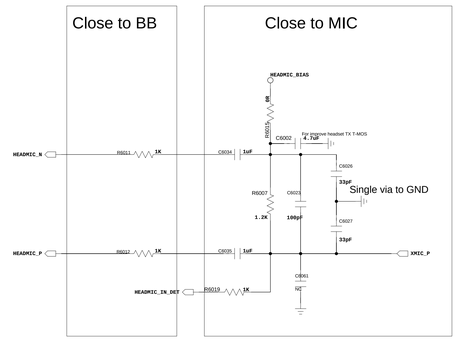


DWN SPEAKER

in SUB BOARD

HEAD MIC

¿¼ÂÇÔö¼Ó¶ú»úMICµØÏß



COMPANY: TRANSSION HOLDINGS			MODEL:		Modified Date:
DRAWN		DATED	TITLE: K6735_F201_MAIN		VERSION: 18/03/2024:20:53
CHECKED		DATED < >	Confidentiality	CONFIDENTIAL	SHEET: OF

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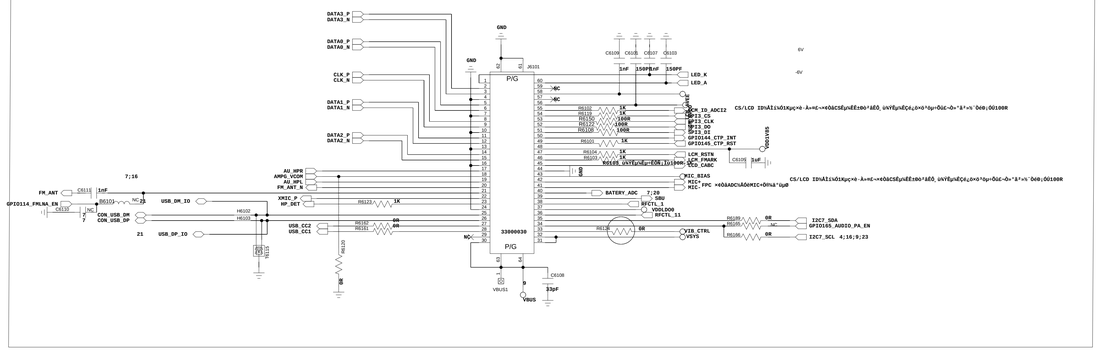
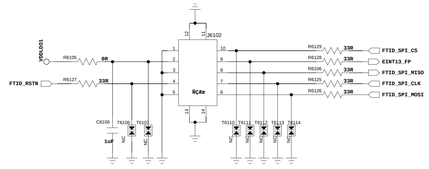
LCM



FingerPrint



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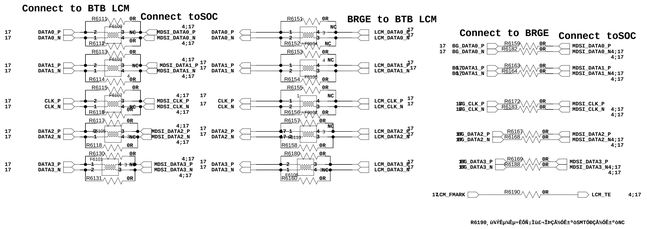

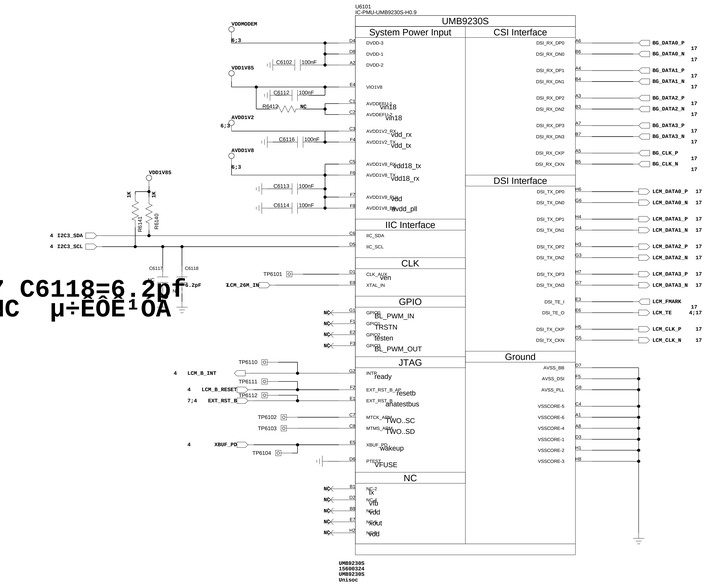

C6117 C6118=6.2pf
Á¬ÈINC µ÷ÈÕÉ¹ÓÃ

COMPANY: TRANSSION HOLDINGS			MODEL: K6735_F201_MAIN	Modified Date:	28/04/2024:14:04
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PERI_CAMERA_I

REAR CAM I (108/50M/13M/8M)

108M 1.0V@1100mA

50M 1.05V@ 260mA

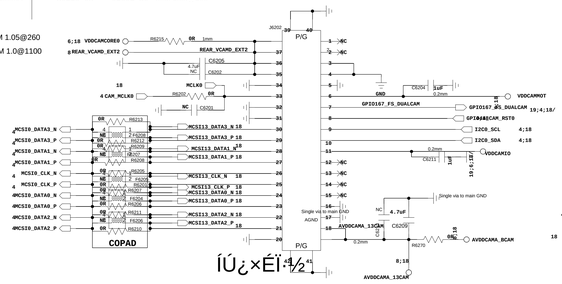
R6215=NC REAR_VCAMD_EXT2¹ÔµÇ U2602SMT108M 1.0@1100

R6215=0R U2602=NC 50M 1.05@260

		SENSOR	I2C Address	VCAMA	VCAMD	VCAMIO	AFVDD	SAP
dphy	50M	S5KJN1SQ03-FGH9	Write:0x20 Read:0x21	2.8V @67.7mA	1.05V @260.7mA	1.8V@ 4mA	2.8V@ 103mA	38pin
	50M	H-S02QU10	Write:0x44 Read:0x45	2.8V @68mA	1.1V @243mA	1.8V@ 22mA	2.8V@ 105mA	38pin
	108M	S5KJN1SQ03-FGH9	Write:0x20 Read:0x21	2.2V @120mA	1.00V @1100mA	1.8V@ 38mA	2.8V@ 103mA	DHPY
	13M	S5K3L6XX03-FG1	Write:0x20Read:0x21	2.8V @45mA	1.05V @150mA	1.8V@ 1.1mA	2.8V@ 125mA	17201836
	13M	OC13A2-WA2AA	Write:0x20Read:0x21	2.8V @45mA	1.2V @115mA	1.8V@ 5mA	2.8V@ 123mA	

50M 1.05@260

108M 1.0@1100



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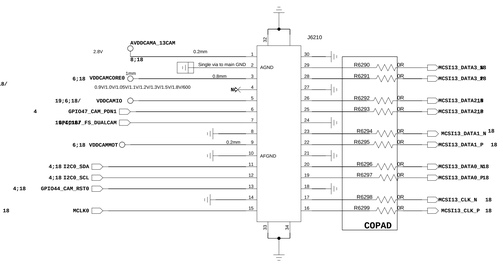
13M



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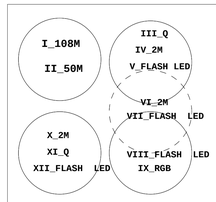


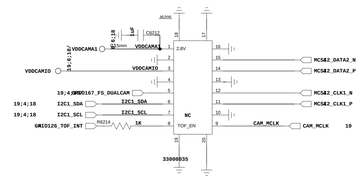
HOT50I&SMART10	I	II	VI		X XI
SPART B	I	II	IIIIV	VIII	X XI
S&P	I	II	V	IX	X XI
S&P	I	II	IIIIV	IX	XII
	I	II	IIIIV		
	I	II	IIIIV		VII

108M/50(0.64)/13M+2M/Q+2M/Q

2MÐé»¯

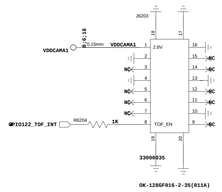
2M

	VCAMA	VCAMIO					SAP
Dé»¯	2.8V@40MA	1.8V@70MA					17201366



¹âÃÔ

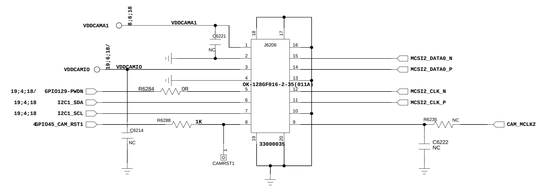
			VCAMA	PIN5	PIN8		SAP
¹âÃÔ			2.8	nc	out		17201729
			2.8				17201744
			2.8				17201845



2MÎ¢¾à

2M

	VCAMA	VCAMIO					SAP
Î¢¾à	2.8V@40MA	1.8V@70MA					17201867
	2.8V@40MA	1.8V@70MA					17201868



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COMPANY: TRANSSION HOLDINGS				MODEL: **K6735_F201_MAIN**	Modified Date: **01/04/2024:17:02**
DRAWN		DATED		TITLE:	VERSION: / SHEET: **18** OF **24**
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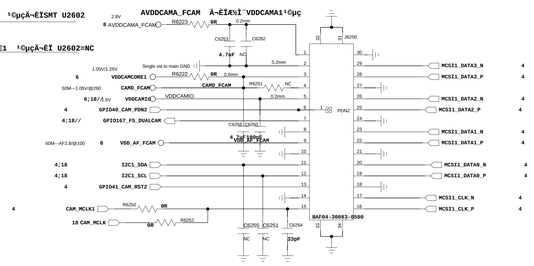
Front Camera (50M/32M/16M/8M/5M)

50M	R6222=0R CAMD_FCAM ¹©µçÃ¬ÈÎSMT U2602
32/8M	R6222=NC VDDCAMCORE1 ¹©µçÃ¬ÈÎ U2602=NC
32M	R6251=0R
8M	R6251=NC



	SENSOR	I2C Address	VCAMA	VCAMIO	VCAMIO	SAP	VCAMAF
50M	S5K,IMX(QS)-FGXX	Write 0x4C Read 0x4D	2.8V@ 67.7mA	1.05V @280.7mA	1.8V @4mA	17201850	2.8V@ _100mA
32M	GC32E1-W4J1XA	Write 0x54 Read 0x55	2.8V@55mA	1.2V @120mA	1.8V @5mA		NC
	HI-1631Q	Write 0x40 Read 0x41	2.8V @81mA	1.1V @179mA	1.8V @14mA	17201884	NC
8M	GC08A3-M0D00	Write 0x20 Read 0x25	2.8V @40mA	1.2V @110mA	1.8V @2mA		NC
	OV08D10-G4SA	Write 0x6C Read 0x6D	2.8V @50mA	1.2V@90mA	1.8V @1mA		NC

COMPANY: TRANSSION HOLDINGS				MODEL:		Modified Date:
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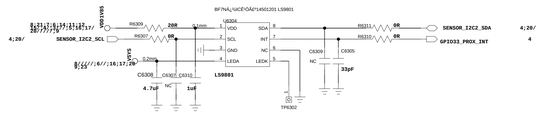
ALS & PROX

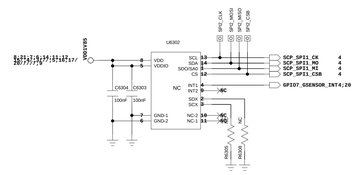
STK33562 I2C address: Write 0x8C, Read 0x8D

R6309µç×è²»½¨Ôé»»ÎÊªØR£¬Ö÷ÒªÊªªÁÉ½µµÍµçÓ´´øÁ´µÄµ×ÕëÎÊÈÎà



MN78912DKDN
STK33562
LS9801

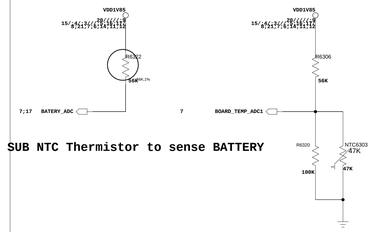
G-Sensor + Gyro Sensor



NTC

Thermistor to sense BATTERY_IC

Thermistor to sense BORAD_TEMP



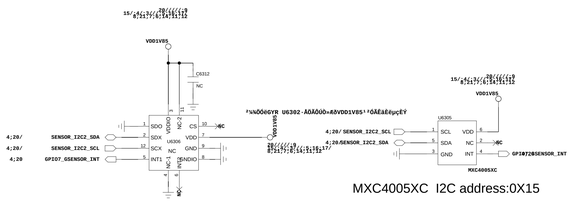
SUB NTC Thermistor to sense BATTERY

Thermistor to sense BORAD
board sensor£¨black side £©
BB IC---->>>>>£¨3----4mm £©
PMIC---->>>>>£¨3----4mm £©

G Sensor

DA218-B I2C address:0x4e/0x4f

STK8BA50-S I2C address:0X18

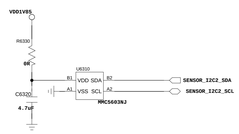


MXC4005XC I2C address:0X15

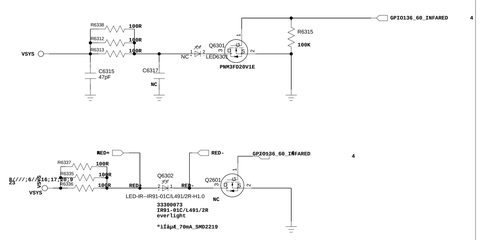
MC3413-P/MC3416 I2C address:0X98/0X99
SC7A20 I2C address:0X36/0X37

M-Sensor(COMPASS)

MMC5603 I2C ADDRESS:0x60(Write)/0x61(Read)
QMC6308 I2C ADDRESS:0x3B(Write)/0x3B(Read)



MMC5603
QMC6308



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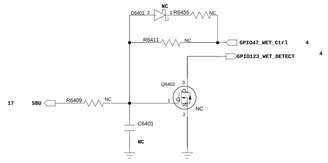
Exchange IO

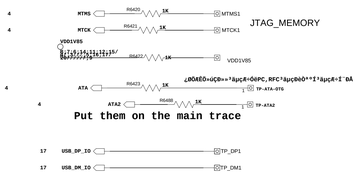
WET detect



Test Points

Bus/Vbat/GND1/BAT_ON/PWR/D+/D-/TX1/RX1/TX0/RX0·ÄÖÄBÃæ,ÇÒ¿ÉÊÉÓ

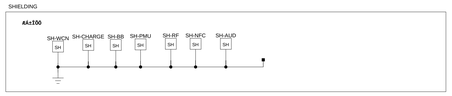
·ÃÆÁ±ÎÖÖÍáÃæÈÝÒ×º¸½Ó´¦



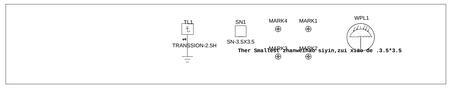
Put them on the main trace

FORCE DOWNLOAD

SHIELDING



LOGO&SN&MARK



Strapping pin

Ball name	Signal name in boot code	Value	Descriptions	Default vale
U1TXD	USB_DLOAD_EN	0	USB download(no time out)	1
		1	UART download	
ADI_SCLK	ARM_BOOT_MD2	0	SD boot(no time out)	1
		1	eMMC boot	

COMPANY: TRANSSION HOLDINGS			MODEL: K6735_F201_MAIN		Modified Date: 27/05/2024:20:48
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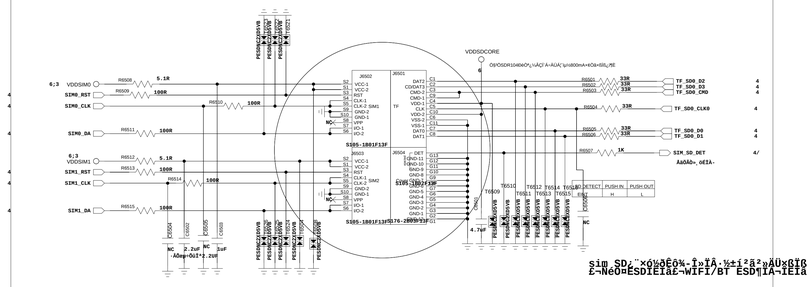
LTR	ECO NO:	APPROVED:	DATE:

SIM0 & SIM1 & TF



sim SD¿¨xO½ÕÉÖ¾-Î»ÏÃ¸½±ì²â²»ÄÜxGIG
£¬NeO=ESDIEIaÈ¬WIFI/BT ESD¶IA¬IEIa

Volume Key

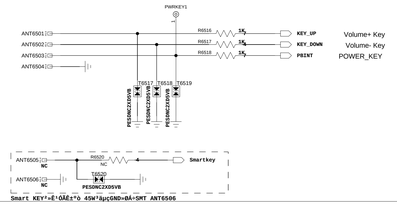
POWER KEY

Press KEYIN0+KEYOUT0/GND when power up
will make the phone boot from USB

KEY Volume

Spport two keys 7S reset ,also support single key(PBINT) 7S reset

Two keys Reset :EXTRSTN+PBINT



COMPANY: TRANSSION HOLDINGS				MODEL: K6735_F201_MAIN	Modified Date 27/05/2024:10:16
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LTR:	ECO NO:	APPROVED:	DATE:

NFC

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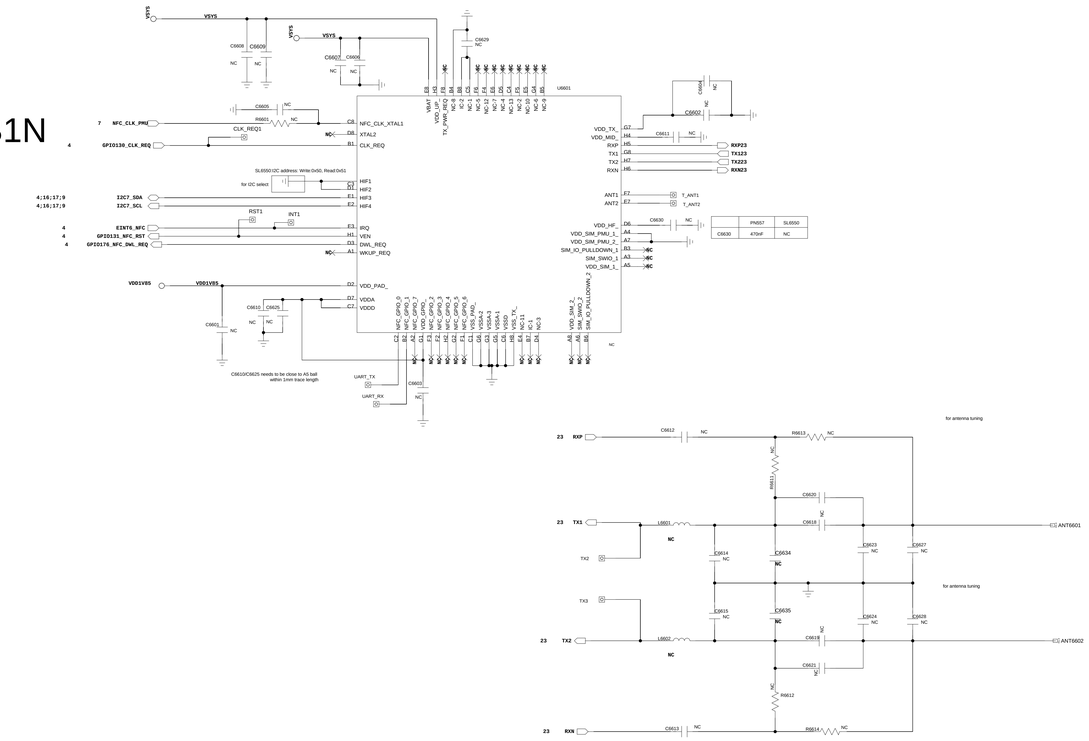
34200040

THN31FGB1N



	PN557	SL6550
C6630	470nF	NC

COMPANY: TRANSSION HOLDINGS				MODEL: K6735_F201_MAIN	Modified Date: 08/03/2024:15:36
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LTR	ECO NO:	APPROVED:	DATE:

COMPANY: TRANSSION HOLDINGS				MODEL: **K6735_F201_MAIN**		Modified Date:	**08/03/2024:15:12**
DRAWN		DATED		TITLE: **CHANGE_LIST**		VERSION:	SHEET: **24** OF **24**
CHECKED		DATED	< >	Confidentiality	CONFIDENTIAL		