# Lincoln School of Computer Science University of Lincoln CMP3752M/CMP9773M, Parallel Programming

## **Parallel Patterns in OpenCL**

The tutorial code is hosted on <u>GitHub</u>. You can either download and extract the repository from a zip file (the green "Clone or download" button) or clone to a local directory by issuing 'git clone https://github.com/gcielniak/OpenCL-Tutorials.git' command from the command line. This time we will need two projects, the original Tutorial 1 and new Tutorial 2. Refer to the previous tutorial descriptions for more information about the workshops and their structure, and also for more technical details on OpenCL.

#### 1 Kernel Execution Model

Let us now look in more detail at the inner workings of OpenCL kernel execution. As mentioned in the previous tutorial, a "device" will run its code on "computing units" which in turn consists of "processing elements" (see Fig. 1). Each separate kernel execution is performed on a processing element and in OpenCL terminology it is called a "work item". You can think about work items as separate threads. In our basic example we have used vectors of length 10, so the kernel launch in this case will consist of 10 work items.

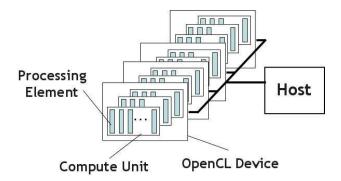


Figure 1 Each OpenCL device consists of at least 1 Compute Unit, which in turn contains one or more Processing Elements (after <a href="https://github.com/HandsOnOpenCL">https://github.com/HandsOnOpenCL</a>).

Each computing unit consists of a fixed number of processing elements which naturally limits the number of work items which might be executed at the same time. In addition, the work items often need to communicate between each other (e.g. share the same variable, report the intermediate results, etc.) and therefore OpenCL introduces a concept of "work groups" which are collections of work items executed by a single compute unit. Finally, to enable work on larger problems, the device will launch as many work groups as possible at the same time, depending on the number of the existing compute units. This process will be repeated until all the necessary work items associated with the problem are completed (see Fig. 2).

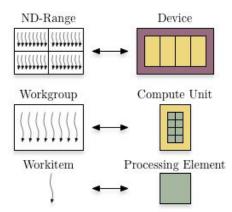


Figure 2 The hierarchy of execution: a problem is executed on a device, a workgroup on a compute unit and a work item on a processing element (after <a href="http://mygsoc.blogspot.co.uk/">http://mygsoc.blogspot.co.uk/</a>).

Let us see some examples to get a better intuition about these terms. For that purpose, we will need to be able to inspect the inner workings of individual kernels. A simple debugging facility is provided by the printf function which can be used to output kernel variables used by each work item. The formatting used by this function is exactly the same as in printf used in other languages (see <a href="https://en.wikipedia.org/wiki/Printf\_format\_string">https://en.wikipedia.org/wiki/Printf\_format\_string</a> for more details). The following example shows how to use this function for printing out the id variable from our simple vector addition kernel:

```
int id = get_global_id(0);
printf("work item id = %d\n", id);
```

Open the solution file, navigate to Tutorial 1 and add the code above. If you run the program, you will see the id values for each consecutive work item being displayed. We can also now better understand the purpose of the get\_global\_id function: it returns a unique id for each work item from 0 to N-1 where N is the total number of work items required to solve a particular problem. Be careful with the printf function though — if you use it on larger problems the function might interfere with the execution of kernels and even freeze your computer!

Task4U: Run the modified kernel on different devices and note the order of the execution of work items. Is it the same for each device and each run?

In general, work items are executed in an arbitrary order so one cannot make any assumptions which work item is executed first.

OpenCL specifies a number of functions similar to get\_global\_id which help to identify different launch parameters (see <a href="https://www.khronos.org/registry/OpenCL/specs/opencl-1.2.pdf#page=244">https://www.khronos.org/registry/OpenCL/specs/opencl-1.2.pdf#page=244</a> for more details). For example, get\_local\_id returns back a work item id within a specific work group. The values vary from 0 to M-1 where M is the size of a work group. This size can be determined by calling get\_local\_size. Now, add the following lines to your code and run the program again and note the differences between local and global ids but also work group size for different devices.

```
int loc_id = get_local_id(0);
printf("global id = %d, local id = %d\n", id, loc_id); //do it for each work item
```

Task4U: Print out the total number of work items for a given problem (just once) and a work group id for each work item. Note the differences between devices.

The work group size in our example varies depending on which device it is running on. This is due to the fact that our kernel execution command has left out the fourth parameter as empty (c1::NullRange) which is signalling to OpenCL that this value should be set automatically. It is possible however to define the work group size (or local size) manually when calling the kernel from the host:

```
queue.enqueueNDRangeKernel(kernel_add, cl::NullRange,cl::NDRange(vector_elements),
cl::NDRange(local_size));
```

There is a requirement imposed by OpenCL 1.2 that the input data size should be divisible by the work group size and therefore not all local sizes are permitted. There are several ways to overcome this limitation which we will see in future workshops, but at the moment we will only work with datasets which meet that condition.

Different kernels will have different preferred work group size (due to device architecture, memory usage, etc.). The recommended values for the work group size can be identified by reading a kernel property called CL\_KERNEL\_PREFERRED\_WORK\_GROUP\_SIZE\_MULTIPLE. This parameter can be accessed in the following way in the host code:

```
cl::Device device = context.getInfo<CL_CONTEXT_DEVICES>()[0]; //get device
cerr << kernel_add.getWorkGroupInfo<CL_KERNEL_PREFERRED_WORK_GROUP_SIZE_MULTIPLE>
(device) << endl; //get info</pre>
```

This value is the smallest work group size suggested, but its multiples are also possible, up to the maximum work group size specified by another kernel parameter which can be accessed in the following way:

```
kernel_add.getWorkGroupInfo<CL_KERNEL_WORK_GROUP_SIZE>(device)
```

Task4U: Run the vector addition kernel on different devices for the work group size equal to the value specified by the preferred work group size parameter and note the differences in local and group ids. You will need to manually adjust the length of the input vector so it is divisible by the work group size.

The work groups play an important role in applications which require work items to communicate with each other. We will see such examples in the following tutorial sessions.

### 2 Parallel Pattern - Map

Parallel programming patterns define standard abstract components from which larger algorithms and programs can be built. One of the most straightforward parallel patterns is the Map (see Fig. 3).

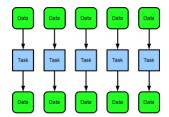


Figure 3 The Map parallel pattern (after <a href="http://parallelbook.com/sites/parallelbook.com/files/">http://parallelbook.com/sites/parallelbook.com/files/</a> <a href="http://parallelbook.com/sites/parallelbook.com/files/">SC13 20131117 Intel McCool Robison Reinders Hebenstreit.pdf</a>).

The map pattern describes the same computation performed on different data without the need for communication between work items. So far in these workshops, we have been working solely with the map pattern, even if we have not mentioned that explicitly. Let us now see how to apply the map pattern to solve practical problems.

For this purpose, we will use some simple applications in digital image processing due to the fact that the results of operations on digital images can be easily visible and provide instant feedback. This is however not, by any means, a tutorial on digital image processing and we will only use these examples as illustrations to generic problems in parallel processing.

The second project in the solution folder called Tutorial 2 is setup to enable I/O operations involving digital images. The framework is based on one a lightweight multi-platform image processing library called CImg to enable simple visualisation and interaction with the user. A digital image in our project is represented as a byte array (CImg<unsigned char>) containing pixels storing the intensity level for each colour channel. CImg stores pixels for each colour channel (RGB) separately, so that the pixels for red channel are stored first, followed by the green and then blue channel (see Fig. 4). The image has the width and height properties defining rows and columns and the spectrum property which specifies the number of colour channels (3 in our case). Currently, only simple ppm images are supported, so if you want to use your own images in the project, you need to convert them into that format first using one of the image manipulation tools (e.g. Gimp).

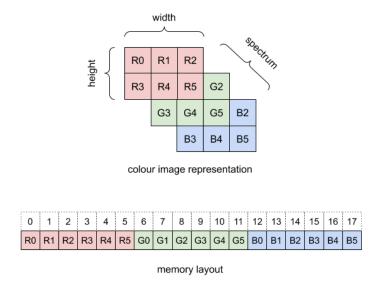


Figure 3 An example Clmg image.

To build the code, you need to switch the active project to Tutorial 2 (right-click on the project in the Solution Explorer/Set as Start-up Project). The code is very similar to Tutorial 1 with additional functions for image loading and display, and changes to variables. This time instead of input and output vectors we have two images: image\_before which is our input and image\_after — our output. Before running the code, study it first and note the differences to the host program from Tutorial 1. The provided kernel code demonstrates a simple copy operation so the output image is exactly the same as the input after the execution of the program. There are two images provided with the code: smaller version called "test.ppm" which is good for development and testing and "test\_large.ppm" for evaluation and analysis.

The identity kernel treats the image as a long 1D array (see Fig. 4) and operates on each byte of the image, without distinguishing between different colour channels.

Task4U: Write a kernel code called filter\_r which will perform colour channel filtering - a template for that kernel is already provided. The output image should only contain values for red colour component and all other components (GB) should be set to 0. Try out the filter for other colour channels too.

Task4U: Write a kernel code called invert which will invert the intensity value of each pixel (e.g. 255 becomes 0, 0 becomes 255, 100 becomes 155, etc.).

Task4U: Profile the image inversion code on a large image (i.e. test\_large.ppm) for at least three different work group sizes. Compare the results to those obtained with the default settings used by OpenCL. Which configuration produced the best results?

Task4U: Write a kernel function called rgb2grey which would convert an input colour image into greyscale. Keep the RGB format of the output image but set each RGB channel to the same value of Y = 0.2126R + 0.7152G + 0.0722B.

#### 3 Parallel Pattern - Stencil

A stencil pattern takes multiple data inputs from a pre-defined neighbourhood and combines them into a single value (see Fig. 5). Similarly to the map pattern, each task/work item is independent. This pattern has several important applications in signal processing such as noise filtering, image blurring/sharpening, etc.

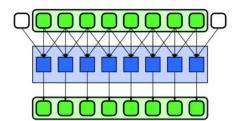


Figure 5 The Stencil parallel pattern (after <a href="http://parallelbook.com/sites/parallelbook.com/files/">http://parallelbook.com/sites/parallelbook.com/files/</a> SC13 20131117 Intel McCool Robison Reinders Hebenstreit.pdf).

#### 3.1 1D Stencil

Let us go back to our project Tutorial 1 so we can have a look at a simple 1D example first. In the kernel file, you can find a kernel function called avg\_filter which performs an averaging operation

in a local window of size 3 (or range 1). The result is an average of values at the central position and the immediate neighbours to the left and right. This operation is equivalent to a simple smoothing filter which can be applied to filter out noise from data (e.g. audio signal). First, adapt the host code so it can run the kernel and then test its operation on different input values.

Task4U: Modify the code such it uses a window of size 5 (or range 2). How does this larger window affect the smoothness of the signal?

Task4U: Currently, the kernel is not handling the boundary conditions. In our case, the leftmost and rightmost vector elements (i.e. id = 0, id = length-1) have one neighbour less and require a different treatment. Add the boundary handling to the kernel and compare the results with the original implementation.

#### 3.2 2D Stencil

So far we have worked with one dimensional arrays only. In certain applications, however, it is more convenient to specify the problem in more dimensions. For example, when dealing with digital images it is easier to refer to pixel x and y coordinates rather than their location in a 1D array. Kernel dimensionality is specified within a kernel execution command. In the 2D case, the first parameter of the NDRange function specifies the number of columns (width) and the second one the number of rows (height). For example, to call a 2D kernel with data arranged into a 5x2 array, the following modification to the host code is required:

```
queue.enqueueNDRangeKernel(kernel_add, cl::NullRange, cl::NDRange(5, 2),
cl::NullRange);
```

The kernel code also needs to be modified. The global indices are now arranged into two dimensions and the get\_global\_size(d) function will return the number of elements along the d<sup>th</sup> dimension. In our example, for d=0 this value is 5 and for d=1 it is 2. These indices can be combined into a global linear index as it is demonstrated in the kernel code called add2D.

Task4U: Modify the host code in Tutorial 1 to run the 2D kernel add2D. Run the kernel in a 5x2 arrangement and see if the results are exactly the same as in the 1D case. Inspect global indices and sizes along both dimensions.

Task4U: Modify the host code in Tutorial 2 to run the ND kernel identityND. Run the kernel in a 3D arrangement with image width, height and spectrum (colour channel) specifying values for 3 dimensions. See if the results are exactly the same as in the 1D case.

#### 3.2.1 Averaging filter

Let us now look at an averaging filter in 2D. The kernel code avg\_filterND in Tutorial 2 performs a similar function to the 1D averaging filter developed in the previous task. The filter calculates an average of 9 pixels in a 3x3 local window. This operation results in a blurred version of the input image.

Task4U: Adjust the **host code** such that the 2D averaging filter kernel can be run on the input image. Specify image.width(), image.height() and image.spectrum() as values for kernel dimensions. Inspect the kernel code (note how 2D indices are used in the loop), run the code and visually test the results.

Task4U: Modify the kernel code such it uses a window of size 5x5 (or range 2). How does this larger window affect the results?

#### 3.2.2 Convolution filter

The averaging filter can also be implemented as a generic image convolution operation. Image convolution uses a rectangular mask of weighting values (also called a convolution kernel) applied to every pixel and its neighbourhood. The result of this operation is a weighted sum of pixel values in the local neighbourhood. Each pixel is multiplied by the corresponding mask value and added together (see <a href="https://en.wikipedia.org/wiki/Kernel (image processing)">https://en.wikipedia.org/wiki/Kernel (image processing)</a> for an illustrative example). Our averaging filter can be simply expressed as a 3x3 mask with all entries equal to 1/9. The result of such a convolution operation is exactly the same as for the avg\_filterND. There are many other combinations of mask values which result in different image processing operations including sharpening, soft blurring, edge detection, etc. There are several examples of such masks listed in the following Wikipedia entry: see <a href="https://en.wikipedia.org/wiki/Kernel (image processing)">https://en.wikipedia.org/wiki/Kernel (image processing)</a>. Tutorial 2 has a simple implementation of a 3x3 convolution filter provided in the kernel file. You will notice that the mask is provided as an additional input argument and initialised in the host code. To run the convolution example, you need to modify the host code by uncommenting the respective code lines responsible for allocating and transferring the mask values to the device.

Task4U: Study the averaging convolution kernel and run the code. Compare the blurring results with those obtained from the avg\_filterND kernel. You may want to use screenshots to perform direct visual comparison.

Task4U: Try out other convolution masks such as those specified in the mentioned Wikipedia article.

#### 4 \*\* Additional Tasks

The following tasks are designed for those eager students who finished all tasks and would like an extra challenge - the instructions provided are deliberately sparse! These tasks are optional and not necessary to progress further in the following tutorials.

Task4U: Write a 1D averaging filtering kernel avg\_filter which will accept an additional parameter defining the radius. Make sure that boundary conditions are correctly considered for different radius size. Compare the results to the original fixed radius implementations from Task 3.1.

Task4U: Write a kernel function brightness which in addition to input and output images will also accept a separate integer parameter called beta. This value should be added to the intensity value of each colour component. This is a simple brightness adjustment function. Test your program on a larger test image for different values of the brightness parameter.

Task4U: Write a kernel function called contrast which will adjust the contrast of an input image. The contrast value should be specified as a fLoat parameter which then should be multiplied by each colour component. Try different values of the parameters on your input image.

Task4U: Modify the kernel function avg\_filterND such that it accepts an additional parameter called range defining the size of the averaging window. As an additional challenge, add proper handling of boundary conditions.

Task4U: Modify the kernel function convolutionND such it accepts an arbitrary mask size specified by an additional parameter called mask\_size. Try the new functionality with larger convolution masks.