

Addressing MUXes

Title		
ReRAM Board		
Size	Number	Revision
A4	1	1
Date:	23.08.2015	Sheet 3of 7
File:	\\.\AddressingMuxes 1.SchDoc	Drawn By: Armando Bisconti

A

B

C

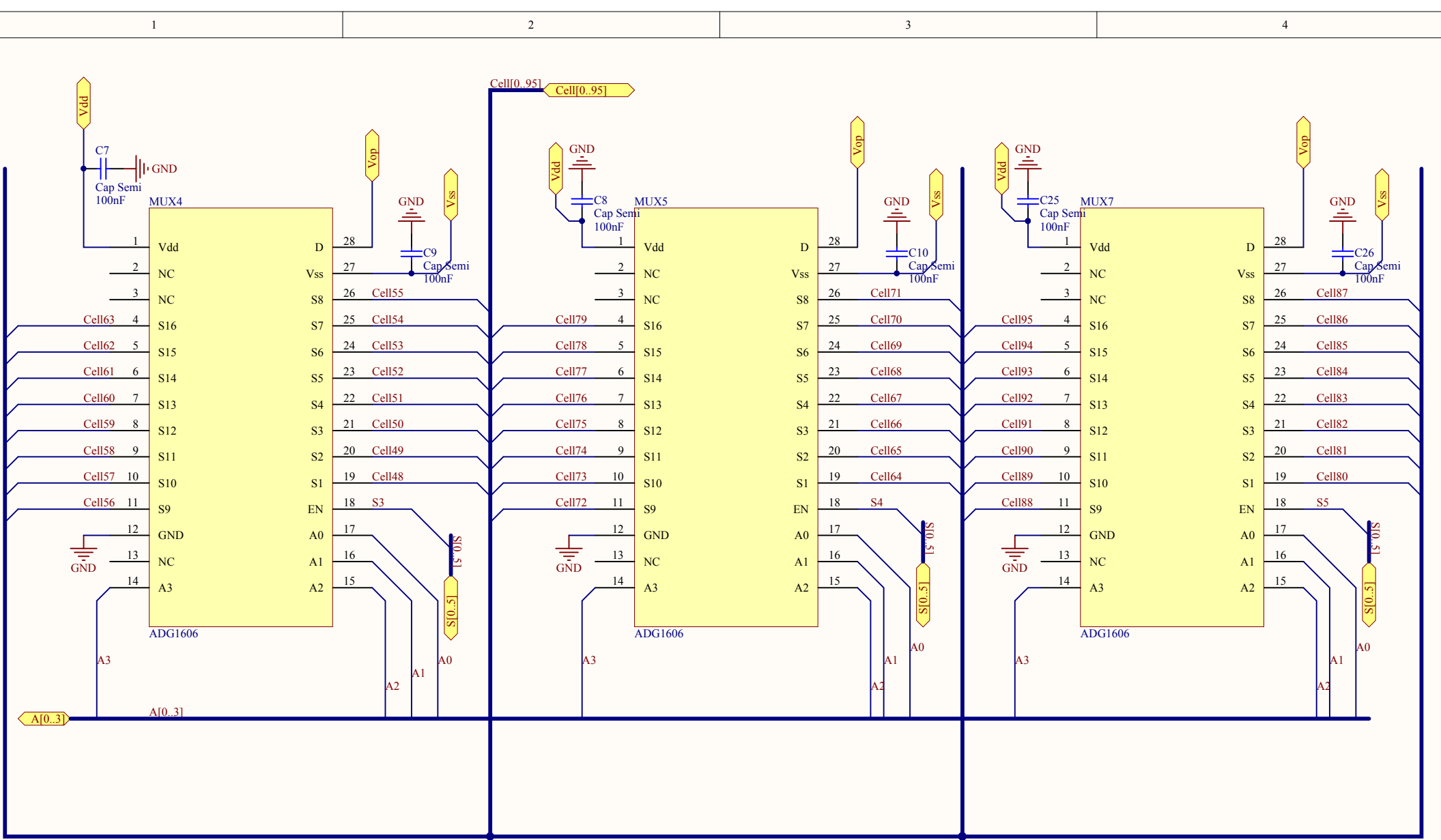
D

A

B

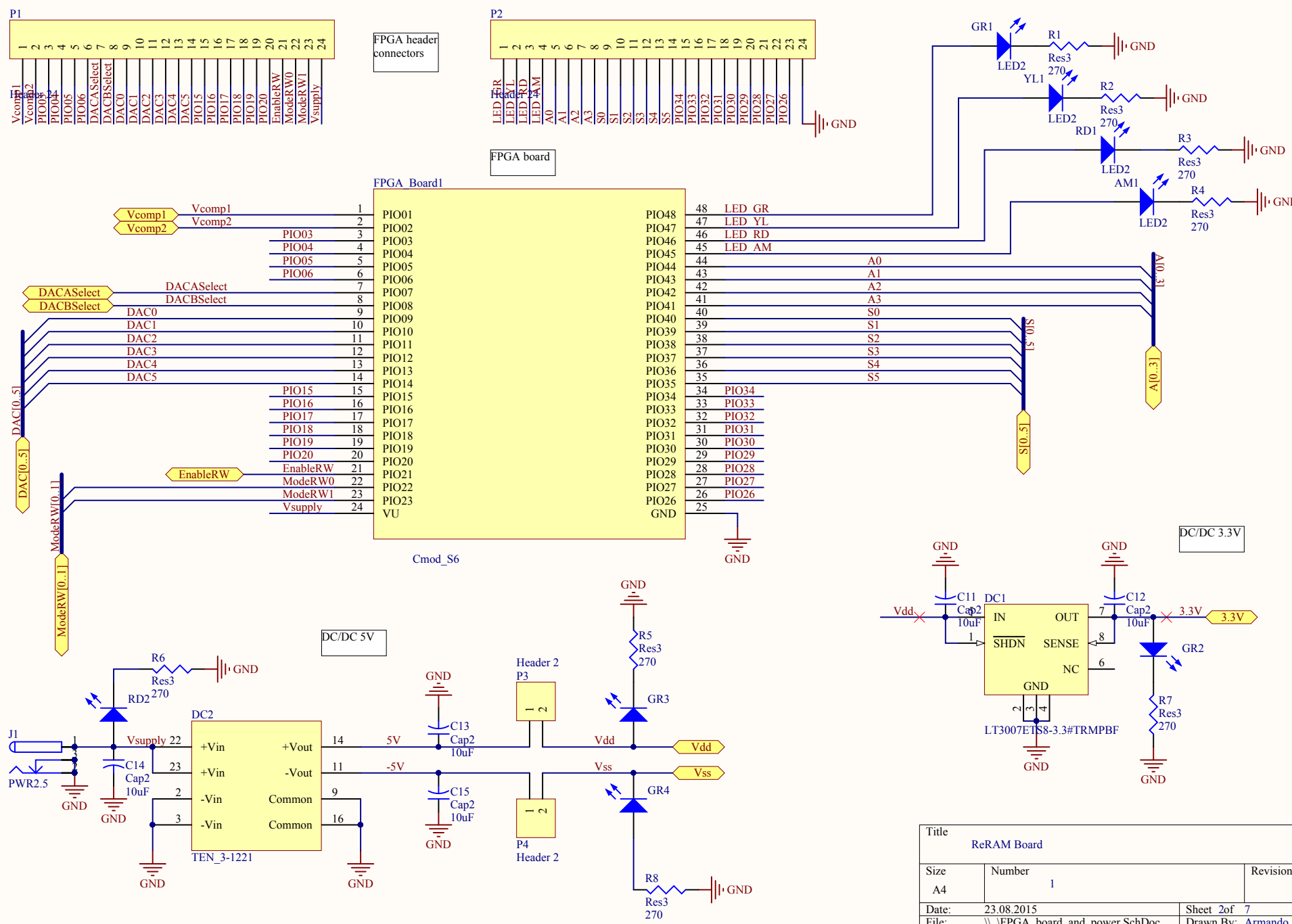
C

D



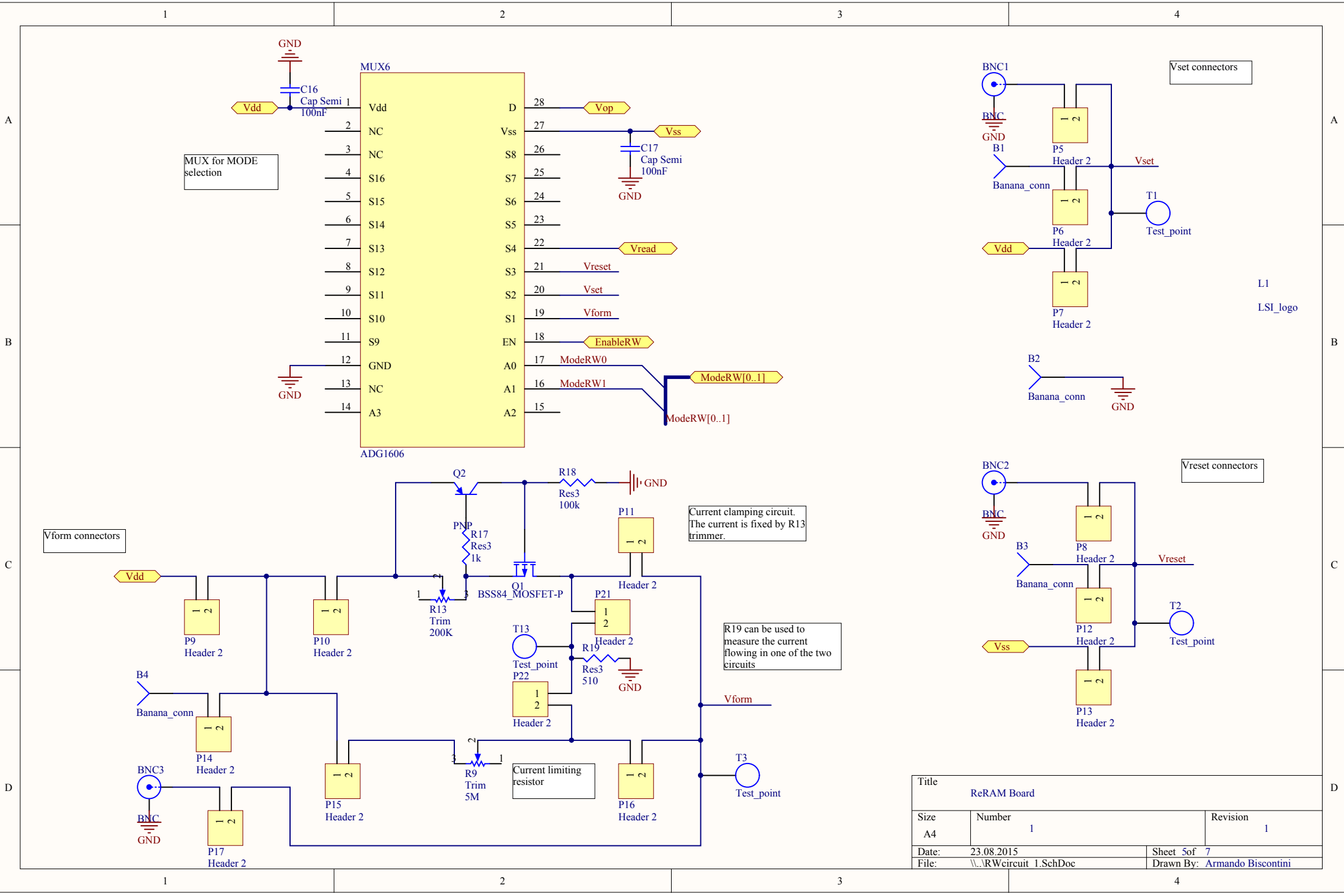
Addressing MUXes

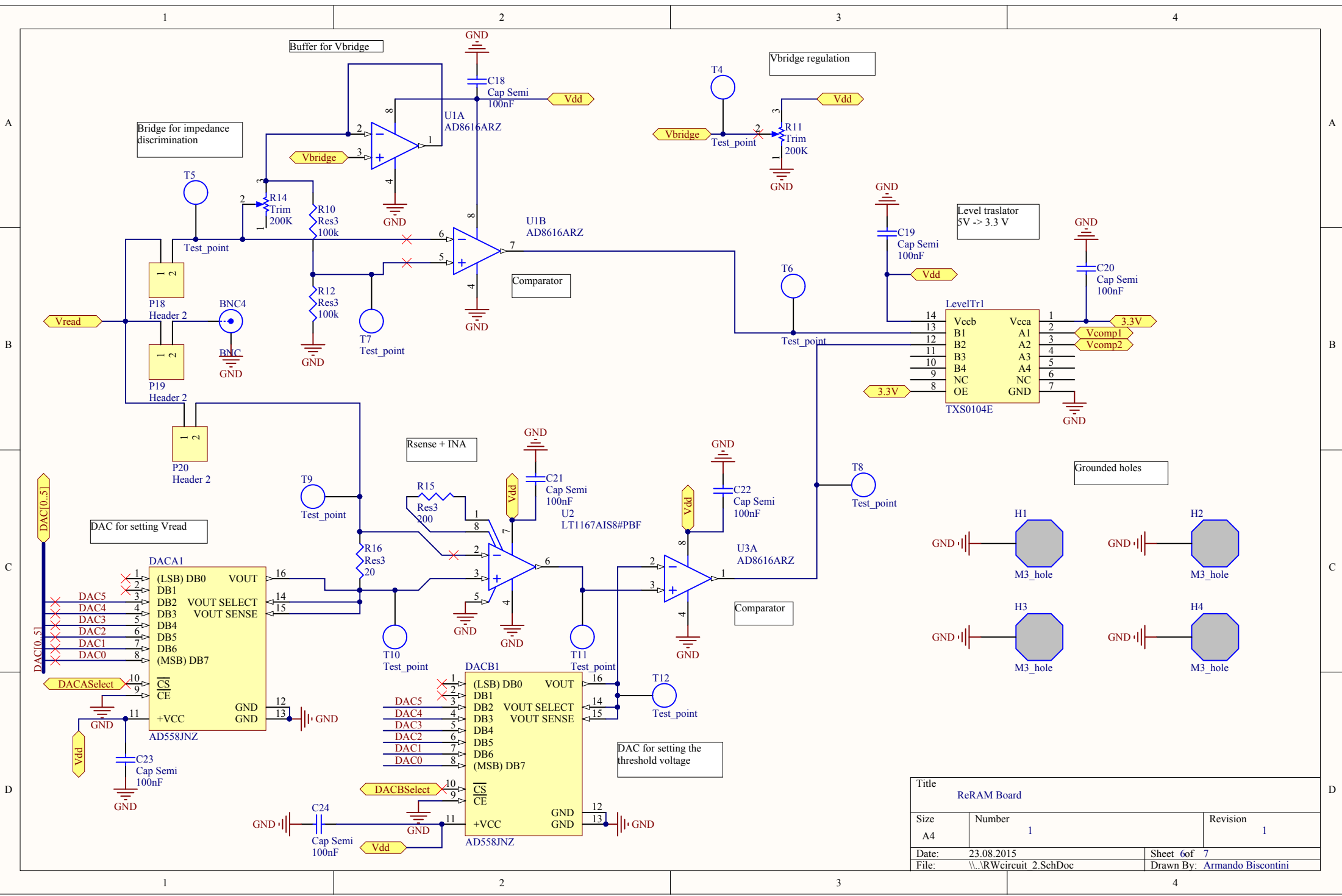
Title		
ReRAM Board		
Size	Number	Revision
A4	1	1
Date:	23.08.2015	Sheet 4of 7
File:	\\.\AddressingMuxes 2.SchDoc	Drawn By: Armando Biscontini



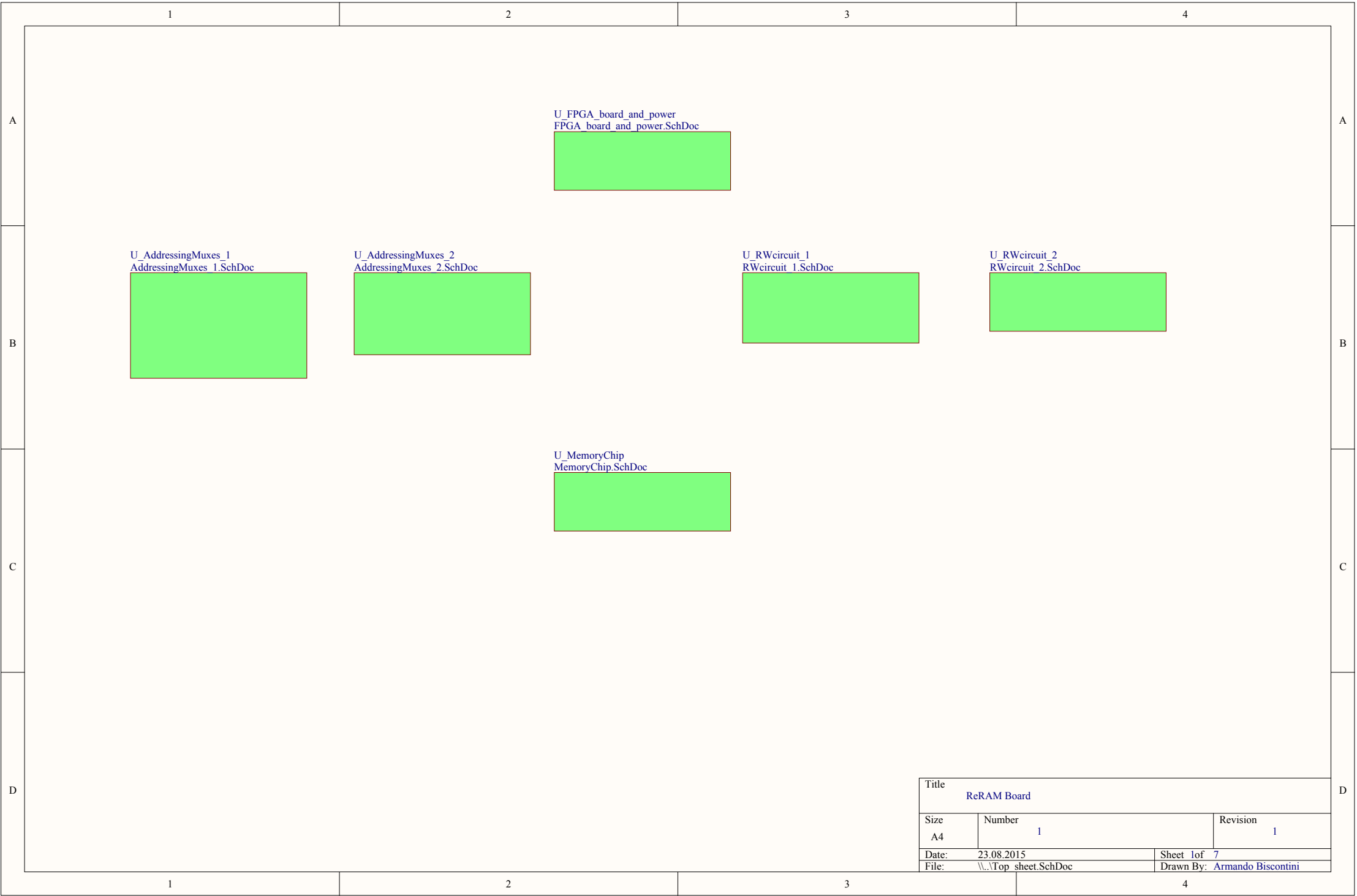
Title		
ReRAM Board		
Size	Number	Revision
A4	1	1
Date:	23.08.2015	Sheet 2of 7
File:	\\.\FPGA board and power.SchDoc	Drawn By: Armando Biscontini







Title ReRAM Board			
Size A4	Number 1	Revision 1	
Date:	23.08.2015	Sheet 6of	7
File:	\\.\RWcircuit 2.SchDoc	Drawn By:	Armando Biscontini



ReRAM Board Rev.1