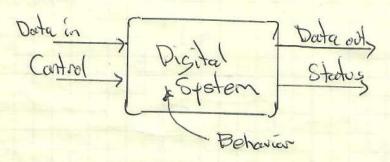
Cambinational logic building blocks

Refine our view of the input to output of the transformation of input to output data is determined by control inputs. Exceptional processing events are reported by status outputs.



NoM Deceder

Data inpot : 1-bit D

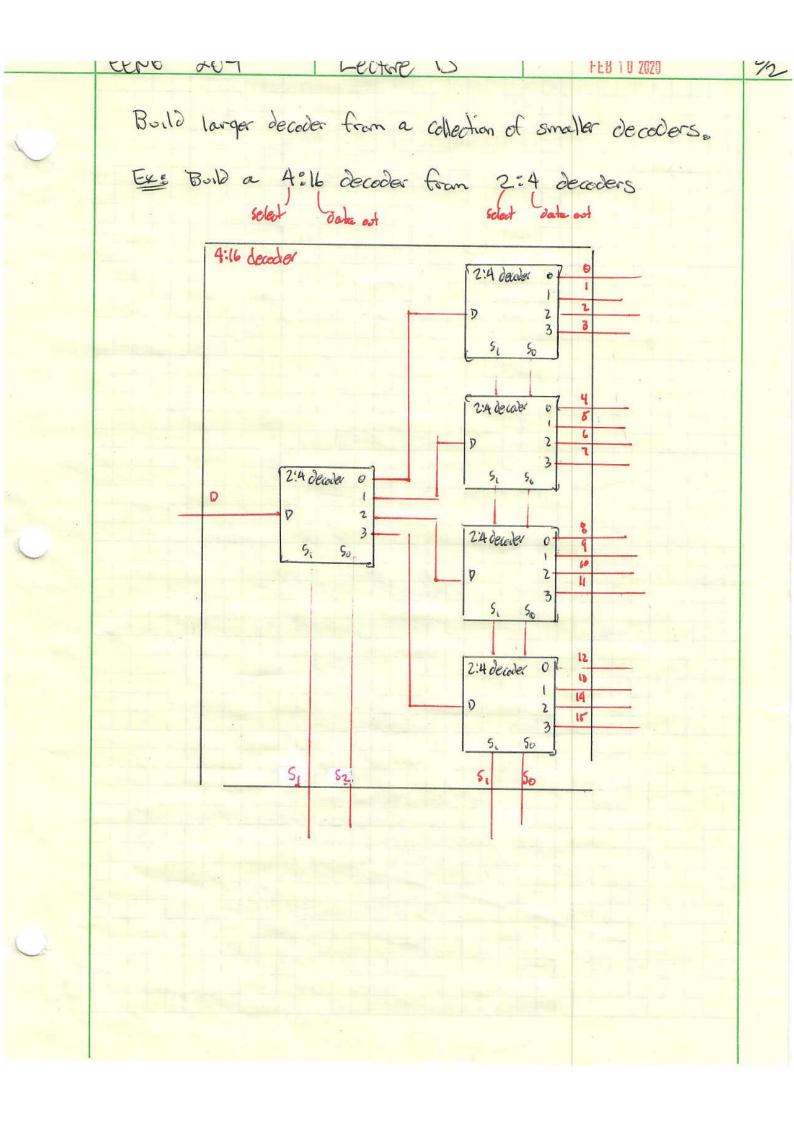
Data outpot & Mbts Y = your one y, yo

Status : None

Behavior & ys = D, all other y = 0

Troth Table for 2:4 decoder

	5,	50	D	13	1 42	190	1 40	
	0	0	0					40 = 5, 50 D
2	0	0	1				1	3
-	0	ι	0					41 = 5,50 D
1	0	(1			i		01
	1	0	0					W2 = 5, 5, D
	1	0	l		1)c 1,00 A
	1	- 1	0					42 = 5, 5, D
	1	1						05
				11	l			



X = A*B°

a	a	6.	100	1/2	X	X	Yo.
O	0	0	0	ė	0	0	0
0	0	0	1	0	O	0	0
0	0	i	o	Ú	0	õ	6
0	0	(1	0	0	0	0
0	1-	0	0	0	0	0	D
O	Ţ	6	1	0	0	0	1
0	1	1	b	0	0	1	0
0	1	1	1	0	0	i	1

o Determine the ROS min expression for FCA, B, C) = TTM(0,1,4,6)

Step		12	13	1 4	1 5 1	6
Function	TTM	Zm	Kmap	Kmap	Sopmin	Posmin
Form	F	F	F	Fi	F'	F