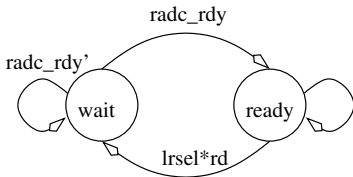
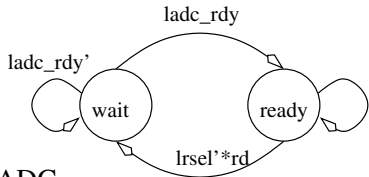


LEFT RIGHT



ADC

DAC

