How To 2

Creating a Project

1. Select an appropriate working directory for your project. I would recommend selecting your network drive.
   a. Create a new folder for your project *projectFolder*,
   b. Download any provided Verilog files into *projectFolder*,
2. Start Quartus II 18.1 (64-bit).
3. Select *File -> New Project Wizard.*
4. In the **Directory, Name, Top-Level Entity** page of the New Project Wizard pop-up:
   a. To the right of the "What is the working directory" box click the . . . button,
   b. In the Select Folder pop-up, navigate so you can see the andgate2 directory created in step 1,
   c. Select the *projectFolder* folder, click Select Folder,
   d. In the "What is the name of this project" field type *projectName*
   e. click *Next.*
5. In the **Project Type** page of the New Project Wizard pop-up:
   a. Select the *Empty project* radio button,
   b. click *Next.*
6. If you have given Verilog files, in the **Add Files** page of the New Project Wizard pop-up:
   a. Click the . . . button to the right of File name,
   b. In the Select File pop-up, navigate to, and select, *projectFiles.v*, click Open,
   c. The file should appear in the window below,
   d. Click *Next*
7. In the **Family & Device Settings** page of the New Project Wizard pop-up:
   a. Device family, Family: Cyclone V
   b. Package: FBGA
   c. Pin Count: 672
   d. Speed Grade: 7_H6
   e. Select *Specific device selected in 'Available devices' list*
   f. From the list of available devices, select: 5CGXFC5C6F27C7
   g. Click *Next*
8. In the **EDA Tool Settings** page of the New Project Wizard pop-up:
   a. In the Simulation row

          i. Tool Name column: ModelSim-Altera

          ii. Formats column: Verilog HDL

    b. Leave other defaults alone

    c. Click Next

9. In the **Summary** page of the New Project Wizard pop-up:

    a. Review information,

    b. Click Finish.

10. Back in the main Quartus II window, Click *Tools -> Options...*

11. In the Options pop-up:

    a. Select *EDA Tool Options* from the Category menu,

    b. If the last row, "ModelSim-Altera" is blank, click on the . . . button at right and navigate to the *C:\intelFPGA_lite\18.1\modelsim_ase*, select the *win32aloem* folder, the click Select Folder,

    c. Click Ok.