

* Create model interface & internal logic

module onesto Dense (throw, play);
output were [1:0] play;
input were [2:0] throw;
Ustuff 7

endmodale

* Instantiate modules

medale rps Game (pl Throw, 000);

input wire [2:0] pl Throw;
wire [1:0] pi Dense;

ones to Dense plo2d (pl Throw, pl Dense);

end module

- a names outside modele may be different inside modele
- · logic inside mode cannot l'see " signals ootside.