GND GND
C56 R1 10 C6 100n VS+
10n GND 7
2
C R3 50 1 RG_2 R6
U2
INA828IDR 6
8 RG
SMA_IN1 R2 1M GND 5
3 100n C7 VS- GND
GND

VREF1-2
JP3 GND C4 100n VS+
2 7
RG_2 R4 1 U3
5.49k INA828IDR 6 50 C10 SMA_OUT1
8 RG R9
GND 100n C5 VS- 5 GND
3
GND
U4
OPA191IDBVT C9
1 OUT V+ 5 100n
2 V- IN- 4 VS+
VS- C8 IN+ 3
100n R5 1M or 0
GND GND

GND GND
C57 R7 10 C16 100n VS+
10n GND 7
2
C R10 50 1 RG_2 R14
U6
INA828IDR 6
8 RG
SMA_IN2 R8 1M GND 5
3 100n C17 VS- GND
GND

VREF2-2
JP2 GND C14 100n VS+
2 7
RG_2 R11 1 U7
5.49k INA828IDR 6 50 C20 SMA_OUT2
8 RG R13
GND 100n C15 VS- 5 GND
3
GND
U8
OPA191IDBVT C19
1 OUT V+ 5 100n
2 V- IN- 4 VS+
VS- C18 IN+ 3
100n R12 1M or 0
GND GND

GND GND
C58 R15 10 C26 100n VS+
10n GND 7
2
C R17 50 1 RG_2 R21
U10
INA828IDR 6
8 RG
SMA_IN3 R16 1M GND 5
3 100n C27 VS- GND
GND

VREF3-2
JP4 GND C24 100n VS+
2 7
RG_2 R18 1 U11
5.49k INA828IDR 6 50 C30 SMA_OUT3
8 RG R20
GND 100n C25 VS- 5 GND
3
GND
U12
OPA191IDBVT C29
1 OUT V+ 5 100n
2 V- IN- 4 VS+
VS- C28 IN+ 3
100n R19 1M or 0
GND GND

ADR130BUJZ-REEL7
GND 1 NC_2 NC 6 GND
2 GND SET 5
VS+ 3 VIN U1 VOUT 4 R36 VREF1-2
C3 C2 C1 10k C51
1u 100n 100n 10u
GNDGND GND GND

U5
ADR130BUJZ-REEL7
GND 1 NC_2 NC 6 GND
2 GND SET 5
VS+ 3 VIN VOUT 4 R37 VREF2-2
C13 C12 C11 10k C52
1u 100n 100n 10u
GNDGND GND GND

U9
ADR130BUJZ-REEL7
GND 1 NC_2 NC 6 GND
2 GND SET 5
VS+ 3 VIN VOUT 4 R38 VREF3-2
C23 C22 C21 10k C53
1u 100n 100n 10u
GNDGND GND GND

Design: Timo Zwettler
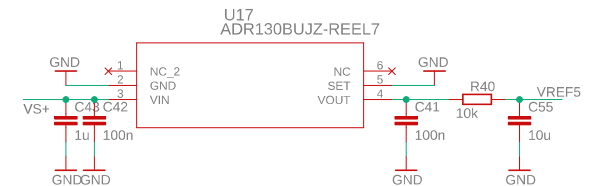TITLE: LQG_mod_RP_Out_Amp
Document Number: REV:
                                    v1.1
Date: 20/10/2020 11:48   Sheet: 1/2

20/10/2020 12:20 f=0.74 D:\Electronics_Projects\LQG_Mod_RP_Out_Amp\RP_Out_Amp_Eagle_Proj\LQG_mod_RP_Out_Amp.sch (Sheet: 1/2)
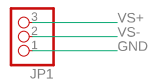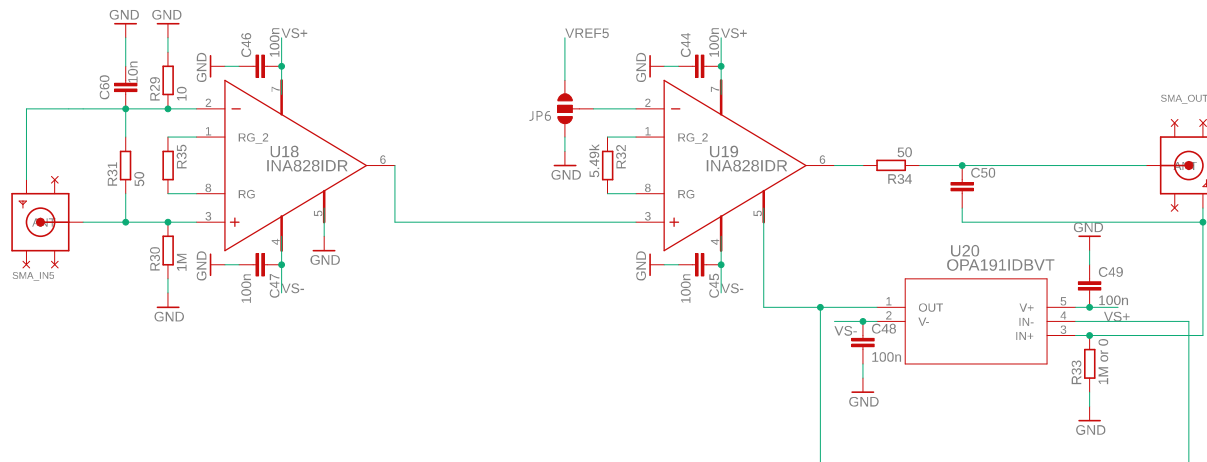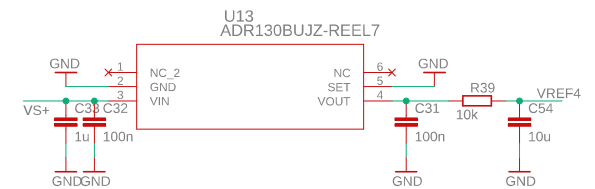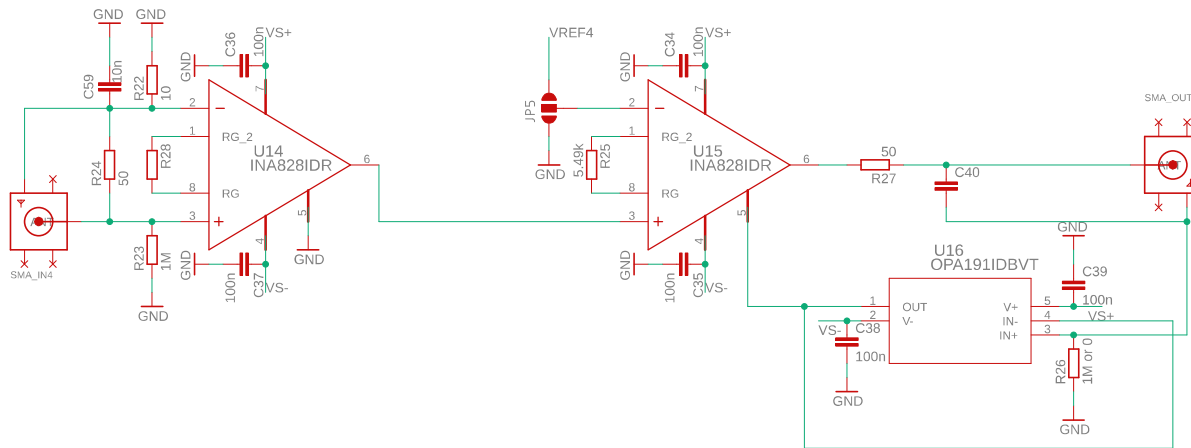
TITLE: LQG_mod_RP_Out_Amp

Document Number:

REV:

Date: 20/10/2020 11:48

Sheet: 2/2