

STM32H747I-DISCO

MB1248

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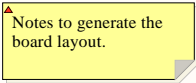
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Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.



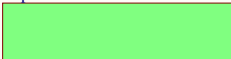
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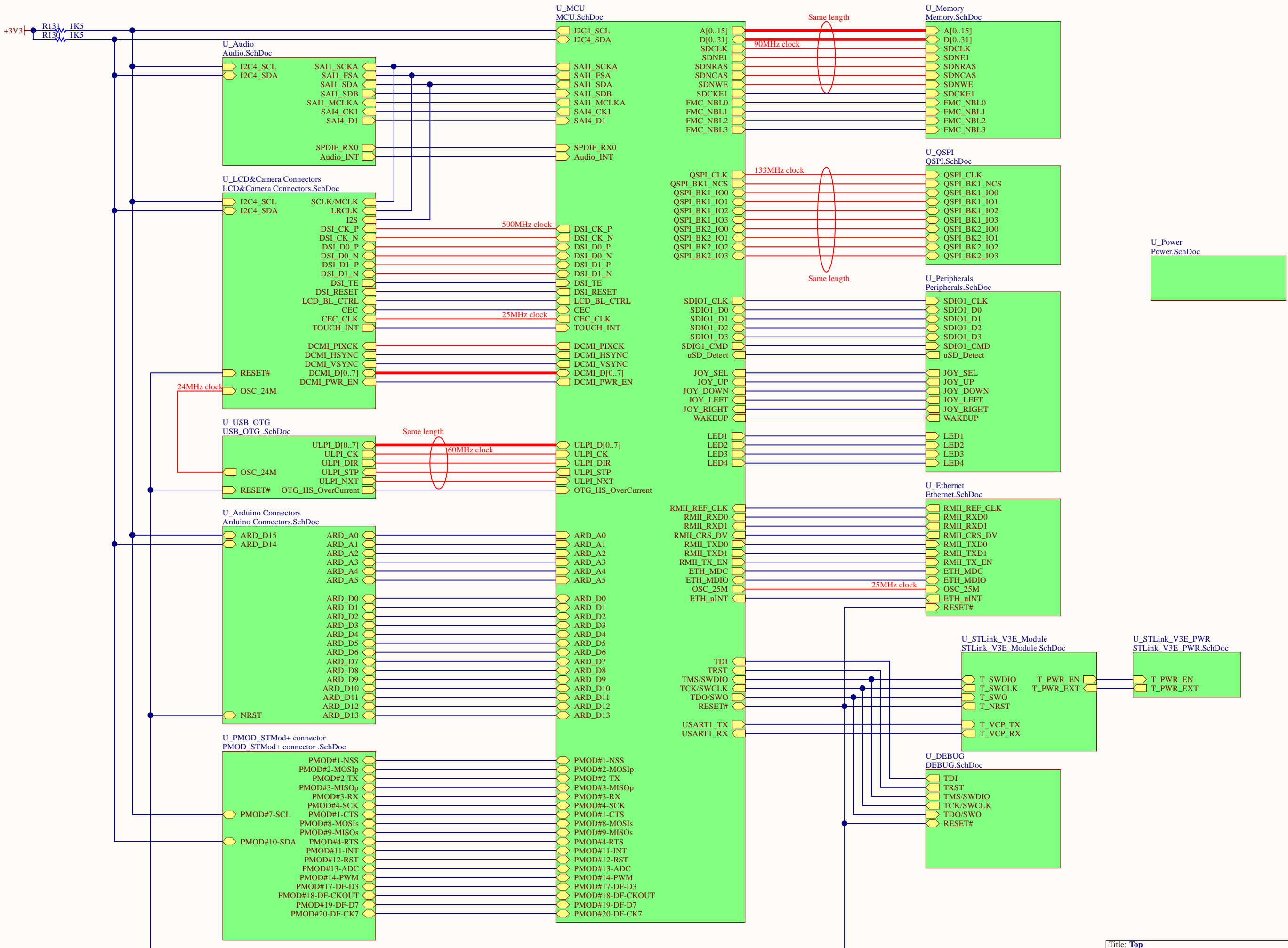
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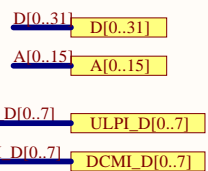
U_Top
Top.SchDoc



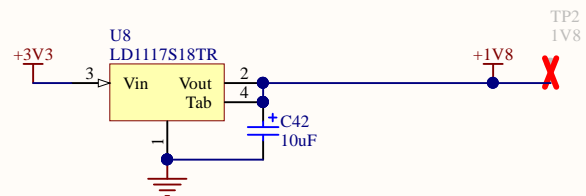
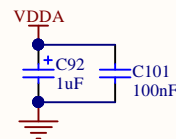
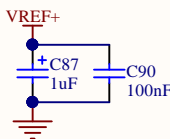
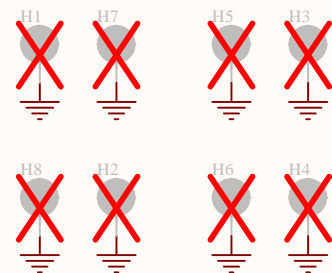
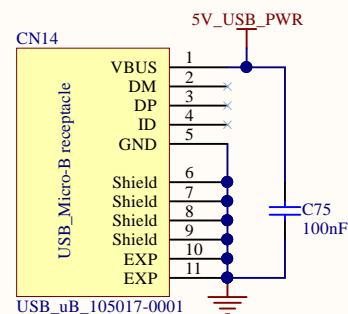
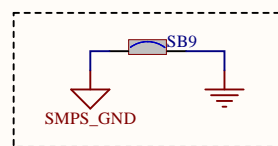
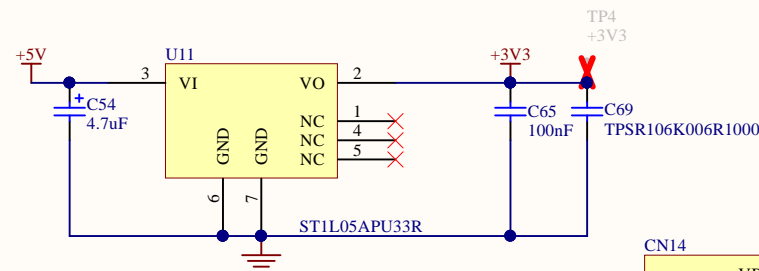
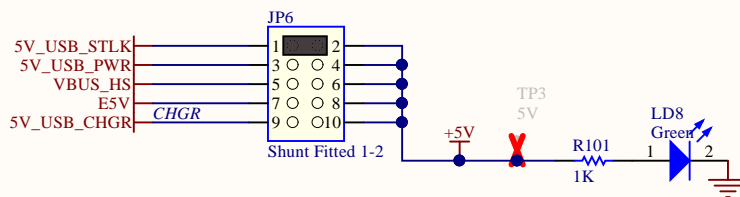
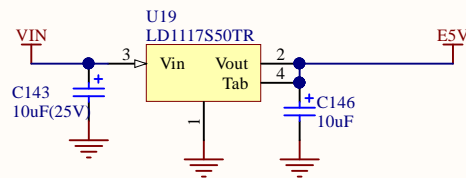
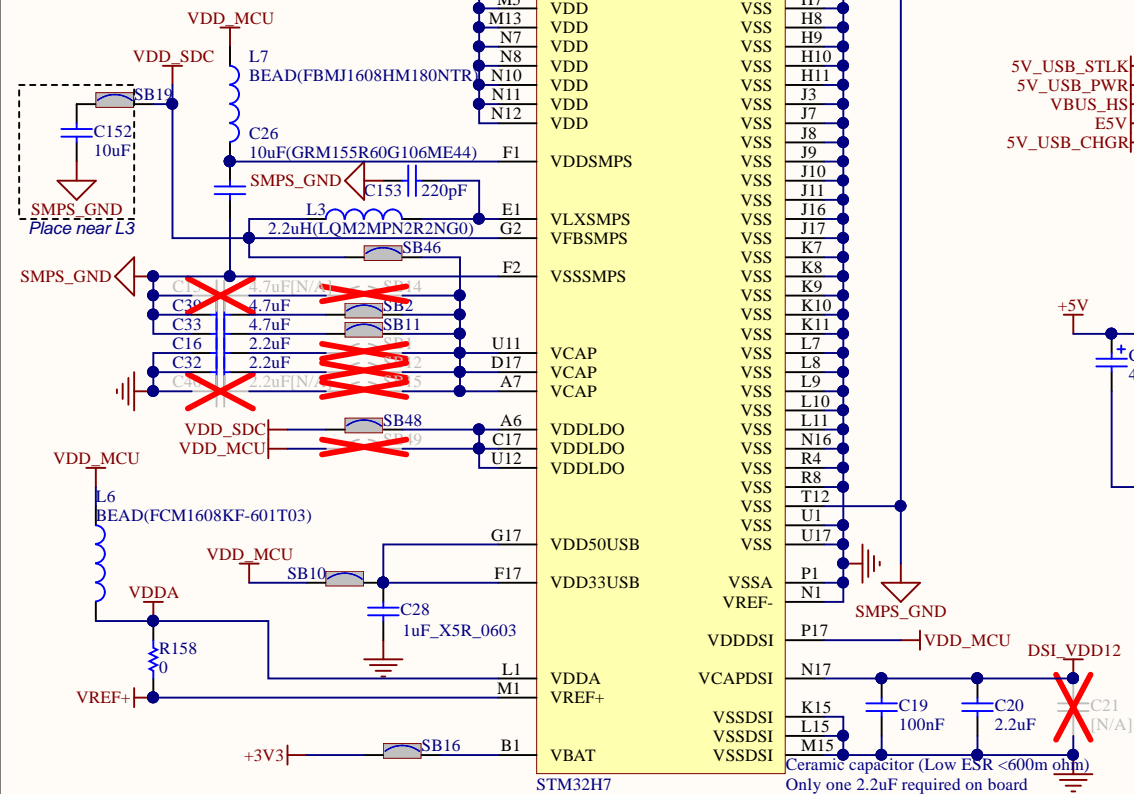
Title: Project Overview		
Project: STM32H747I-DISCO		
Variant: H747I		
Revision: D-03		Reference: MB1248
Size: A4	Date: 21/03/2022	Sheet: 1 of 16

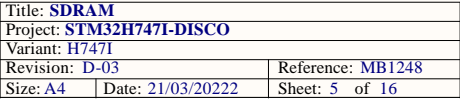




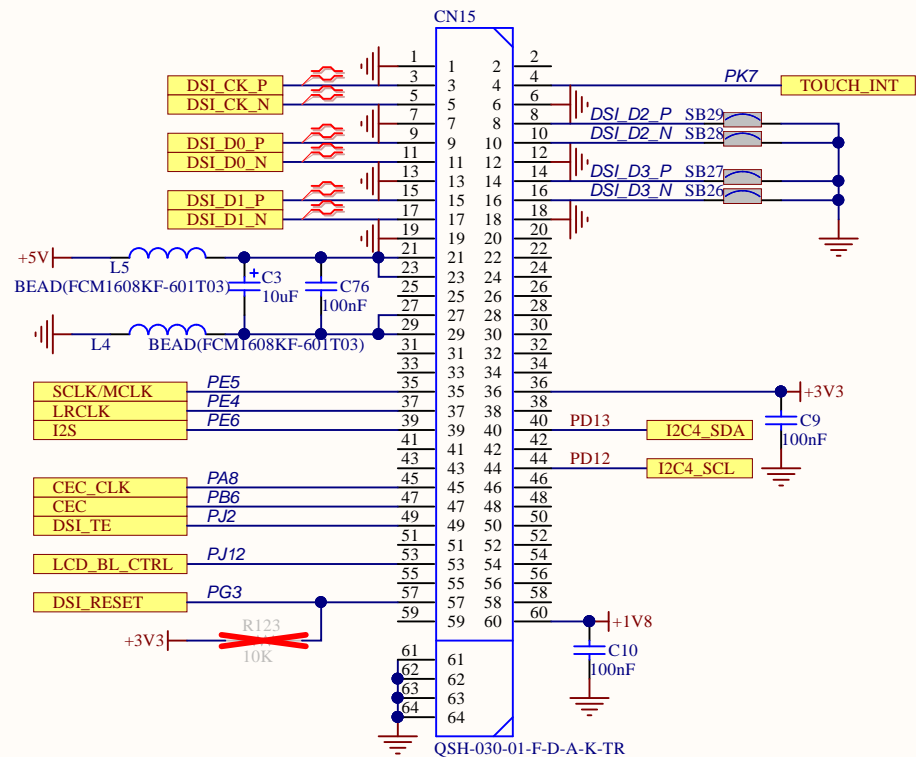


LDO mode: SB1, SB12, SB49 mounted, SB2, SB11, SB19, SB46, SB48 removed
 SMPS mode (default): SB2, SB11, SB19, SB46, SB48 mounted, SB1, SB12, SB49 removed
 SB14, SB15 are backup





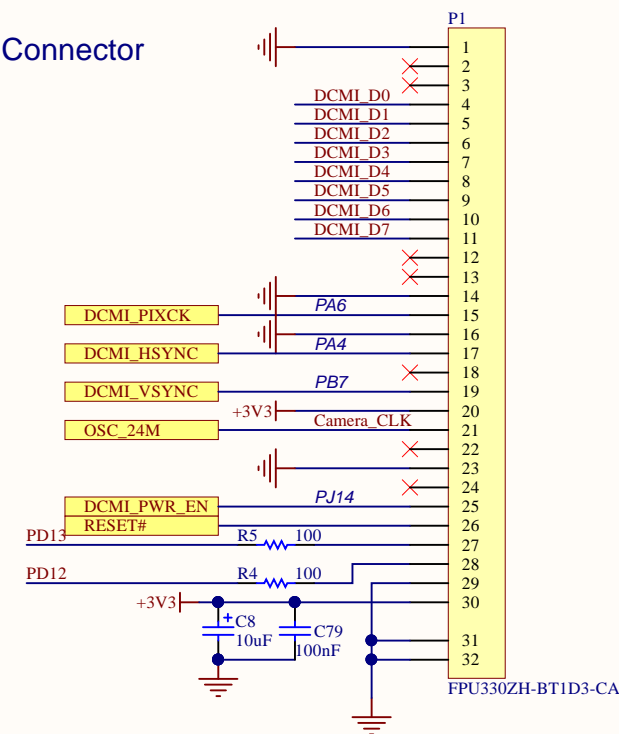
DSI LCD

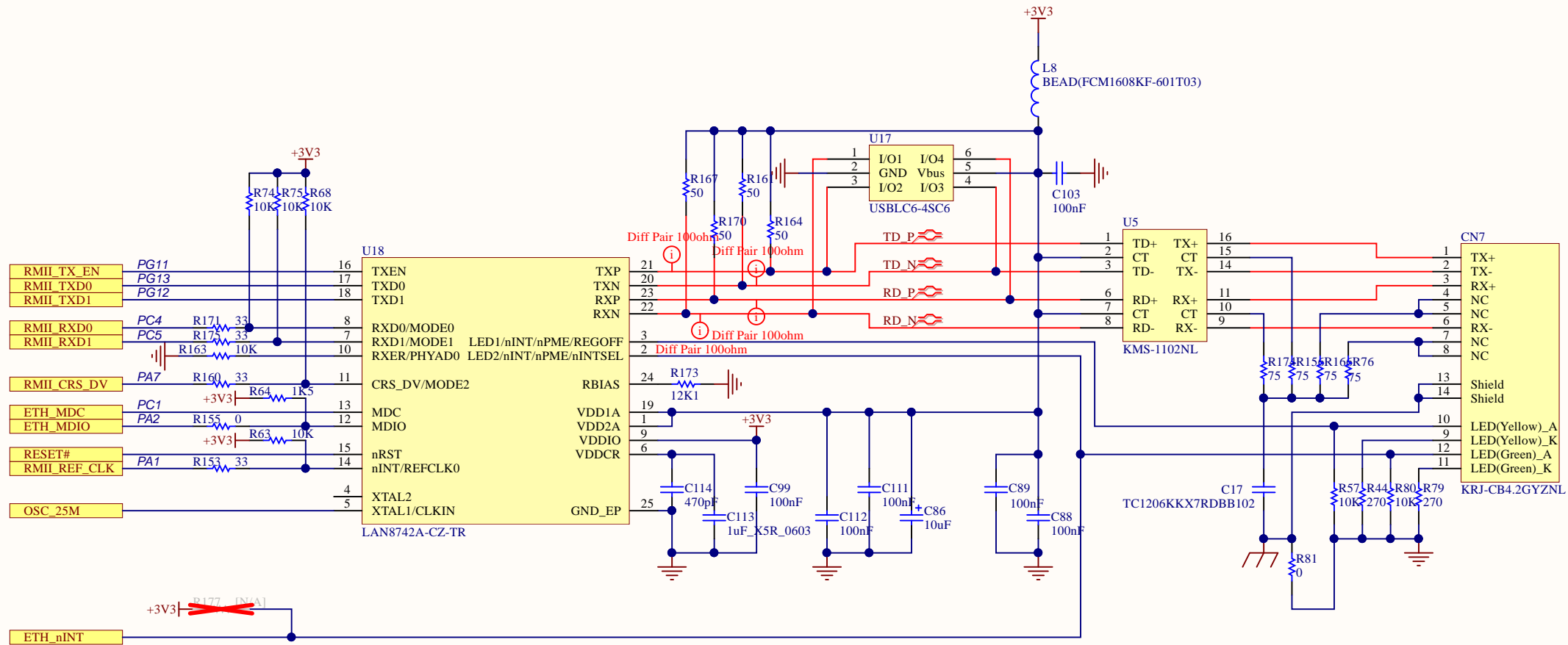


DCMI_D[0..7]

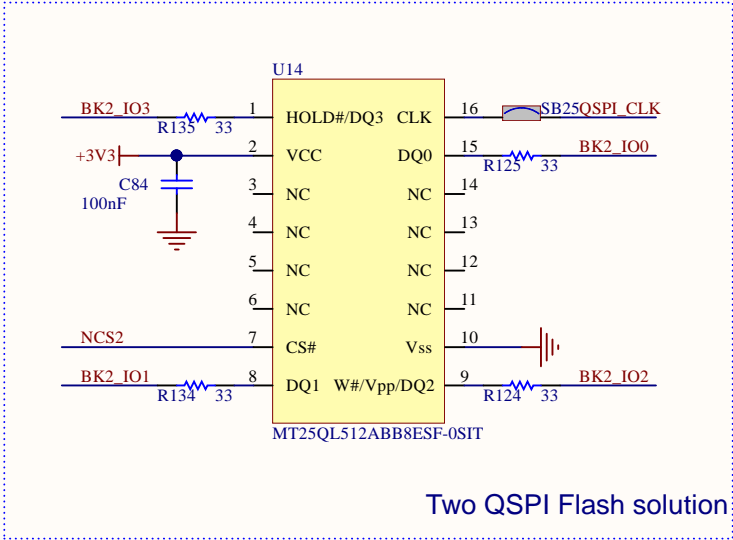
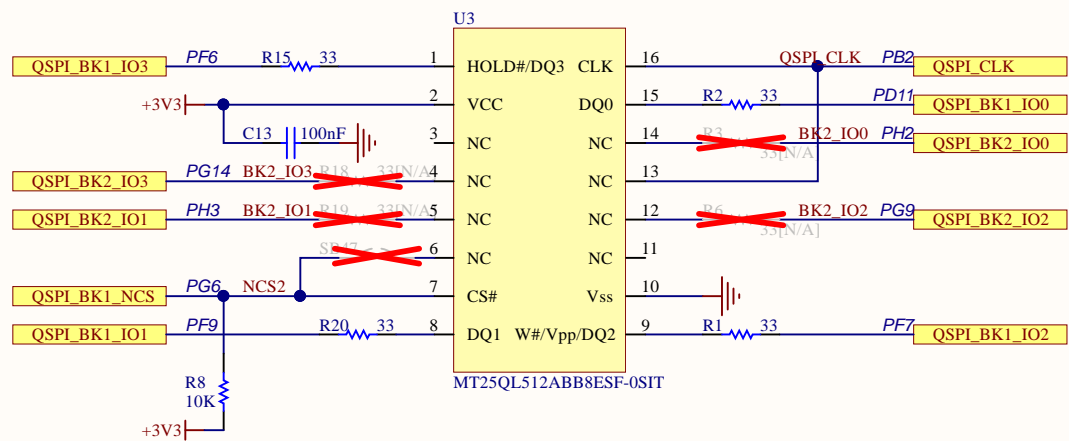
DCMI_D[0..7]

Camera Connector





Twin Quad SPI Flash

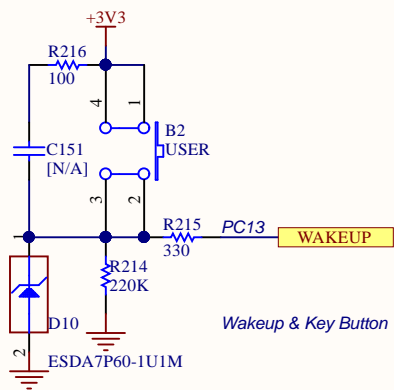


Two QSPI Flash solution

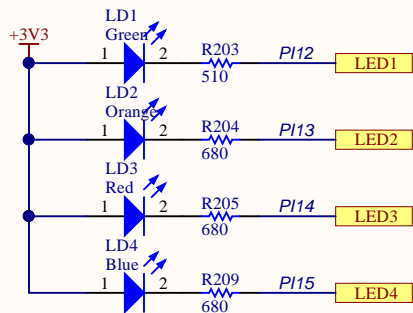
Twin Quad SPI Flash solutions:

One Twin Quad SPI Flash	U3:MT25TL01GHBB8ESF-0SIT	U14:NA	R3/R6/R18/R19, SB47 ON	R124/R125/R134/R135, SB25, C84 OFF
Two Quad SPI Flash (default)	U3:MT25QL512ABB8ESF-0SIT	U14:MT25QL512ABB8ESF-0SIT	R124/R125/R134/R135, SB25, C84 ON	R3/R6/R18/R19, SB47 OFF

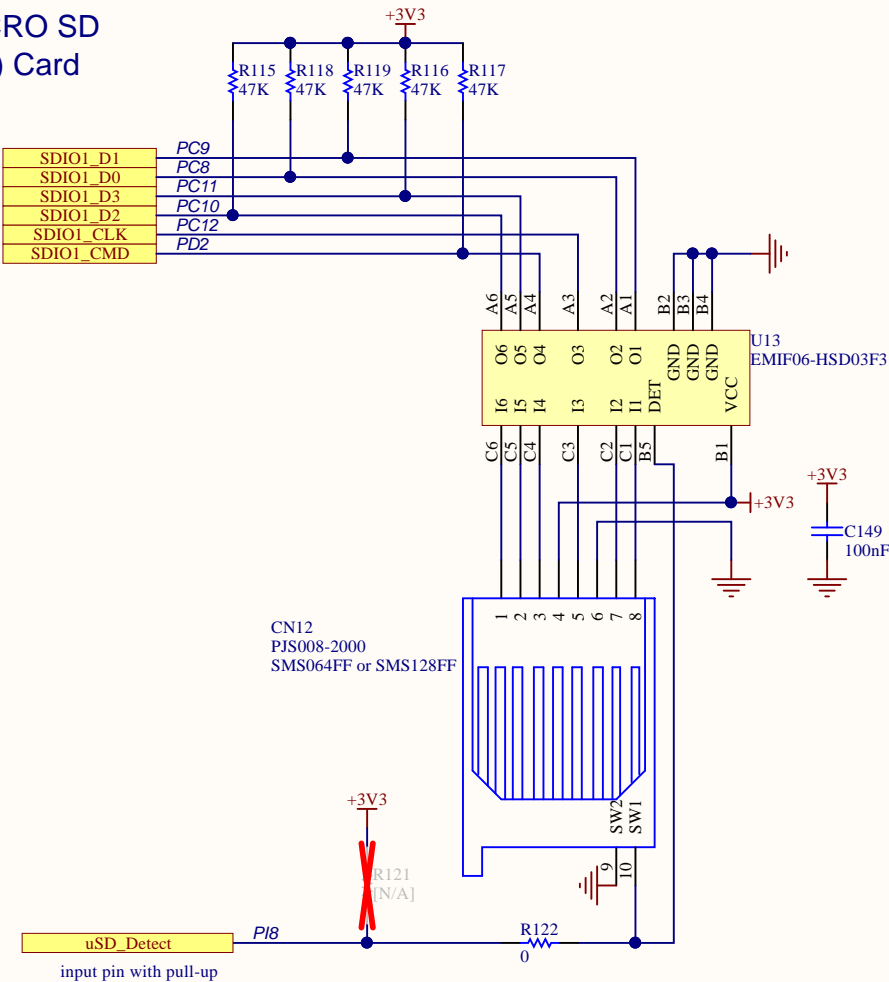
Buttons



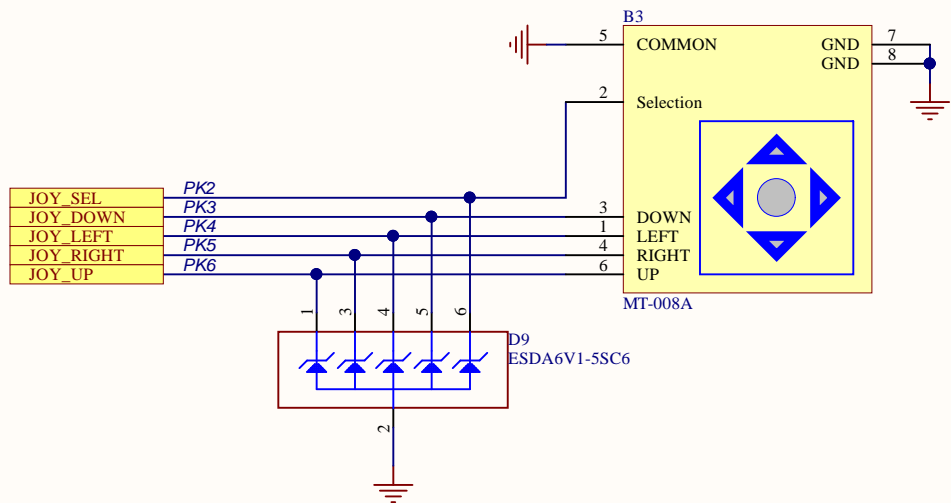
LEDs



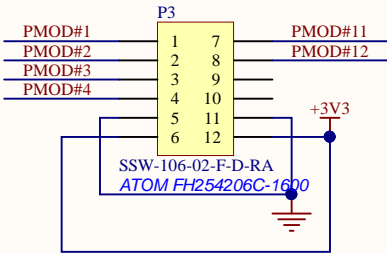
MICRO SD (TF) Card



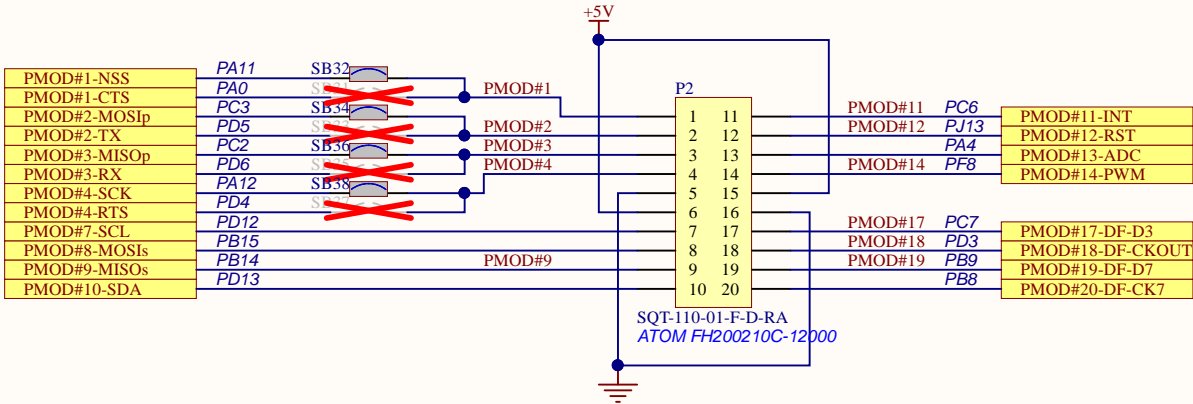
Joystick



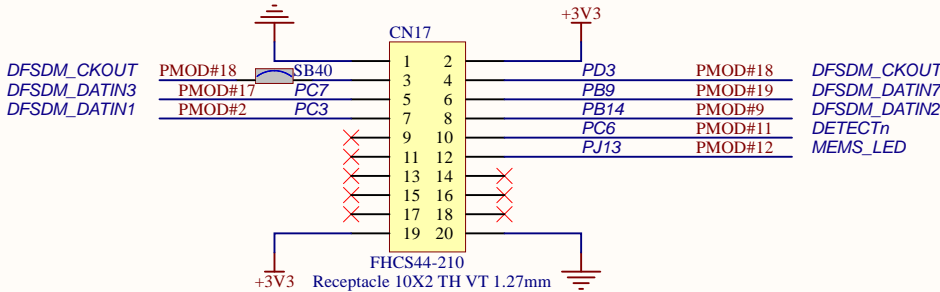
PMOD

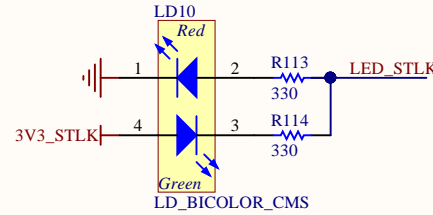
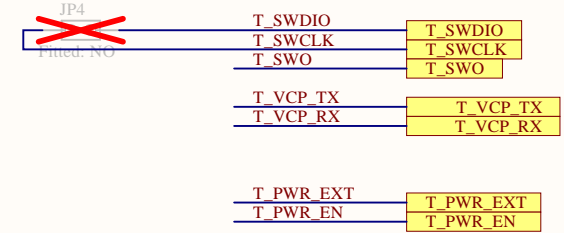
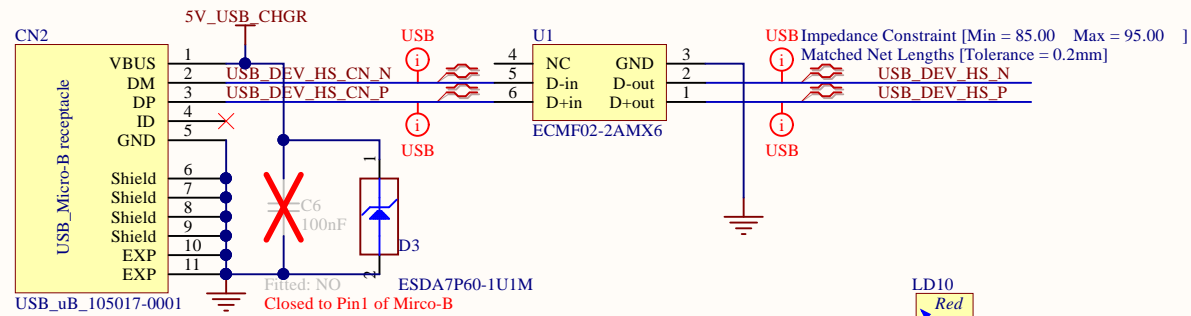


STMod+

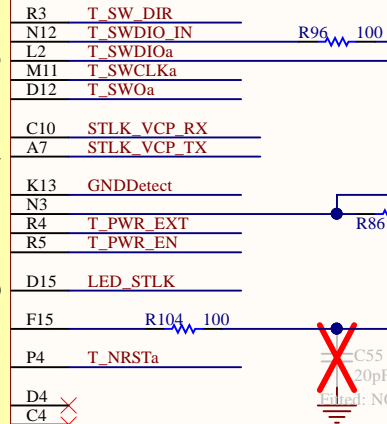
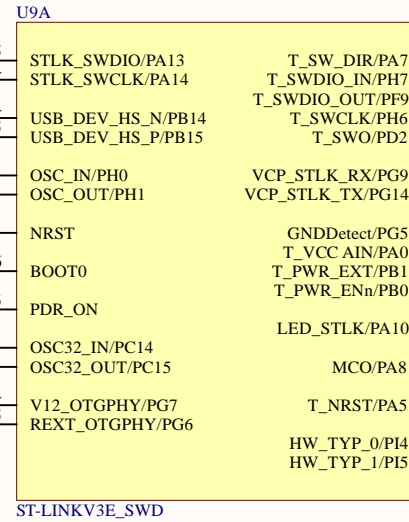
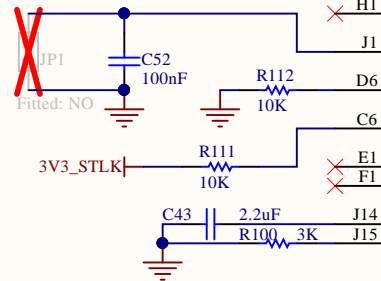
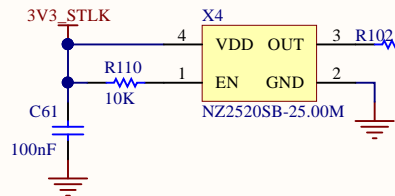
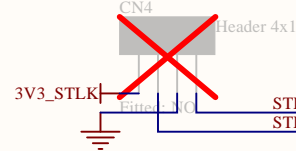


Audio Connector





Must be on a border or the PCB.
Silkscreen = Reserved



STDC14 Receiver
T_VCC must be connected to VDD_MCU of the Target

