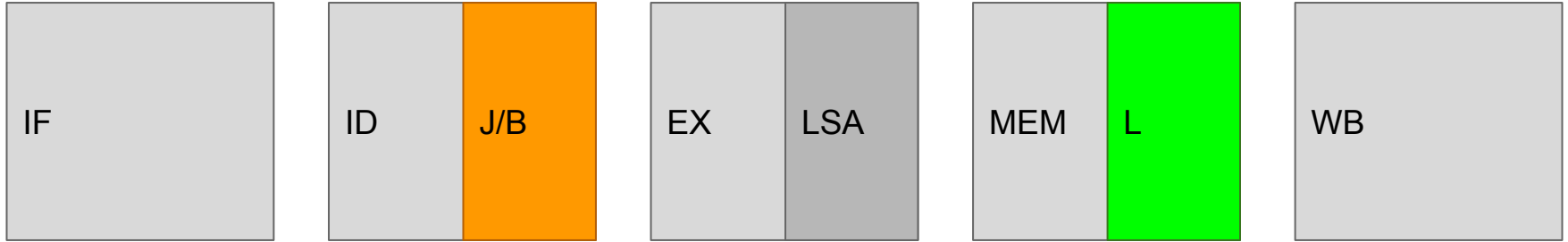


MC733

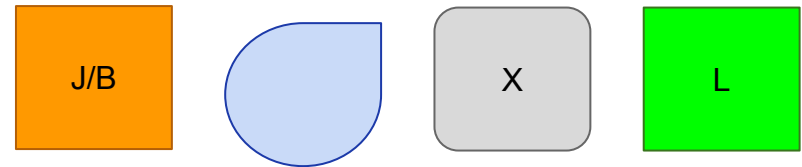
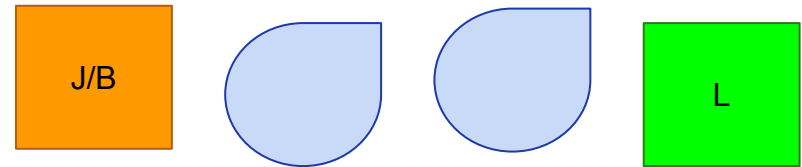
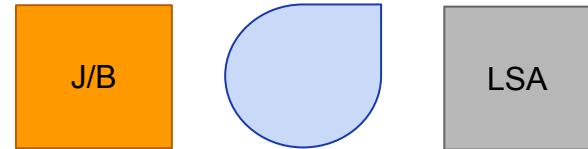
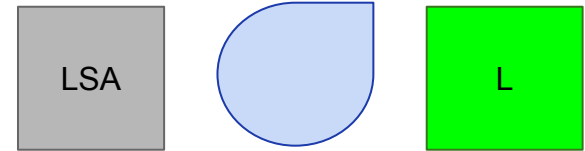
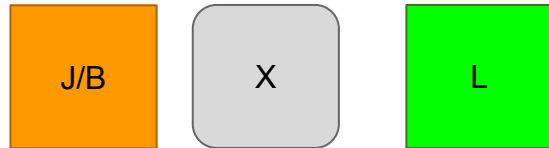
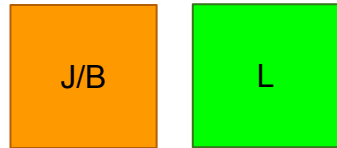
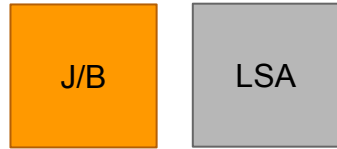
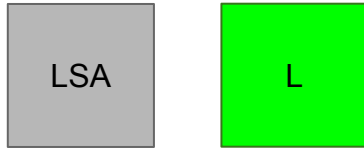


Simulando Várias Configurações do
Processador MIPS

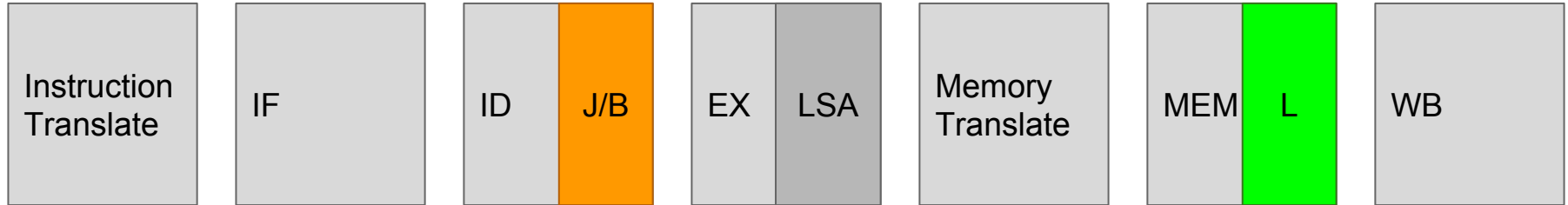
Pipeline de 5 Estágios



Hazards Detectados


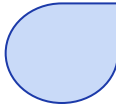
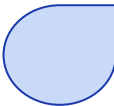




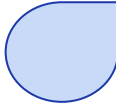







Pipeline de 7 Estágios

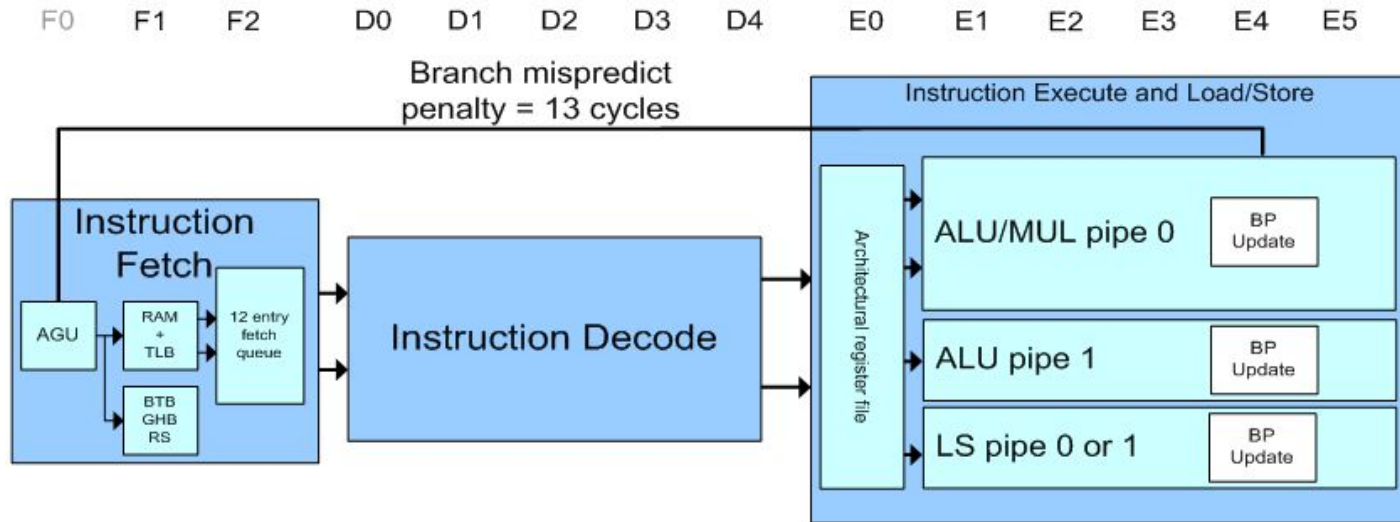


Hazards Detectados

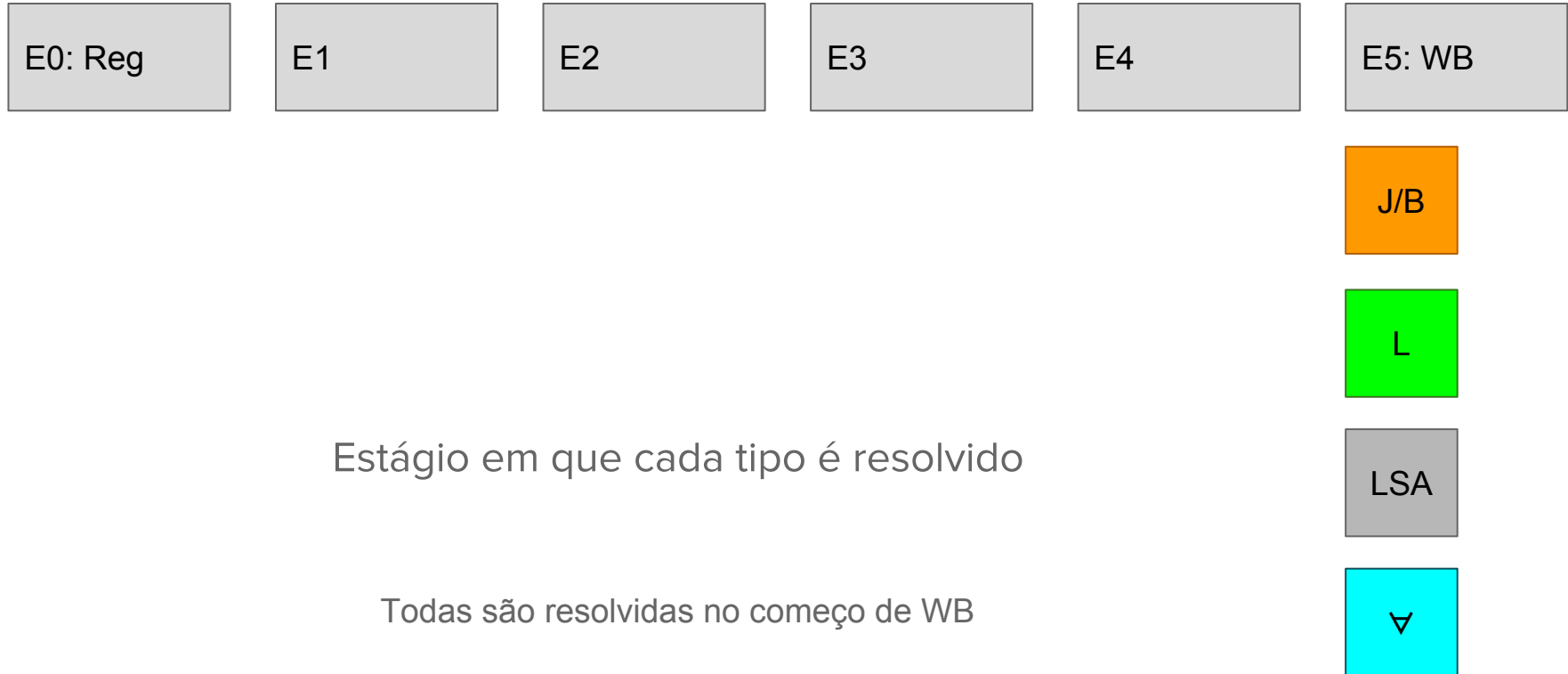
≠ 5

+1		LSA	L			LSA			L	
Novo		LSA	X	L		LSA		X	L	
		J/B	LSA			J/B		LSA		
+1		J/B	L			J/B				L
+1		J/B	X	L		J/B			X	L
Novo		J/B	X	X	L	J/B		X	X	L

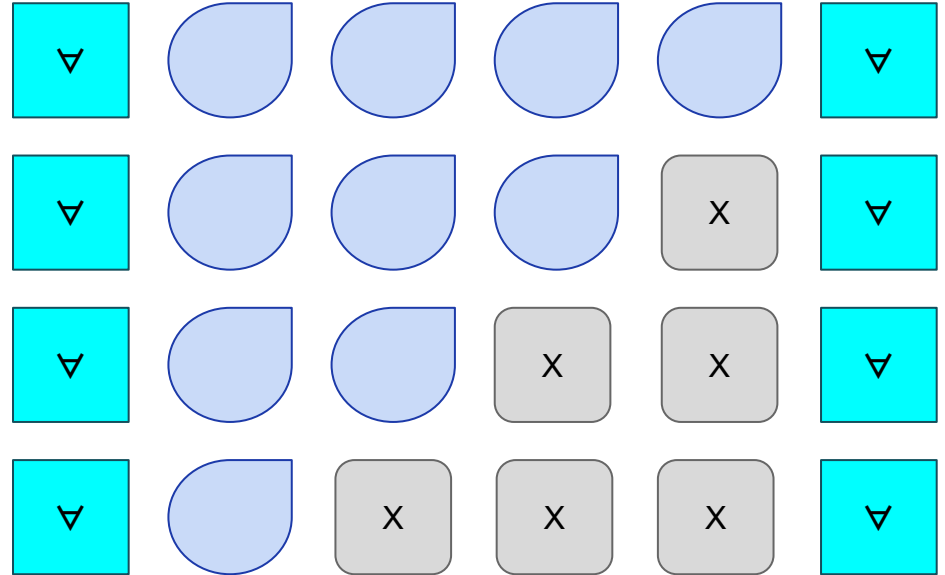
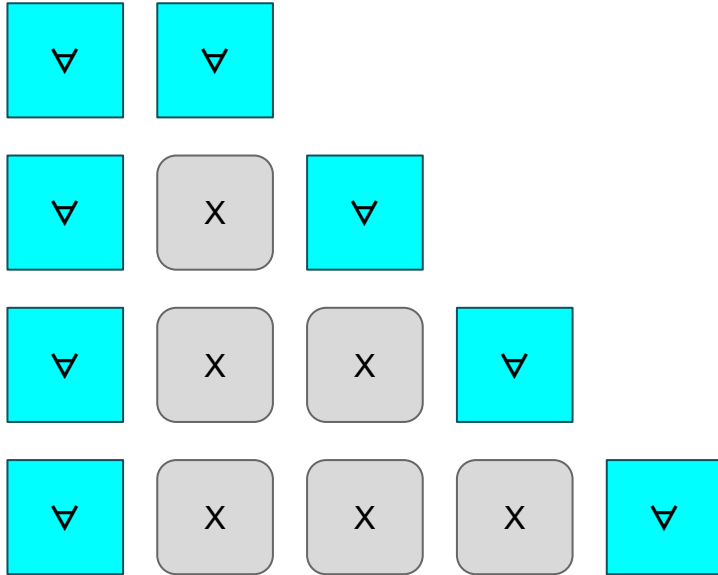
Pipeline de 13 Estágios



Pipeline de 13 Estágios



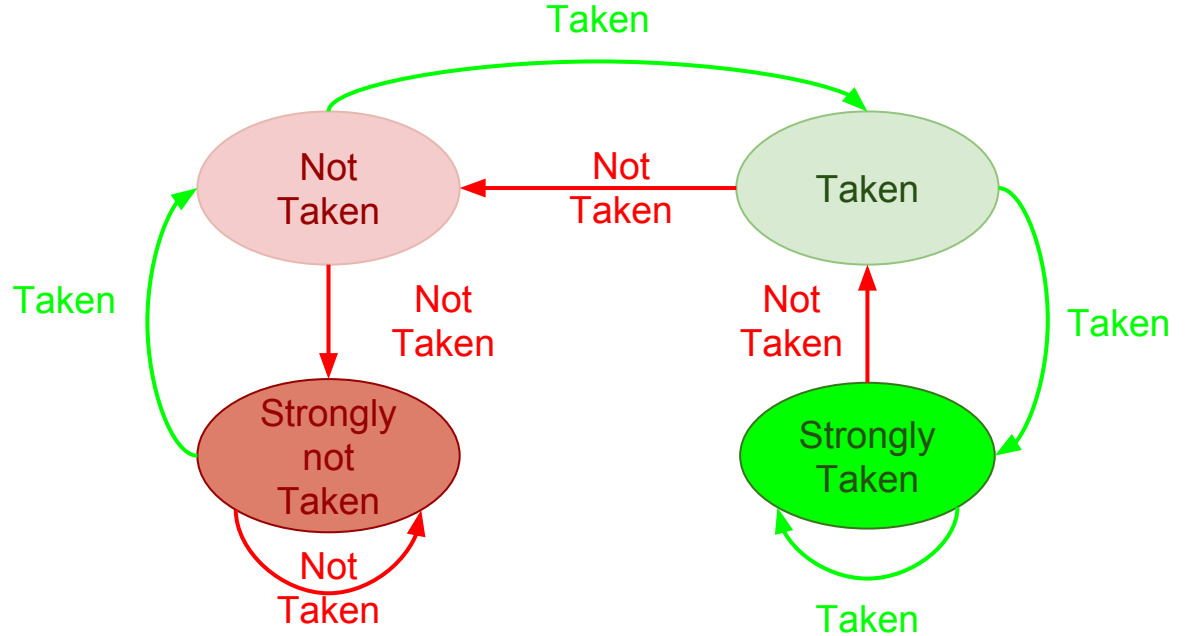
Hazards Detectados



Branch Prediction

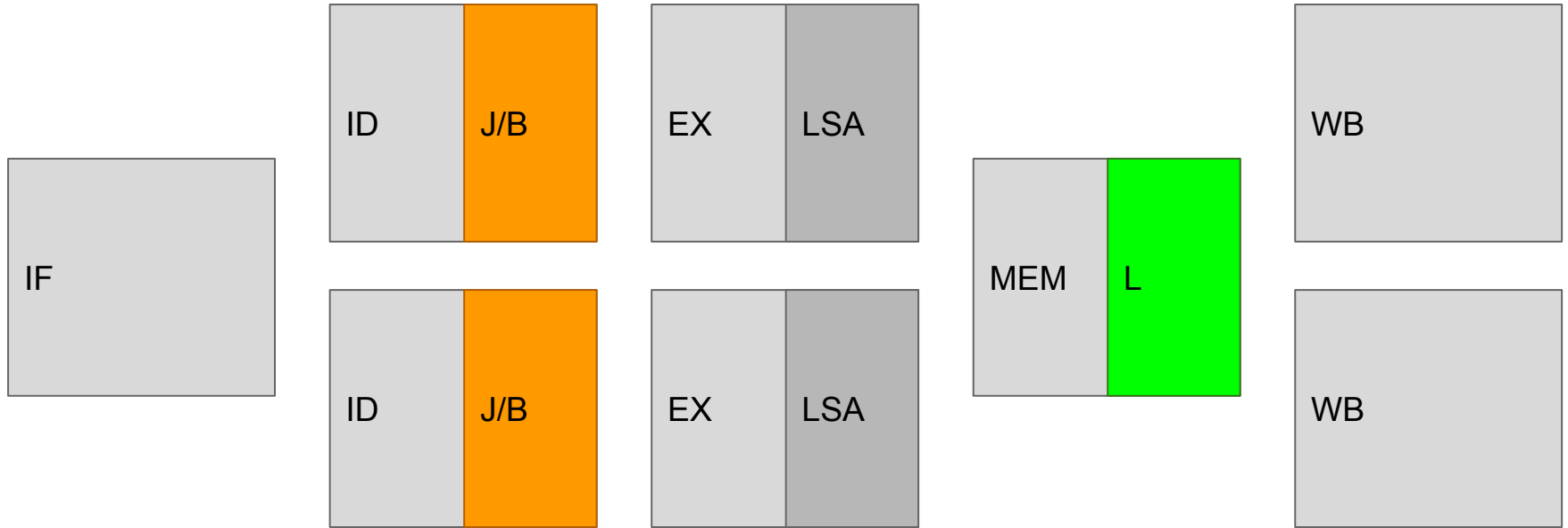
Always Taken

2 Bits

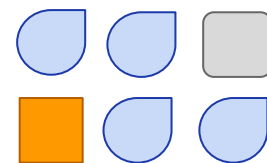
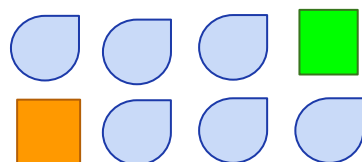
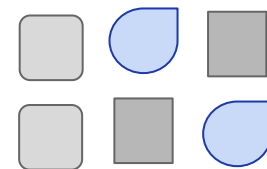
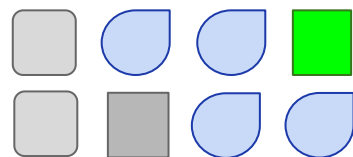
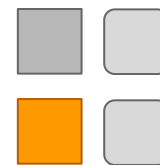
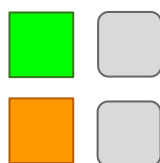
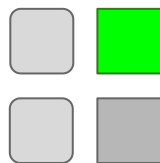
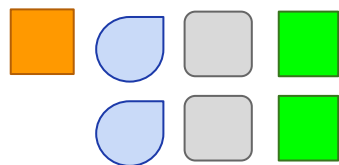
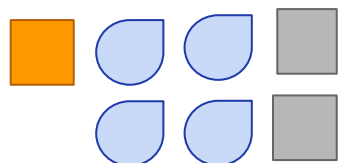
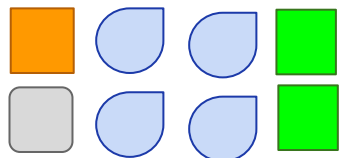
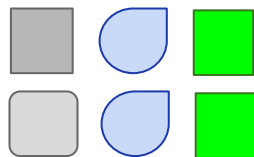
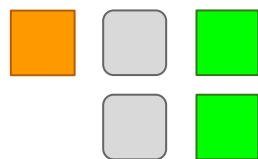
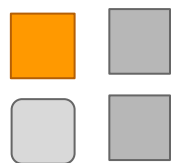
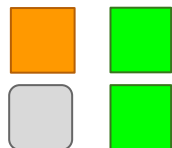
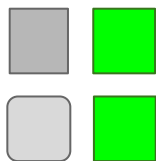


Always not Taken

Pipeline de 5 Estágios Superescalar



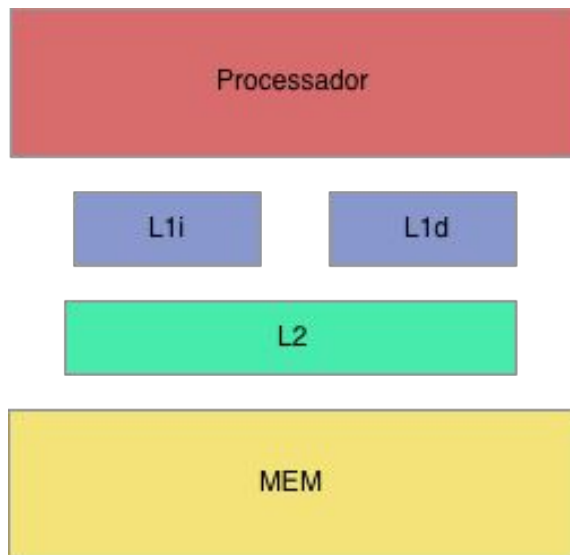
Hazards Detectados



Dinero IV

d4-7/d4.h

Funções doread e dowrite



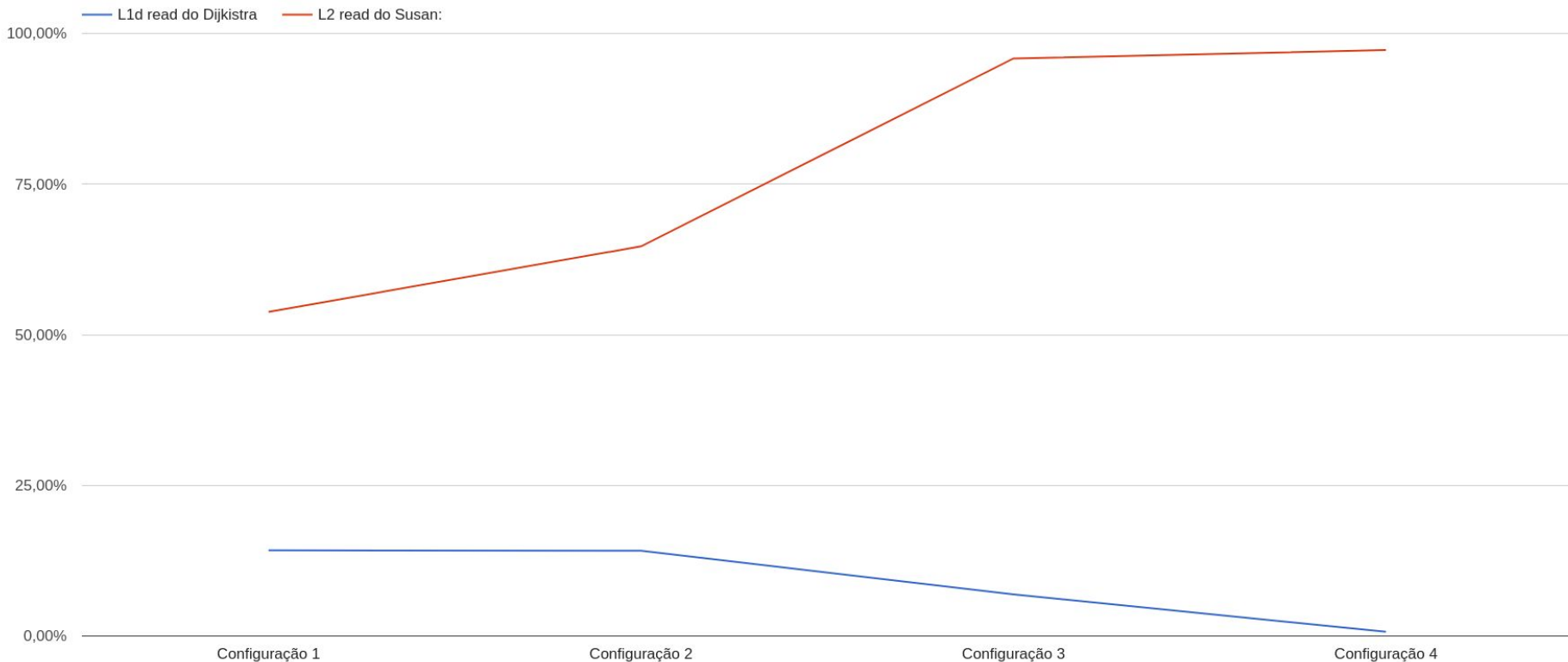
Configurações de Cache

Configuração 1 Configuração 2 Configuração 3 Configuração 4

L1i ou L1d size	14	14	15	15
L1 assoc	2	4	2	4
L1 block size	4	4	4	4
L2 size	18	18	19	19
L2 assoc	2	4	2	4
L2 block size	5	5	5	5

Resultados Cache

Config. Relevantes de Cache



Resultados Simulações

