LACHUGARI SRINIKETH REDDY

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OBJECTIVE

Seeking to apply my VLSI design skills to innovative semiconductor projects while continuously enhancing my technical abilities and contributing to organizational growth.

PROFILE SUMMARY

- Pursuing B.Tech in computer science and engineering at vidya Jyothi institute of technology.

TECHNICAL SKILLS

- Python, full-stack web development

PROJECTS

PROJECT-1:
Title: Design of Voting machine using Synopsys IC Compiler II 32nm Technology :

- The project involves the digital design and implementation of a simple voting machine system that allows users to vote among three candidates.
- The system was modelled using Hardware Description Language (HDL) and synthesized using Synopsys IC Compiler II (ICC2) tool.

PROJECT-2
Title: Design and Analysis of Full Adder Based on Domino Logic Technique using cadence 45nm technology (Duration: Sep 2022 – Dec 2022)

- Implemented a full adder circuit using Domino logic to reduce power and area.

PROJECT-3:
Title: Implementation of Low Power Flash ADC Using 2:1 MUX Pass Transistor Logic using cadence 45nm technology

- Designed a low-power, area-efficient flash ADC.

WORK EXPERIENCE

Electronics Corporation of India Limited (ECIL)
Apprentice – Automated Spectrum Monitoring System
Duration: 6 months (May 2019 - Nov 2019)

- Worked on a live Automated Spectrum Monitoring System project for RF signal observation.
- Gained hands-on exposure to spectrum analyzers, antennas, and RF measurement techniques.

EDUCATIONAL QUALIFICATIONS

Mahindra University, Hyderabad, India
2024 - Present
CGPA: 7.38/10.0
Master of Technology, VLSI and Embedded Systems

MLR Institute of Technology, Hyderabad, India
2020 - 2023
CGPA: 8.68/10.0
Bachelor of Technology, Electronics and Communication Engineering

Government Institute of Electronics, Secunderabad, India
2016 - 2019
Percentage: 85.96%
Diploma, Industrial Electronics Engineering

St. Anthony's High School, Siddipet, India 2015-
2016
GPA: 9.0/10.0
Secondary Education – Telangana State Board