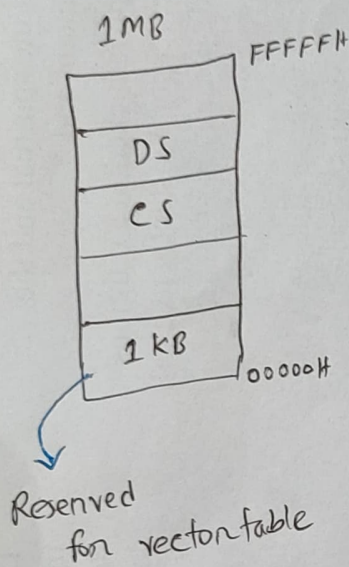


CSE 331 / 2-16 / 04.04.2024 /

Yu Manuth

chapter - 15

BIOS & DOS Interrupts



⊗ There are $2^8 = 256$ interrupt in 8086

The code or service routine can be store anywhere in the memory, but we need call by address to execute these. These address are saved in vector table. In the memory first 1KB, 00000H \rightarrow 000FFH are reserved for the vector table.

So, vector number range 00 \rightarrow FFH

⊗ There are two part in physical address

Segment address \Rightarrow 16 bit = 2 byte

Offset address \Rightarrow 16 bit = 2 byte

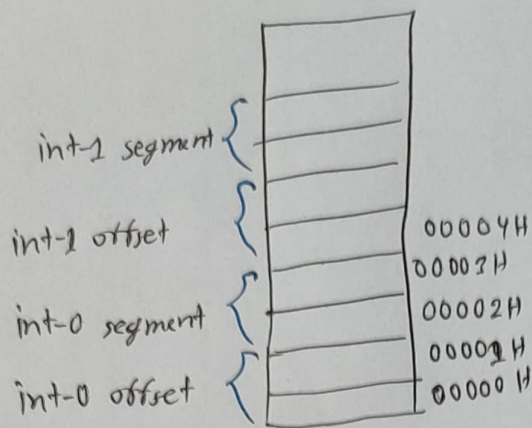
So, for saving one physical address

we need $2 + 2 = 4$ byte of memory

So, for saving 256 vector, we need

$$\begin{aligned} &= 256 \times 4 \\ &= 1024 \text{ byte} \\ &= 1 \text{ KB} \end{aligned}$$

That's why 1KB memory reserved for vector table



1st two byte for offset then 2nd two byte for segment

* We can easily calculate physical address of vector on interrupt by multiplying by 4.

* int 21h \Rightarrow Calculate the physical address

$\Rightarrow 21h \times 4h = 84h \Rightarrow$ So the int 21h is located in 00084H address

~~84~~

84, 85 \Rightarrow offset

86, 87 \Rightarrow segment

* There are three types of interrupts:

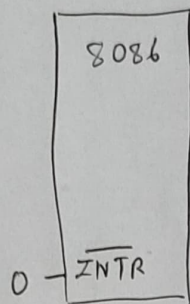
(i) Hardware \Rightarrow I/O Device interrupt

(ii) Software \Rightarrow interrupt occurs through software

(iii) Processor Exception \Rightarrow Out of processor capacity.

— divide overflow, divid by 0.

* interrupt request pin - active low

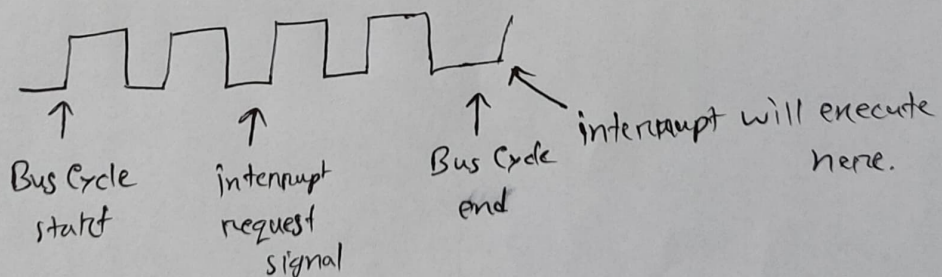


⇒ when hardware connected, it will give 0 signal and pin will activated and interrupts the CPU.

* 4 step of interrupt request line:

i) Hardware that need service, send an interrupt request signal

ii) processor suspends the current task it is executing and transfer the control to the interrupt routine.



iii) Interrupt routine will give the services

iv) control is back to the original executing task at the point where it was suspended.

⊗ For interrupt types and names:

- we don't need to memorize all interrupt name and index
- We just need to take the idea of each interrupt.

Midterm Syllabus

Up to This

25.04.2024