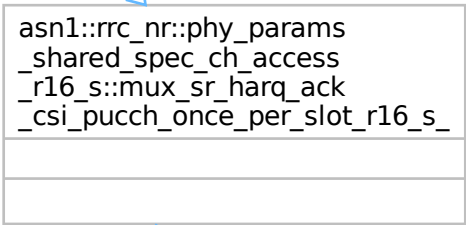


+diff\_symbol\_r16\_present  
+same\_symbol\_r16\_present

+cfg\_ul\_grant\_type1\_r16\_present  
+cfg\_ul\_grant\_type2\_r16\_present  
+dl\_sps\_r16\_present  
+dyn\_sfi\_r16\_present  
+ext  
+mux\_harq\_ack\_pusch\_diff\_symbol\_r16\_present  
+mux\_sr\_harq\_ack\_csi\_pucch\_multi\_per\_slot\_r16\_present  
+mux\_sr\_harq\_ack\_csi\_pucch\_once\_per\_slot\_r16\_present  
+mux\_sr\_harq\_ack\_pucch\_r16\_present  
+pdsch\_repeat\_multi\_slots\_r16\_present  
...



+mux\_sr\_harq\_ack\_csi\_pucch\_once\_per\_slot\_r16

