```
bool
```

```
int8_t
```

+p_max_nr_fr2_mcg_r16
_present
+p_max_nr_fr2_scg
_r16_present
+p_max_ue_fr2_r16
_present

+p_max_nr_fr2_mcg_r16
+p_max_nr_fr2_scg_r16
+p_max_ue_fr2_r16

```
asn1::rrc_nr::cfg_restrict
_info_scg_s::pwr_coordination
_fr2_r16_s_
```