

uint8_t

modulation_order_dl

ssb_rate_match_type

uint16_t

array< uint16_t, 5 >

+csi\_rs\_rate\_match  
+max\_mu\_mimo\_users\_dl  
+max\_num\_mimo\_layers\_pdsch  
+max\_num\_ssb\_per\_pdsch\_slot  
+max\_pdschs\_tbs\_per\_slot  
+pdcch\_rate\_match  
+pdsch\_allocation\_types  
+pdsch\_cbg  
+pdsch\_data\_in\_dmrs\_symbols  
+pdsch\_dmrs\_additional\_pos  
...

+supported\_max\_modulation\_order\_dl

+ssb\_rate\_match

+supported\_rate\_match\_pattern\_type\_and\_method

+max\_number\_csi\_rs\_rate\_match\_per\_slot  
+max\_number\_prb\_sym\_bitmap\_per\_slot

srsran::fapi::pdsch_param
+ pdsch_mapping_type
+ pdsch_vrb_to_prb_mapping
+ pdsch_dmrs_config_types
+ pdsch_dmrs_max_length
+ preemption_support
+ pdsch_non_slot_support
+ pdsch_trans_type_support
+ pdsch_mac_pdu_bit_alignment
+ universal_rate_match
+ require_pdsch_signaling_associations

