```
asn1::rrc_nr::sl_drx
_cfg_uc_semi_static_r17
_s::sl_drx_on_dur_timer_r17_c_

+   sl_drx_on_dur_timer
    _r17_c_()
+   sl_drx_on_dur_timer
    _r17_c_()
+   operator=()
+   ~sl_drx_on_dur_timer
    _r17_c_()
+   set()
+   type()
+   pack()
+   unpack()
+   to_json()
+   sub_milli_seconds()
+   milli_seconds()
+   sub_milli_seconds()
+   milli_seconds()
+   set_sub_milli_seconds()
+   set_milli_seconds()
```

```
asn1::rrc_nr::sl_drx
_cfg_uc_semi_static_r17
_s::sl_drx_cycle_start
    _offset_r17_c_

+   sl_drx_cycle_start
    _offset_r17_c_()
+   sl_drx_cycle_start
    _offset_r17_c_()
+   operator=()
+   ~sl_drx_cycle_start
    _offset_r17_c_()
+   set()
+   type()
+   pack()
+   unpack()
+   to_json()
+   ms10()
    and 59 more...
```

```
uint8_t
```

+sl_drx_on_dur_timer_r17   +sl_drx_cycle_start   +sl_drx_slot_offset_r17
                                _offset_r17

```
asn1::rrc_nr::sl_drx
_cfg_uc_semi_static_r17_s

+       pack()
+       unpack()
+       to_json()
```