```
bool
                              uint8 t
   +fr1_and_fr2_r17_present
+fr1_only_r17_present
                                 +fr1 and fr2 r17
                                  +fr\overline{1} only r\overline{1}7
    +fr2 only r17 present
                                  +fr2 only r17
 asn1::rrc nr::meas
 _and_mob_params_mrdc
 _common_v1730_s::independent
     gap cfg max cc r17 s
                  +independent gap cfg
                       max cc r17
     asn1::rrc nr::meas
     and mob params mrdc
        common v1730 s
        + pack()
               unpack()
               to ison()
        +
```