

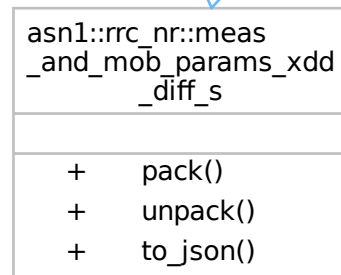
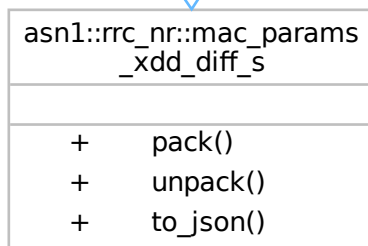
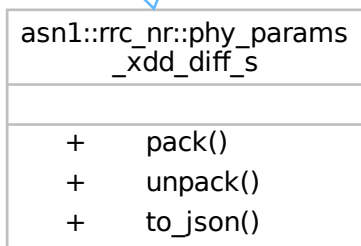


+dl_sched_offset_pdsch
_type_a_present
+dl_sched_offset_pdsch
_type_b_present
+dyn_sfi_present
+ext
+two_different_tpc
_loop_pucch_present
+two_different_tpc
_loop_pusch_present
+two_pucch_f0_2_consec
symbols_present
+ul_sched_offset_present

+enhanced_skip_ul_tx
_cfg_r16_present
+enhanced_skip_ul_tx
_dyn_r16_present
+ext
+lc_ch_sr_delay_timer
_present
+long_drx_cycle_present
+multiple_cfg_grants
_present
+multiple_sr_cfgs
_present
+secondary_drx_group
_r16_present
+short_drx_cycle_present
+skip_ul_tx_dyn_present
...

+dummy_present
+event_a_meas_and
_report_present
+ext
+ho_inter_f_present
+ho_lte_5_gc_present
+ho_lte_epc_present
+intra_and_inter_f
_meas_and_report_present
+sftd_meas_nr_neigh_drx
_present
+sftd_meas_nr_neigh
_present

+mac_params_xdd_diff
_present
+meas_and_mob_params
_xdd_diff_present
+phy_params_xdd_diff
_present



+phy_params_xdd_diff

+mac_params_xdd_diff

+meas_and_mob_params_xdd_diff

