

srsran::bounded\_bitset< N, LowestInfoBitIsMSB >

+   bounded\_bitset()

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+   bounded\_bitset()

+   bounded\_bitset()

+   bounded\_bitset()

+   operator=()

+   size()

+   empty()

+   resize()

+   set()

and 35 more...

+   bit\_order()

+   max\_size()

< pdcch\_constants::  
MAX\_NOF\_FREQ\_RESOURCES,  
true >

srsran::bounded\_bitset< pdcch\_constants::MAX\_NOF\_FREQ\_RESOURCES, true >

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and 35 more...

+   bit\_order()

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unsigned

cce\_to\_reg\_mapping\_type

+bwp\_size\_rb  
+bwp\_start\_rb  
+duration  
+interleaver\_size  
+reg\_bundle\_size  
+shift\_index  
+start\_symbol\_index

+frequency\_resources

+cce\_to\_reg\_mapping

srsran::pdcch\_processor::coreset\_description