```
asn1::rrc nr::sl csi
 rs cfg r16 s::sl csi
 rs freq alloc r16 c
+ sl csi rs freq alloc
  rī6 c ()
+ sl csi rs freq alloc
  r16 c ()
+ operator=()
+ ~sl csi rs freq alloc
  r16 c ()
+ set()
+ type()
+ pack()
+ unpack()
+ to ison()
+ sl one ant port r16()
+ sl two ant port r16()
+ sl one ant port r16()
+ sl two ant port r16()
+ set sl one ant port
  r16()
+ set sl two ant port
  r16()
```