bool

```
+enhanced skip ul tx
    cfg_r16 present
+enhanced skip ul tx
   dyn r16 present
         +ext
+lc ch sr delay timer
        present
+long drx cycle present
+multiple cfg grants
        present
+multiple sr cfgs
        present
+secondary drx group
      r16_present
+short_drx_cycle_present
+skip ul tx dyn present
```

```
asn1::rrc_nr::mac_params
_xdd_diff_s

+ pack()
+ unpack()
+ to json()
```