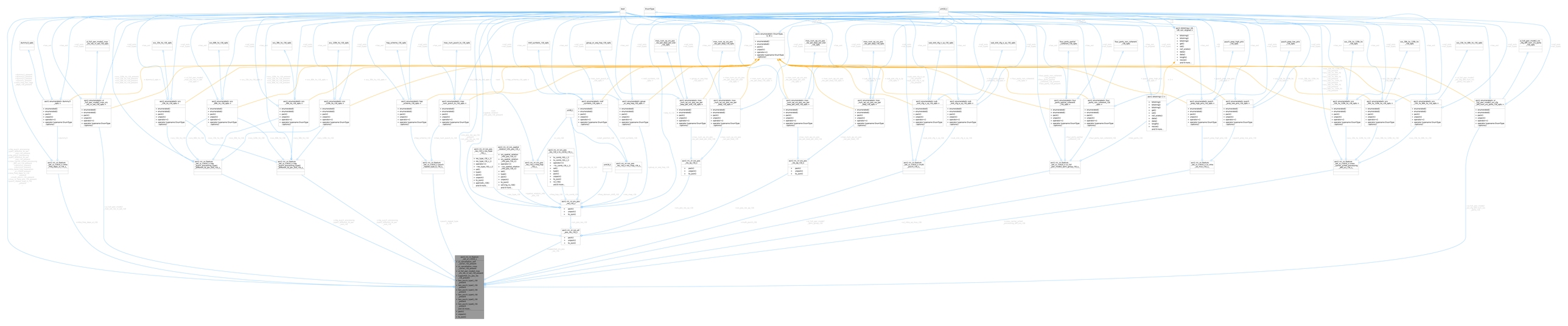

asn1::rrc_nr::feature
_set_ul_v1610_s
+ ul_cancellation_self
_carrier_r16_present
+ ul_cancellation_cross
_carrier_r16_present
+ ul_full_pwr_mode2_max
_srs_res_in_set_r16_present
+ supported_srs_pos_res
_ r16_present
+ two_pucch_type1_r16
_ present
+ two_pucch_type2_r16
_ present
+ two_pucch_type3_r16
_ present
+ two_pucch_type4_r16
_ present
+ two_pucch_type5_r16
_ present
+ two_pucch_type6_r16
_ present
and 10 more...

+ pack()
+ unpack()
+ to_json()

bool

+cbg_pusch_processing
_type1_different_tb_per
_slot_r16_present
+cbg_pusch_processing
_type2_different_tb_per
_slot_r16_present
+cross_carrier_sched_processing
_diff_scs_r16_present
+dummy1_present
+dummy2_present
+intra_band_freq_separation
_ ul_v1620_present
+intra_freq_daps_ul_r16
_ present
+multi_pucch_r16_present
+mux_sr_harq_ack_r16_present
+pusch_repeat_type_b_r16
_ present
...

asn1::enumerated< max
_ num_pusch_tx_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< EnumType,
E, M >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< hop
_scheme_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< ul
_full_pwr_mode2_max_srs
_ res_in_set_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::rrc_nr::feature
_set_ul_v1610_s::cbg
_ pusch_processing_type1
_different_tb_per_slot_r16_s_

+scs_120k_hz_r16_present
+scs_15k_hz_r16_present
+scs_30k_hz_r16_present
+scs_60k_hz_r16_present

asn1::enumerated< scs
_ 15k_hz_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< scs
_30k_hz_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< scs
_60k_hz_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< scs
_ 120k_hz_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::rrc_nr::feature
_set_ul_v1610_s::cbg
_ pusch_processing_type2
_different_tb_per_slot_r16_s_

+scs_120k_hz_r16_present
+scs_15k_hz_r16_present
+scs_30k_hz_r16_present
+scs_60k_hz_r16_present

asn1::rrc_nr::srs_all
_ pos_res_r16_s

+ pack()
+ unpack()
+ to_json()

+srs_pos_res_ap_r16
_ present
+srs_pos_res_sp_r16
_ present

asn1::rrc_nr::srs_pos
_ res_r16_s

+ pack()
+ unpack()
+ to_json()

+ext

+spatial_relation
_ info_pos_r16_present

asn1::enumerated< nrof
_symbols_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::rrc_nr::srs_pos
_ res_r16_s::freq_hop
_ r16_s_

+ext

asn1::enumerated< group
_or_seq_hop_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< max
_ num_ap_srs_pos_res_per
_ bwp_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< max
_ num_ap_srs_pos_res_per
_ bwp_per_slot_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< max
_ num_sp_srs_pos_res_per
_ bwp_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< max
_ num_sp_srs_pos_res_per
_ bwp_per_slot_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::rrc_nr::feature
_set_ul_v1610_s::intra
_freq_daps_ul_r16_s_

+dummy1_present
+dummy2_present
+dummy3_present
+dummy_present
+intra_freq_two_tags
_daps_r16_present

asn1::enumerated< dummy3
_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::rrc_nr::feature
_set_ul_v1610_s::multi
_ pucch_r16_s_

+sub_slot_cfg_e_cp
_ r16_present
+sub_slot_cfg_n_cp
_ r16_present

asn1::enumerated< sub
_slot_cfg_n_cp_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< sub
_slot_cfg_e_cp_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< pusch
_ prep_low_prio_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< pusch
_ prep_high_prio_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::rrc_nr::feature
_set_ul_v1610_s::cross
_carrier_sched_processing
_diff_scs_r16_s_

+scs_15k_hz_120k_hz
_ r16_present
+scs_15k_hz_30k_hz
_ r16_present
+scs_15k_hz_60k_hz
_ r16_present
+scs_30k_hz_120k_hz
_ r16_present
+scs_30k_hz_60k_hz
_ r16_present
+scs_60k_hz_120k_hz
_ r16_present

asn1::enumerated< scs
_ 15k_hz_120k_hz_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< scs
_ 15k_hz_60k_hz_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< scs
_30k_hz_120k_hz_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< ul
_full_pwr_mode2_srs_cfg
_diff_num_srs_ports_r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::rrc_nr::feature
_set_ul_v1610_s::ul_full
_ pwr_mode2_tpmi_group_r16_s_

+four_ports_non_coherent
_ r16_present
+four_ports_partial
_coherent_r16_present
+two_ports_r16_present

asn1::bitstring< 2 >

+ bitstring()
+ bitstring()
+ bitstring()
+ get()
+ set()
+ nof_octets()
+ data()
+ data()
+ length()
+ resize()
and 9 more...

+has_ext
+is_aligned

asn1::bitstring< LB,
UB, ext, aligned >

+ bitstring()
+ bitstring()
+ bitstring()
+ get()
+ set()
+ nof_octets()
+ data()
+ data()
+ length()
+ resize()
and 9 more...

+has_ext
+is_aligned

asn1::enumerated< four
_ ports_non_coherent_r16
_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::enumerated< four
_ ports_partial_coherent
_ r16_opts >

+ enumerated()
+ enumerated()
+ pack()
+ unpack()
+ operator=()
+ operator typename EnumType
::options()

+has_ext

asn1::rrc_nr::feature
_set_ul_v1610_s::pusch
_ repeat_type_b_r16_s_

+pusch_repeat_type
_ b_r16

+max_num_pusch_tx_r16

max_num_pusch_tx_r16_opts

uint32_t

+nof_exts
+nof_types

+nof_exts
+nof_types

+nof_exts
+nof_types

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+nof_types

+nof_exts
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+nof_exts
+nof_types

+nof_exts
+nof_types

+seq_id_r16

+nof_exts
+nof_types

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+lb
+ub

+lb
+ub

+nof_exts
+nof_types

+nof_exts
+nof_types

< max_num_pusch_tx
_ r16_opts > < hop_scheme_r16_opts >

< ul_full_pwr_mode2
_ max_srs_res_in_set
_ r16_opts >

< scs_15k_hz_r16_opts > < scs_60k_hz_r16_opts > < scs_30k_hz_r16_opts > < scs_120k_hz_r16_opts > < nrof_symbols_r16

_opts >

< group_or_seq_hop
_ r16_opts >

< max_num_ap_srs_pos
_ res_per_bwp_r16_opts >

< max_num_ap_srs_pos
_ res_per_bwp_per_slot
_ r16_opts >

< max_num_sp_srs_pos
_ res_per_bwp_r16_opts >

< max_num_sp_srs_pos
_ res_per_bwp_per_slot
_ r16_opts >

< dummy3_opts > < sub_slot_cfg_n_cp

_ r16_opts >

< sub_slot_cfg_e_cp
_ r16_opts >

< pusch_prep_low_prio
_ r16_opts >

< pusch_prep_high_prio
_ r16_opts >

< scs_15k_hz_120k_hz
_ r16_opts >

< scs_15k_hz_60k_hz
_ r16_opts >

< scs_30k_hz_120k_hz
_ r16_opts >

< ul_full_pwr_mode2
_srs_cfg_diff_num_srs
_ ports_r16_opts >

< four_ports_non_coherent
_ r16_opts >

< four_ports_partial
_coherent_r16_opts >

EnumType

+hop_scheme_r16

hop_scheme_r16_opts

+ul_full_pwr_mode2
_ max_srs_res_in_set_r16

ul_full_pwr_mode2_max
_srs_res_in_set_r16_opts

+cbg_pusch_processing
_type1_different_tb_per
_slot_r16

+scs_15k_hz_r16 +scs_15k_hz_r16

scs_15k_hz_r16_opts

+scs_30k_hz_r16 +scs_30k_hz_r16

scs_30k_hz_r16_opts

+scs_60k_hz_r16 +scs_60k_hz_r16

scs_60k_hz_r16_opts

+scs_120k_hz_r16 +scs_120k_hz_r16

scs_120k_hz_r16_opts

+cbg_pusch_processing
_type2_different_tb_per
_slot_r16

+supported_srs_pos
_ res_r16

+srs_pos_res_r16

uint8_t

+srs_pos_res_id_r16

asn1::rrc_nr::srs_pos
_ res_r16_s::res_map_r16_s_

+c_srs_r16 +start_position_r16

asn1::rrc_nr::srs_pos
_ res_r16_s::tx_comb_r16_c_

+ tx_comb_r16_c_()
+ tx_comb_r16_c_()
+ operator=()
+ ~tx_comb_r16_c_()
+ set()
+ type()
+ pack()
+ unpack()
+ to_json()
+ n2_r16()
and 8 more...

+tx_comb_r16 +res_map_r16

+nrof_symbols_r16

nrof_symbols_r16_opts

uint16_t

+freq_hop_r16 +freq_domain_shift_r16

+group_or_seq_hop_r16

group_or_seq_hop_r16_opts

asn1::rrc_nr::srs_pos
_ res_r16_s::res_type
_ r16_c_

+ res_type_r16_c_()
+ res_type_r16_c_()
+ operator=()
+ ~res_type_r16_c_()
+ set()
+ type()
+ pack()
+ unpack()
+ to_json()
+ aperiodic_r16()
and 8 more...

+res_type_r16

asn1::rrc_nr::srs_spatial
_ relation_info_pos_r16_c

+ srs_spatial_relation
_ info_pos_r16_c()
+ srs_spatial_relation
_ info_pos_r16_c()
+ operator=()
+ ~srs_spatial_relation
_ info_pos_r16_c()
+ set()
+ type()
+ pack()
+ unpack()
+ to_json()
+ serving_rs_r16()
and 8 more...

+spatial_relation_info
_ pos_r16

asn1::rrc_nr::srs_pos
_ res_ap_r16_s

+ pack()
+ unpack()
+ to_json()

+srs_pos_res_ap_r16

+max_num_ap_srs_pos
_ res_per_bwp_r16

max_num_ap_srs_pos
_ res_per_bwp_r16_opts

+max_num_ap_srs_pos
_ res_per_bwp_per_slot_r16

max_num_ap_srs_pos
_ res_per_bwp_per_slot
_ r16_opts

asn1::rrc_nr::srs_pos
_ res_sp_r16_s

+ pack()
+ unpack()
+ to_json()

+srs_pos_res_sp_r16

+max_num_sp_srs_pos
_ res_per_bwp_r16

max_num_sp_srs_pos
_ res_per_bwp_r16_opts

+max_num_sp_srs_pos
_ res_per_bwp_per_slot_r16

max_num_sp_srs_pos
_ res_per_bwp_per_slot
_ r16_opts

+intra_freq_daps_ul_r16

+dummy3

dummy3_opts

+multi_pucch_r16

+sub_slot_cfg_n_cp_r16

sub_slot_cfg_n_cp_r16_opts

+sub_slot_cfg_e_cp_r16

sub_slot_cfg_e_cp_r16_opts

asn1::rrc_nr::feature
_set_ul_v1610_s::ul_intra
_ ue_mux_r16_s_

+ul_intra_ue_mux_r16

+pusch_prep_low_prio_r16

pusch_prep_low_prio
_ r16_opts

+pusch_prep_high_prio_r16

pusch_prep_high_prio
_ r16_opts

+cross_carrier_sched
_ processing_diff_scs_r16

+scs_15k_hz_120k_hz_r16

scs_15k_hz_120k_hz
_ r16_opts

+scs_15k_hz_60k_hz_r16

scs_15k_hz_60k_hz_r16_opts

+scs_30k_hz_120k_hz_r16

scs_30k_hz_120k_hz
_ r16_opts

+ul_full_pwr_mode2
_srs_cfg_diff_num_srs
_ ports_r16

ul_full_pwr_mode2_srs
_cfg_diff_num_srs_ports
_ r16_opts

+ul_full_pwr_mode2
_tpmi_group_r16

+two_ports_r16

< 2 >

+four_ports_non_coherent_r16

four_ports_non_coherent
_ r16_opts

+four_ports_partial
_coherent_r16

four_ports_partial
_coherent_r16_opts