

srsran::detail::harq _process< IsDownlink >
+ id + DEFAULT_ACK_TIMEOUT_SLOTS + SHORT_ACK_TIMEOUT_DTX + MAX_NOF_TBS # logger # timeout_notifier # max_ack_wait_in_slots # last_slot_ind # last_slot_tx # last_slot_ack # slot_ack_timeout # tb_array
+ harq_process() + slot_indication() + slot_tx() + slot_ack() + empty() + has_pending_retx() + is_waiting_ack() + max_nof_harq_retxs() + tb() + reset() + cancel_harq_retxs() + get_slot_ack_timeout() # tx_common() # new_tx_tb_common() # new_retx_tb_common() # ack_info_common() # reset_tb()

< true >

srsran::detail::harq _process< true >
+ id + DEFAULT_ACK_TIMEOUT_SLOTS + SHORT_ACK_TIMEOUT_DTX + MAX_NOF_TBS # logger # timeout_notifier # max_ack_wait_in_slots # last_slot_ind # last_slot_tx # last_slot_ack # slot_ack_timeout # tb_array
+ harq_process() + slot_indication() + slot_tx() + slot_ack() + empty() + has_pending_retx() + is_waiting_ack() + max_nof_harq_retxs() + tb() + reset() + cancel_harq_retxs() + get_slot_ack_timeout() # tx_common() # new_tx_tb_common() # new_retx_tb_common() # ack_info_common() # reset_tb()

srsran::dl_harq_process
+ empty() + has_pending_retx() + is_waiting_ack() + last_alloc_params() + new_tx() + new_retx() + tx_2_tb() + ack_info() + save_alloc_params() + increment_pucch_counter()