```
bool
                              uint8 t
  +fr1_and_fr2_r17_present
+fr1_only_r17_present
                                +fr1 and fr2 r17
                                 +fr1 only r17
    +fr2 only r17 present
                                 +fr2 only r17
 asn1::rrc nr::meas
 and mob_params_mrdc
 common v1730 s::independent
     gap cfg max cc r17 s
```