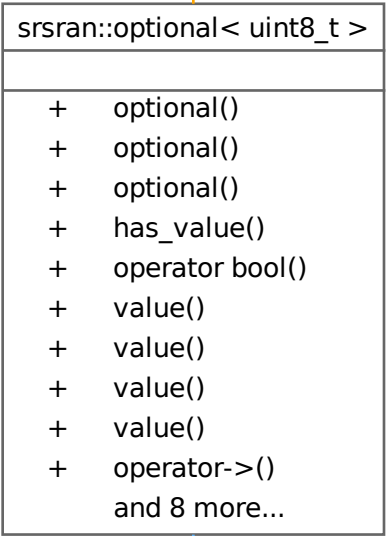
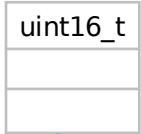
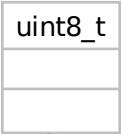
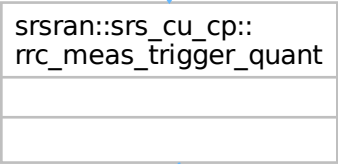


< uint8\_t >



+rsrp  
+rsrq  
+sinr



+a5\_thres\_1  
+a5\_thres\_2

+report\_on\_leave  
+use\_allowed\_cell\_list

+hysteresis

+time\_to\_trigger

