```
bool
             +ext
  +high speed demod
    ca scell r17 present
  +high_speed_meas_ca
_scell_r17_present
  +high speed meas inter
      freq r17 present
```

```
asn1::rrc_nr::high
_speed_cfg_v1700_s
+ pack()
+ unpack()
+ to_json()
```