```
srsran::cell_config
_builder_params
+ pci
+ scs_common
+ channel_bw_mhz
+ dl_arfcn
+ band
+ offset_to_point_a
```

+ coreset0_index + max_coreset0_duration

+ max_coreset0_duration + search_space0_index + k_ssb

+ nof_dl_ports + min_k1

+ csi rs enabled

+ min_k2 + tdd_ul_dl_cfg_common



::cell_config_builder _params_extended + ssb_arfcn + cell_nof_crbs + ssb_scs

+ cell_config_builder _params_extended()