```
asn1::rrc nr::sl csi
                 rs cfg r16 s::sl csi
                 rs freq alloc r16 c
                + sl_csi_rs_freq_alloc
                  r16 c ()
                + sl csi rs freq alloc
                  _r16_c ()
                + operator=()
                + ~sl csi rs freq alloc
                  _r16_c_()
                                                       uint8_t
bool
                + set()
                + type()
                + pack()
                + unpack()
                + to json()
                + sl one ant port r16()
                + sl two_ant_port_r16()
                + sl_one_ant_port_r16()
                + sl_two_ant_port_r16()
                + set_sl_one_ant port
                  _r16()
                + set sl two ant port
                  r16()
              +ext
      +sl csi rs first
      symbol r16 present +sl csi rs freq alloc r16
                                                           +sl csi rs first symbol r16
      +sl csi rs freq alloc
          r16 present
                   asn1::rrc nr::sl csi
                     _rs_cfg_r16_s
                        pack()
                        unpack()
                         to_json()
```