```
bool
```

+inter_sn_cond_pscell
_change_fdd_tdd_endc
_r17_present
+inter_sn_cond_pscell
_change_fdd_tdd_nrdc_r17
_present
+inter_sn_cond_pscell
_change_fr1_fr2_endc_r17
_present
+inter_sn_cond_pscell
_change_fr1_fr2_nrdc_r17
_present
+mn_initiated_cond_pscell
_change_fr1_fdd_endc_r17
_present
+mn_initiated_cond_pscell
_change_fr1_tdd_endc_r17
_present
+mn_initiated_cond_pscell
_change_fr2_tdd_endc_r17
_present
+sn_initiated_cond_pscell
_change_fr1_fdd_endc_r17
_present
+sn_initiated_cond_pscell
_change_fr1_tdd_endc_r17
_present
+sn_initiated_cond_pscell
_change_fr2_tdd_endc_r17
_present
...

```
asn1::rrc_nr::meas
_and_mob_params_mrdc
_common_v1700_s::cond
_pscell_change_params
_r17_s_
```