```
srsran::hal::hw accelerator
           < T. U >
   + ~hw accelerator()
   + enqueue operation()
   + dequeue operation()
                < int8 t, uint8 t >
  srsran::hal::hw accelerator
      < int8 t. uint8 t >
   + ~hw accelerator()
   + enqueue operation()
      dequeue operation()
  srsran::hal::hw accelerator
          pusch dec
+ ~hw accelerator pusch
  dec(\bar{0})
+ configure operation()
+ read operation outputs()
+ free harg context entry()
+ is external harg supported()
```