```
detail::strong_property
_t< Properties, strong
_type< T, Tag, Properties... > >
                   detail::bounded_integer
                        base< Integer >
                     +
                          strong_type()
                     +
                          strong_type()
                     +
                          strong_type()
                     +
                          operator=()
                          value()
                          value()
 detail::bounded_integer
     base< uint8_t >
   +
       strong_type()
   +
       strong_type()
   +
       strong_type()
       operator=()
   +
       value()
       value()
                   srsran::bounded_integer
< Integer, MIN_VALUE,
MAX_VALUE >
                    + bounded_integer()
                    + bounded_integer()
                    + bounded_integer()
                    + operator=()
                    + valid()
                    + operator Integer()
                    + to_uint()
                    + to_int()
                    + operator++()
                    + operator++()
                       and 10 more...
                    + min()
                    + max()
                       assert_bounds()
                         < uint8_t, 0, 31 >
    srsran::bounded_integer
       < uint8_t, 0, \overline{3}1 >
     + bounded_integer()
     + bounded_integer()
     + bounded_integer()
     + operator=()
     + valid()
                                      aggregation_level
                                                                 sib1_rtx_periodicity
     + operator uint8_t()
     + to_uint()
     + to_int()
     + operator++()
     + operator++()
        and 10 more...
     + \min()
     + max()
     # assert_bounds()
                                                +si_message_dci_aggr
                     +si_message_mcs_index
                                                                           +sib1_retx_period
                        +sib1 mcs index
                                                 +sib1_dci_aggr_lev
                                     srsran::scheduler_si
                                        _expert_config
```