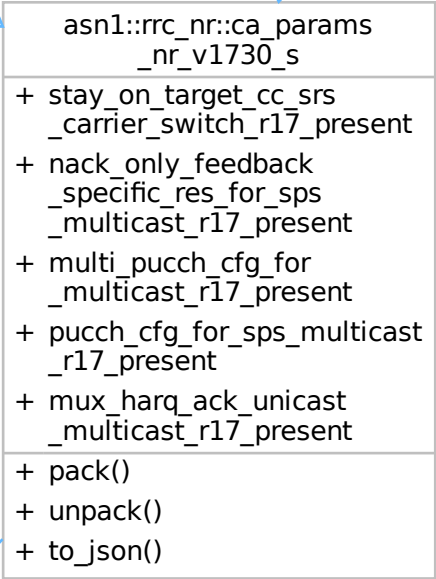


+dmrs\_bundling\_non  
\_back\_to\_back\_tx\_per  
\_bc\_r17\_present  
+dmrs\_bundling\_pucch  
\_rep\_per\_bc\_r17\_present  
+dmrs\_bundling\_pusch\_multi  
\_slot\_per\_bc\_r17\_present  
+dmrs\_bundling\_pusch\_rep  
\_type\_a\_per\_bc\_r17\_present  
+dmrs\_bundling\_pusch\_rep\_type  
\_b\_per\_bc\_r17\_present  
+dmrs\_bundling\_restart\_per  
\_bc\_r17\_present  
+fdm\_codebook\_for\_mux\_unicast  
\_multicast\_harq\_ack\_r17\_present  
+max\_num\_g\_rnti\_harq\_ack\_codebook  
\_r17\_present  
+mode1\_for\_type1\_codebook\_generation  
\_r17\_present  
+mode2\_tdm\_codebook\_for\_mux  
\_unicast\_multicast\_harq\_ack\_r17  
\_present  
...

+max\_num\_g\_rnti\_harq  
\_ack\_codebook\_r17

+ca\_params\_nr\_for\_dc  
\_v1730\_present



+ca\_params\_nr\_for\_dc  
\_v1730

