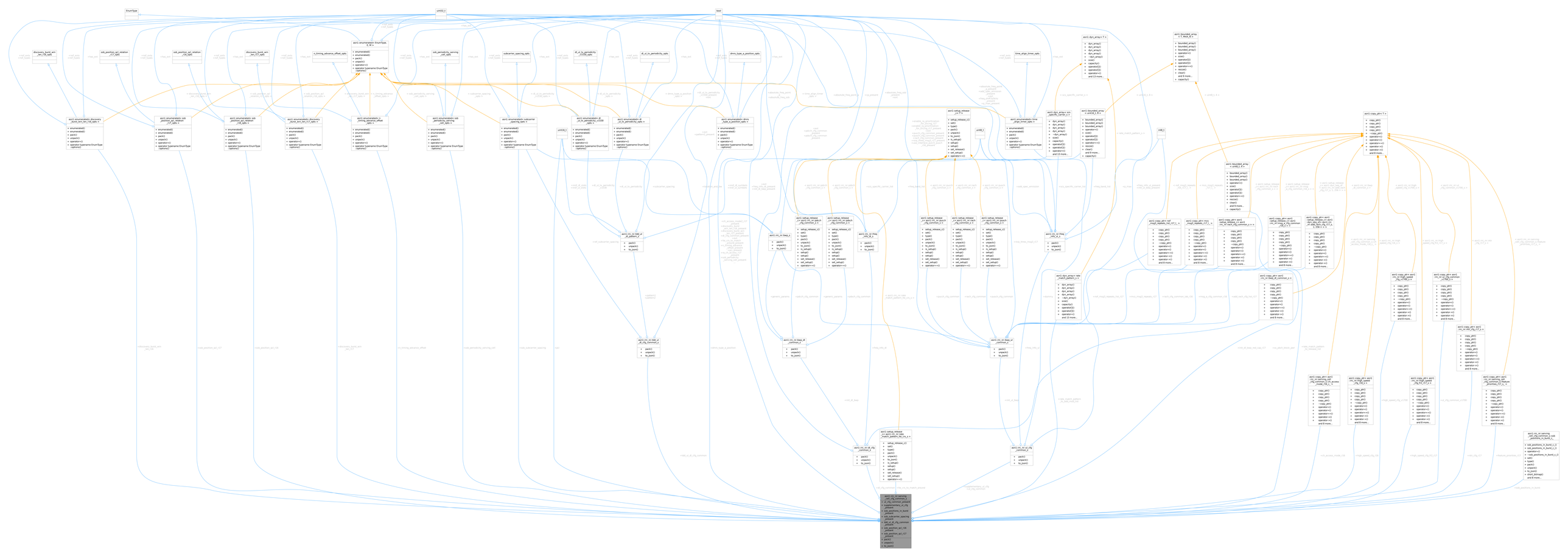
EnumType
uint32_t
bool
discovery_burst_win__len_r16_opts
ssb_position_qcl_relation__r16_opts
ssb_position_qcl_relation__r17_opts
discovery_burst_win__len_r17_opts
n_timing_advance_offset_opts
avrc1::enumerated< EnumType, 2, 0 >
ssb_periodicity_serving__r16_opts
subcarrier_spacing_opts
dl_ul_tx_periodicity__r15750_opts
dl_ul_tx_periodicity_opts
dmrs_type_a_position_opts
time_align_timer_opts
avrc1::dyn_array< T >
avrc1::bounded_array< T, MAX_N >
avrc1::bounded_array< uint8_t, 4 >
avrc1::bounded_array< uint8_t, 8 >
avrc1::enumerated< discovery_burst_win_len_r16_opts >
avrc1::enumerated< ssb_position_qcl_relation__r16_opts >
avrc1::enumerated< ssb_position_qcl_relation__r17_opts >
avrc1::enumerated< discovery_burst_win_len_r17_opts >
avrc1::enumerated< n_timing_advance_offset_opts >
avrc1::enumerated< ssb_periodicity_serving__r16_opts >
avrc1::enumerated< subcarrier_spacing_opts >
avrc1::enumerated< dl_ul_tx_periodicity__r15750_opts >
avrc1::enumerated< dl_ul_tx_periodicity_opts >
avrc1::enumerated< dmrs_type_a_position_opts >
avrc1::enumerated< time_align_timer_opts >
avrc1::dyn_array< sss_specific_carrier_s >
avrc1::bounded_array< sss_specific_carrier_s, 8 >
avrc1::setup_release_c< T >
avrc1::setup_release_c< uint8_t >
avrc1::copy_ptr< T >
avrc1::copy_ptr< ref_msg3_repeats_ref_r17_s_ >
avrc1::copy_ptr< recv_msg3_repeats_r17_s_ >
avrc1::copy_ptr< avrc1::setup_release_c< avrc1::enumerated< pdsch_cfg_common_sib_r16_s_::msg1_repeats_r16_opts > > >
avrc1::copy_ptr< avrc1::setup_release_c< avrc1::enumerated< pdsch_cfg_common_sib_r17_s_::msg1_repeats_r17