



ECE/CS 252

Intro to Computer Engineering

Week 10 Discussion



Attendance via TopHat

Course code: **265393**

Attendance code:

4585



Reminders

- Up Next:
 - Subroutines
- Remember your videos and reading
 - Including the video quiz!
 - HW10, Tuesday 10 PM
- **A4 on Friday Nov 15**

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Exam Review

- Set a register to a value: $R5 \leftarrow -3$

AND R5, R5, #0 ; clear R5
ADD R5, R5, -3 *[-16, 15]*

<div><div></div><div></div><div></div><div></div><div></div></div>					
1	0	0	0	0	-16
0	1	1	1	1	15

- Set a register to a value: $R5 \leftarrow -17$

LD R5, value17

at the end of code

value17 . Fill # -17

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Exam Review

- Copy register value to another register: $R1 \leftarrow R2$

① `ADD R1, R2, #0`

② `ADD R1, R2, -1`

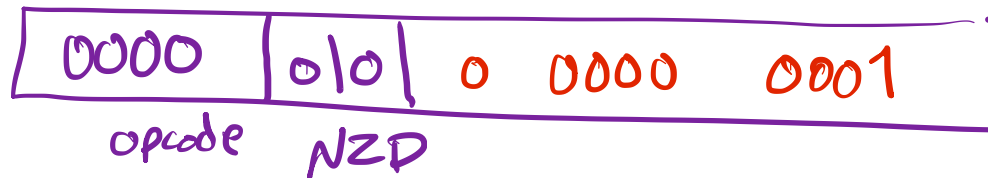
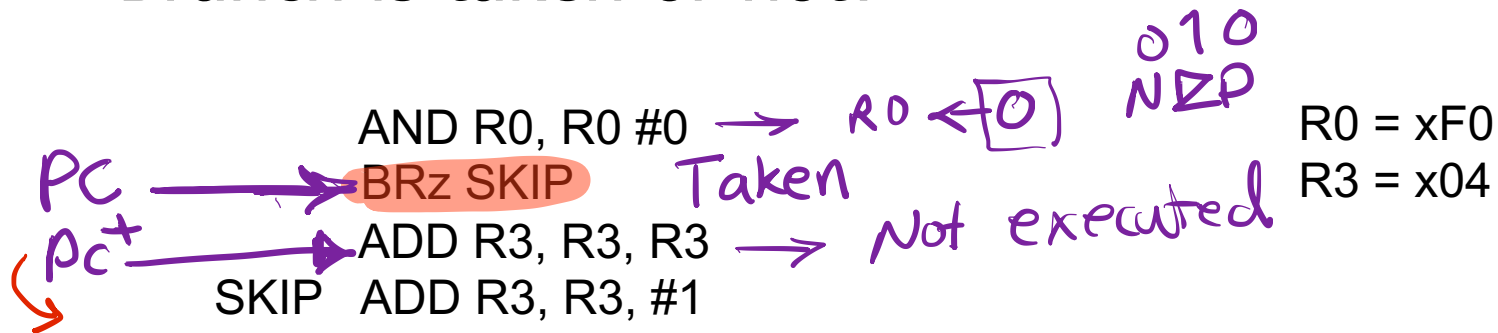
-1
↓
all one
111 ... 1

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Exam Review

- Branch is taken or not?



$$\text{effective addr} = PC^+ + \text{SEXT}(\text{offset})$$

⇓
SKIP =

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Exam Review

- Branch is taken or not?

```
AND R0, R0, R2  
BRZ SKIP  
ADD R3, R3, R3  
SKIP ADD R3, R3, #1
```

NOT Taken
executed

R0 = xF0
R2 = x10
R3 = x04

FB
10 → 8 1111 0000
0001 0000
0001 0000

NZP → 001

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Exam Review

- Branch is taken or not?

AND R0, R0, R2		
BRzp SKIP	Taken	
ADD R3, R3, R3	x	not executed
SKIP ADD R3, R3, #1	✓	

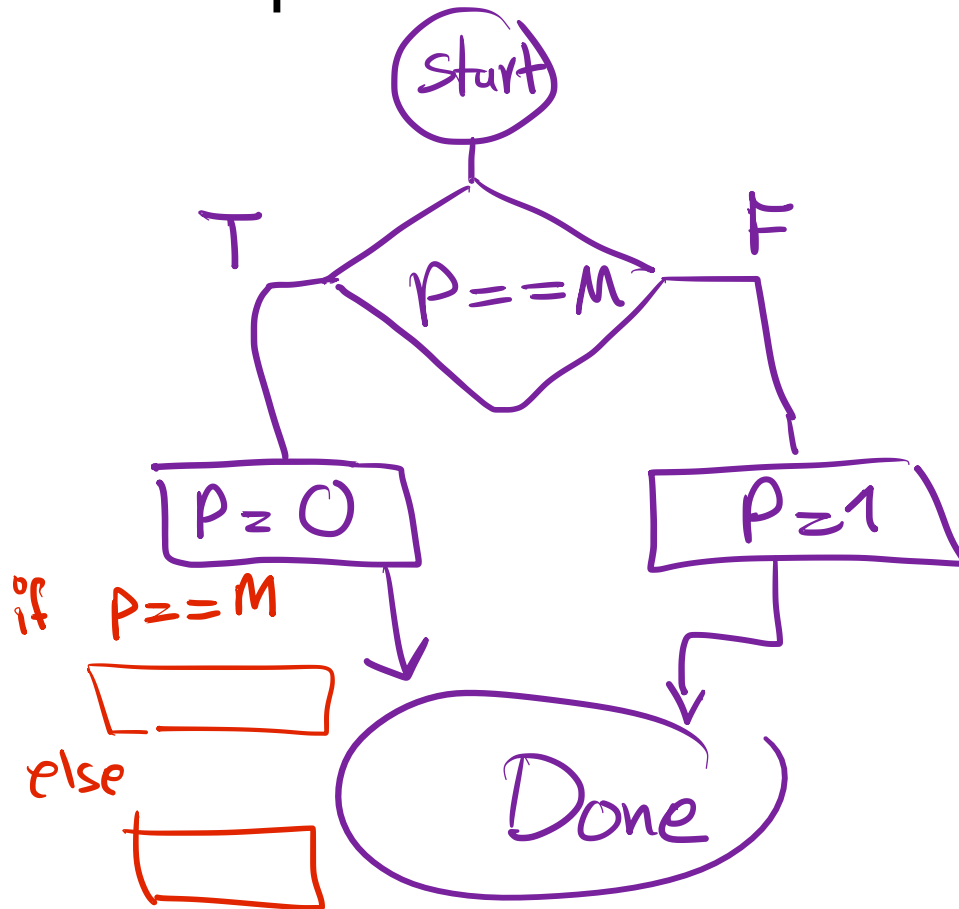
R0 = xF0
R2 = x10
R3 = x04

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Exam Review

- Complete Branch condition



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.orig x0200
START

→ LD R0, P
→ LD R1, M
Negate $\left\{ \begin{array}{l} \text{NOT R1, R1} \\ \text{ADD, R1, R1, \#1} \end{array} \right.$
 $P-M \leftarrow \text{ADD R2, R0, R1}$
BR[] PgetsOne
NP

PgetsZero

AND R4, R4, #0
ST R4, P

[BR Done]

PgetsOne

AND R4, R4, #0
AND R4, R4, #1
ST R4, P

[]

[Done]

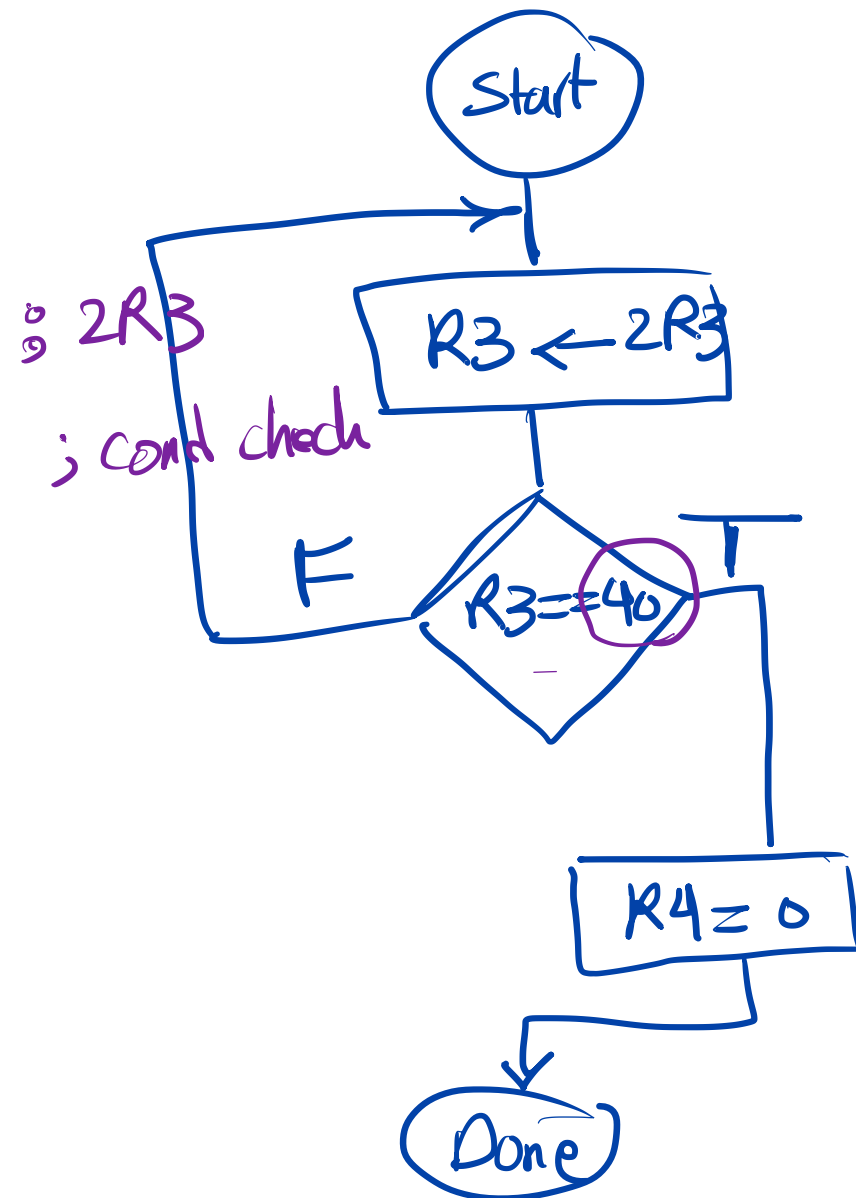
spin BR spin
P .FILL x10
M .FILL x20



Exam Review

- write code

Start
LD R1, ValueNeg40
loop ADD R3, R3, R3
ADD (R2), R1, R3
BRNP loop
AND R4, R4, #0
Spin BR Spin
valueNeg40 = Fill -40





.FILL, .BLKW, .STRINGZ

- .FILL #8 1 line

0x8

- .BLKW #8 8 lines

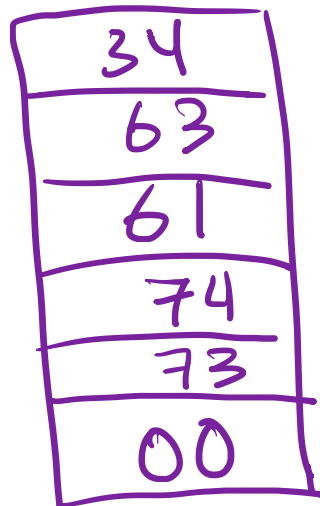
?
?
?
?
?
?
?
?
- .STRINGZ "8" 2 lines

0x38
00



.STRINGZ

- VAR .STRINGZ "4cats"



HEX	CHAR	description	HEX	CHAR	HEX	CHAR	HEX	CHAR
00	NUL	<i>null</i>	20	space	40	@	60	`
01	SOH	<i>start of header</i>	21	!	41	A	61	a
02	STX	<i>start of text</i>	22	"	42	B	62	b
03	ETX	<i>end of text</i>	23	#	43	C	63	c
04	EOT	<i>end of trans</i>	24	\$	44	D	64	d
05	ENQ	<i>enquiry</i>	25	%	45	E	65	e
06	ACK	<i>acknowledge</i>	26	&	46	F	66	f
07	BEL	<i>bell</i>	27	'	47	G	67	g
08	BS	<i>backspace</i>	28	(48	H	68	h
09	HT	<i>horizontal tab</i>	29)	49	I	69	i
0A	LF	<i>line feed</i>	2A	*	4A	J	6A	j
0B	VT	<i>vertical tab</i>	2B	+	4B	K	6B	k
0C	FF	<i>form feed</i>	2C	,	4C	L	6C	l
0D	CR	<i>carriage return</i>	2D	-	4D	M	6D	m
0E	SO	<i>shift out</i>	2E	.	4E	N	6E	n
0F	SI	<i>shift in</i>	2F	/	4F	O	6F	o
10	DLE	<i>data link escape</i>	30	0	50	P	70	p
11	DC1	<i>device control 1</i>	31	1	51	Q	71	q
12	DC2	<i>device control 2</i>	32	2	52	R	72	r
13	DC3	<i>device control 4</i>	33	3	53	S	73	s
14	DC4	<i>device control 4</i>	34	4	54	T	74	t
15	NAK	<i>negative ack</i>	35	5	55	U	75	u
16	SYN	<i>synchronous idle</i>	36	6	56	V	76	v
17	ETB	<i>end of trans block</i>	37	7	57	W	77	w
18	CAN	<i>cancel</i>	38	8	58	X	78	x
19	EM	<i>end of medium</i>	39	9	59	Y	79	y
1A	SUB	<i>substitute</i>	3A	:	5A	Z	7A	z
1B	ESC	<i>escape</i>	3B	;	5B	[7B	{
1C	FS	<i>file separator</i>	3C	<	5C	\	7C	
1D	GS	<i>group separator</i>	3D	=	5D]	7D	}
1E	RS	<i>record separator</i>	3E	>	5E	^	7E	~
1F	US	<i>unit separator</i>	3F	?	5F	_	7F	DEL



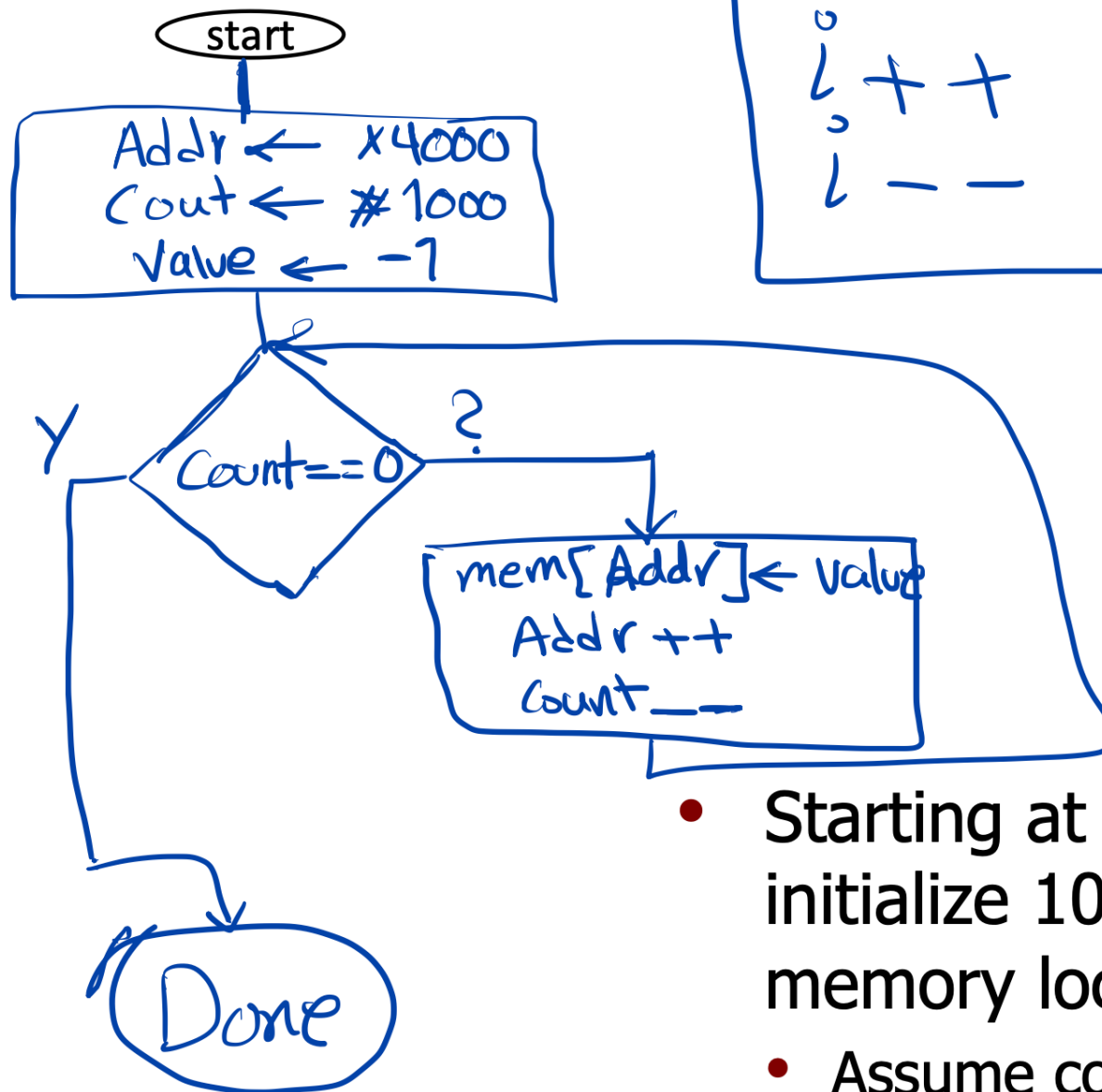
Programming Example

start

- Starting at address x4000, initialize 1000 consecutive memory locations to -1
 - Assume code starts at x0200



Programming Example



$i++$
 $i--$

$i = i + 1$
 $i = i - 1$

- Starting at address x4000, initialize 1000 consecutive memory locations to -1
- Assume code starts at x0200



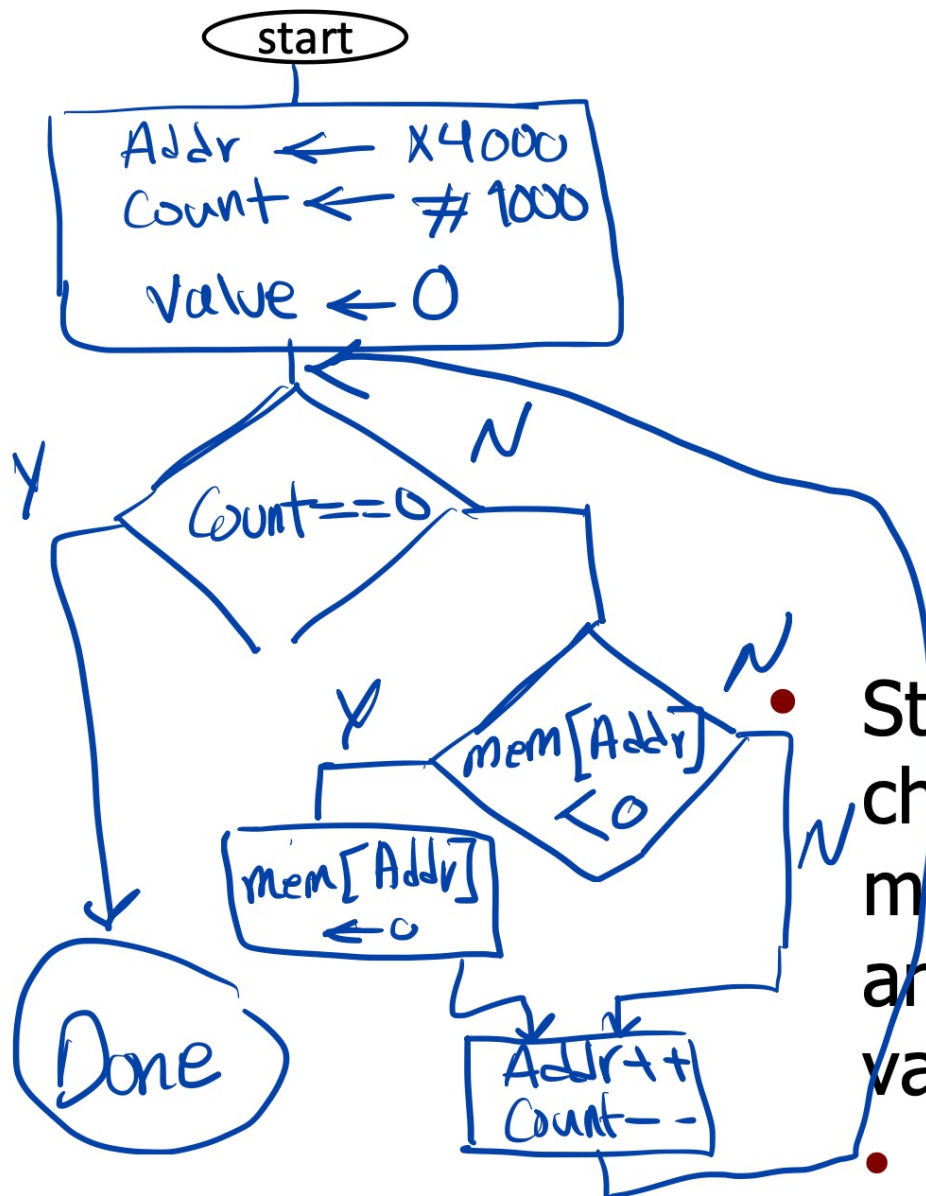
Programming Example

start

- Starting at address x4000, check 1000 consecutive memory locations and set any negative values to 0
 - Assume code starts at x0200



Programming Example



- Starting at address x4000, check 1000 consecutive memory locations and set any negative values to 0

- Assume code starts at x0200



