Adrián Silva Palafox

Microprocesadores avanzados

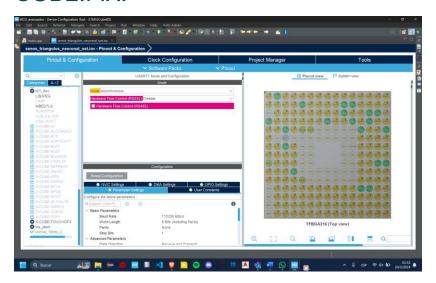
74799

Ing. Electrónica y telecomunicaciones

MAIN.CPP:

https://github.com/La-guajolota/STM32F411CEU-owo-STM32F746/blob/main/senos_triangulos_neuronal_net/Core/Src/main.cpp

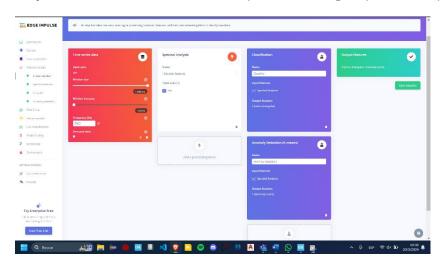
CUBE.MX:

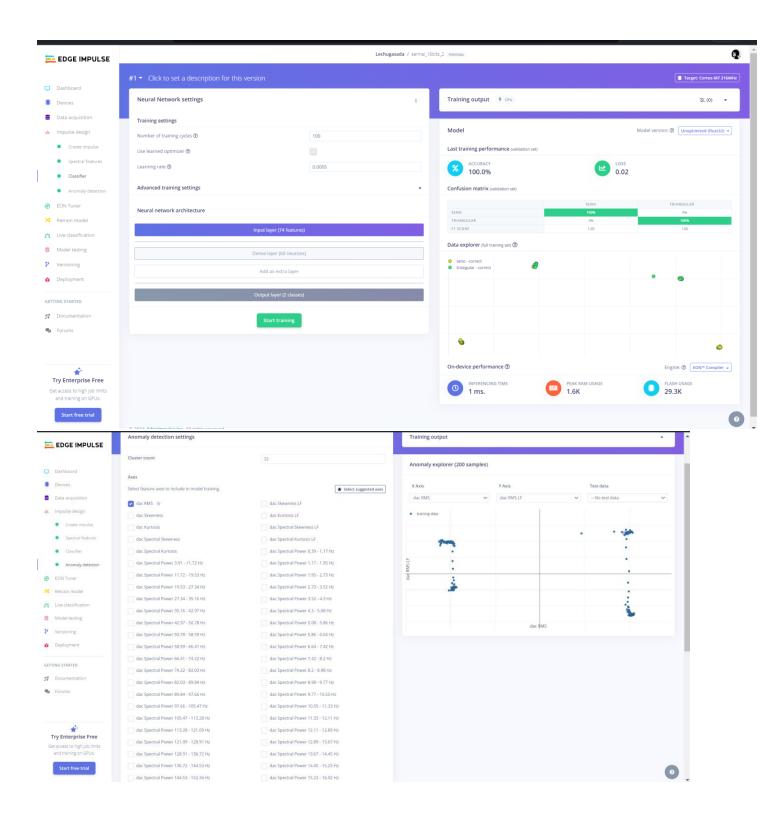


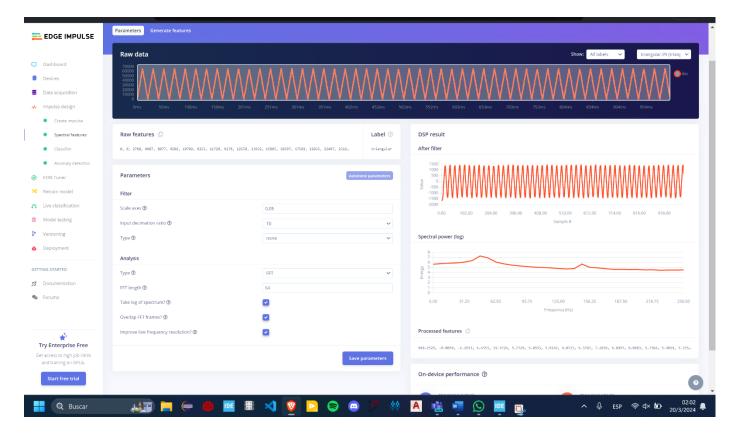
EDGE_IMPULSE:

Scripts de pyhton que se escribieron para generar los trainings y tests. <a href="https://github.com/La-guajolota/STM32F411CEU-owo-STM32F746/tree/main/redes-pyhton/Senos-Triangulares-senalesRuidosas-pyhton/Senos-pyh

Proyecto donde se entrenó la red: https://studio.edgeimpulse.com/public/365511/live







Conecciones y el Uart:

