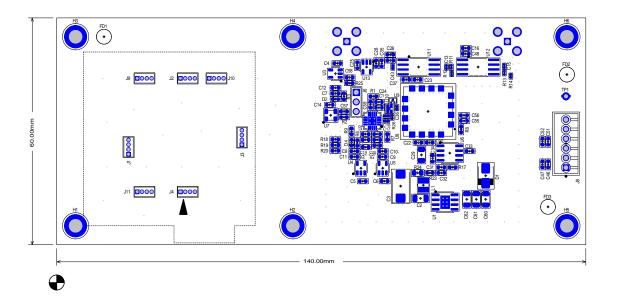


Impedance controlled tracks

A3 1CM

	Layer	Impedance	Track width	Spacing	Ref. layer 1	Ref. layer 2	Tolerance	Note
Γ	L1	50 OHM	0.476	0.774		L2	+/-10%	with solder mask
Γ	L1	50 OHM	0.510	0.757		L2	+/-10%	without solder mask

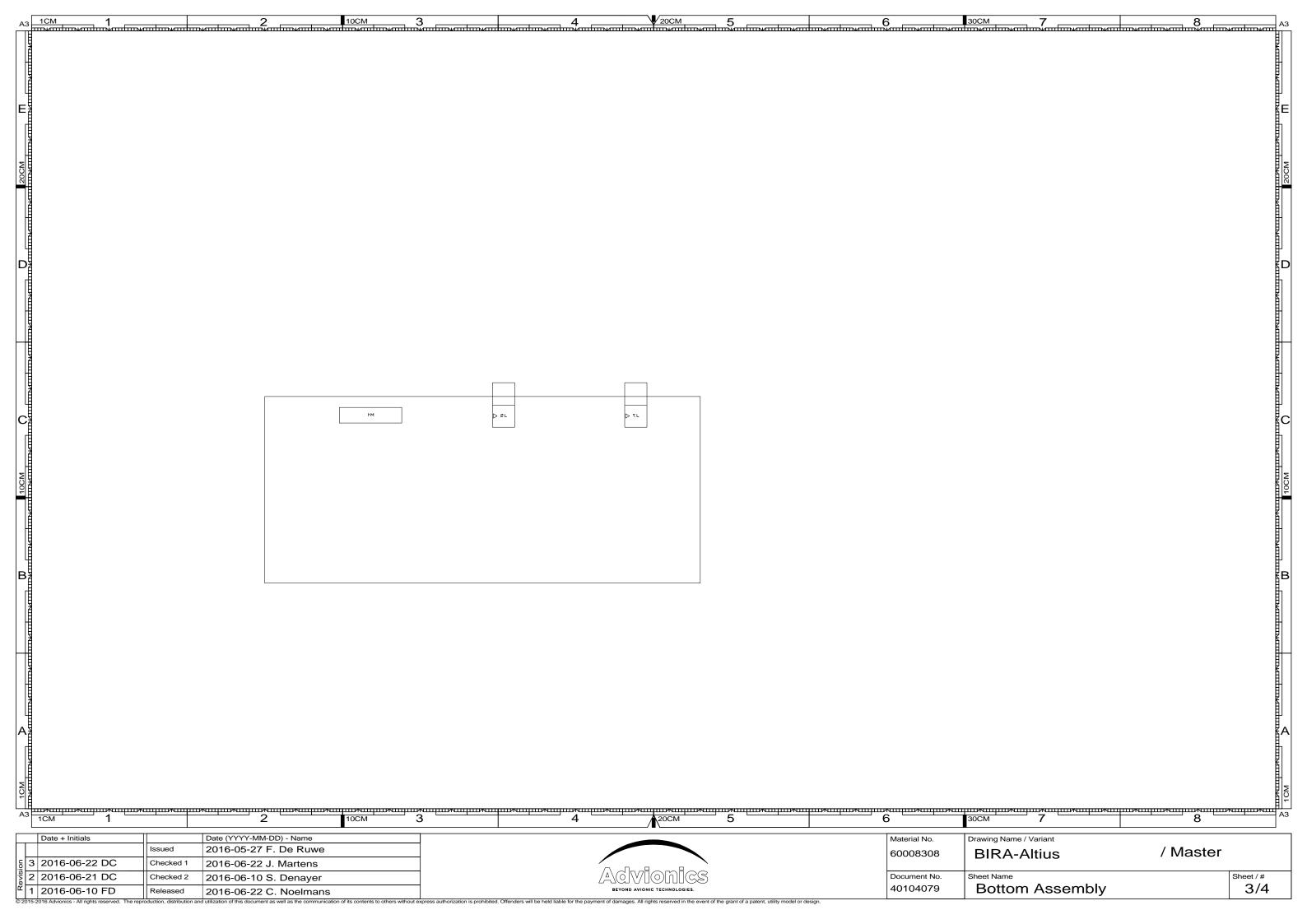


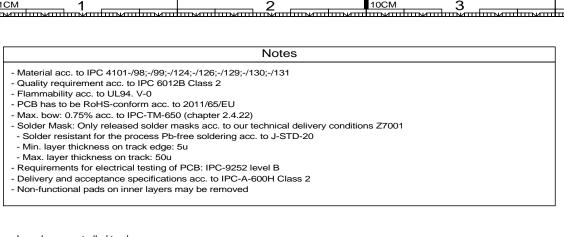
Description	Base Tickness (µm)	IPC ref.	Layer	Function
Silkscreen		See note	Top Silk	Silkscreen
Soldermask 1		See note	Top Solder Resist	Soldermask
Copper Foil	18		Top Elec	Layer 1
Prepreg	100	See note		
Prepreg	180	See note		
Copper Foil	35		GND	Layer 2
Prepreg	930	See note		
Copper Foil	35		VCC	Layer 3
Prepreg	180	See note		
Prepreg	100	See note		
Copper Foil	18		Bottom Elec	Layer 4
Soldermask 2		See note	Bottom Solder Resist	Soldermask

| Date + Initials | Date (YYYY-MM-DD) - Name | Issued | 2016-05-27 F. De Ruwe | 2016-06-22 J. Martens | 2 2016-06-21 DC | Checked 2 | 2016-06-10 S. Denayer | 2 2016-06-10 FD | Released | 2016-06-22 C. Noelmans | 2016-06-2



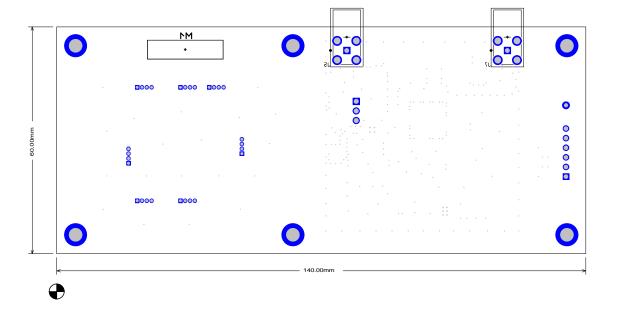
Material No.	Drawing Name / Variant		
60008308	BIRA-Altius	/ Master	
Document No.	Sheet Name		Sheet / #
40104079	Top Component overview	/	2 /4





Impedance controlled tracks

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Prepreg	180	See note		
Prepreg	100	See note		
Copper Foil	18		Bottom Elec	Layer 4
Soldermask 2		See note	Bottom Solder Resist	Soldermask

A3 1CM 1		2 10CM	3 4 20CM 5	6	30CM 7	8	
Date + Initials	la a consideration of the cons	Date (YYYY-MM-DD) - Name		Material No.	Drawing Name / Variant	,	
	Issued	2016-05-27 F. De Ruwe		60008308	BIRA-Altius	/ Maste	er
§ 3 2016-06-22 DC	Checked 1	2016-06-22 J. Martens	Advieries				
2 2016-06-21 DC	Checked 2	2016-06-10 S. Denayer		Document No.	Sheet Name		Sheet /#
1 2016-06-10 FD	Released	2016-06-22 C. Noelmans	BEYOND AVIONIC TECHNOLOGIES. express authorization is prohibited. Offenders will be held liable for the payment of damages. All rights reserved in the event of the grant of a patent, utility model or	40104079	Bottom Compone	nt overview	4 /4