

Acceptance Test Plan: v0.2

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This document outlines all of the tests required to deliver LFEV-Y5. The plan is presented as an overview with the ATP number next to the test. This refers to the document that describes the test procedure. The requirements are from the SoW for 2017



Lafayette College: Electrical and Computer Engineering

ATPsNone of these tests can be viewed as completed until appropriate documentation has been uploaded to the webpage.

Item	Item Demonstrated Successful Test Verification				
Item	description	Requirements	Criteria	Method	
ATP-01	Accumulator integration	R001a R001c R001d R001e R002a R002c R004a (TSV part) R005a R005b (Manual)	Packs power motor and all telemetry is recorded by VSCADA. Control by using the throttle. Verify by accelerating and looking at dash, pack screens, and log files remotely	Test	
ATP-02	Accumulator charging	R001b R001g R002b R002h	Packs charge by the charging port and open the safety loop VSCADA reacts correctly Verify by looking at the dash	Test	
ATP-03	CAN Bus link	R002a R002c R002d R002e R002f R002g R002j R002k R003a(8) R003d R004a (CAN Bus part) R005a (CAN Bus part) R005c (CAN Bus part) R007c R007c	DAQ by VSCADA of TSI, GLV, TSV, Cooling. Verify by looking at cell phone and looking at dash and remote computer in each mode of VSCADA	Test	
ATP-04	Safety loop	R001g R002b R002c R002d R002k	Fault by: Crashing BRB IMD Cooling	Test	

		R002m R003b R003c R003d R004a (Safety loop part) R005c (IMD fault) R007b	VSCADA limit Pack fault Throttle fault Brake fault User defined limit (warn) User defined limit (halt) Pack charging Verify by looking at the dash, the remote computer and the cellphone	
ATP-05	Cruise Control	R002l R005b (Software)	Motor can maintain desired speed Verify by checking motor speed compared to target	Test
ATP-06	24h endurance test	GPR006	At the end of all other tests leave the car running for 24h	Test
ATP-07	Unexpected shutdown	R002k	VSCADA works after unexpected GLV shutdown Packs stop powering motor with GLV shutdown	Test
ATP-08	Expected shutdown	R002i	All hardware is put into a safe state for storage. Check packs and all boards are off	Test

Compliance Matrix

All requirements should also have a QA by each subsystem.

Requirement	Test(s) to demonstrate acceptance
R001a	ATP-01
R001b	ATP-02 OR https://sites.lafayette.edu/ece492-
	sp16/files/2016/05/QAR001b.pdf
R001c	ATP-01
R001d	ATP-01
R001e	ATP-01
R001f	https://sites.lafayette.edu/ece492-
	sp16/files/2016/05/QAR001e.pdf
R001g	ATP-02

R002a	ATP-01 or ATP-03
R002b	ATP-02
R002c	ATP-01 OR ATP-03 OR ATP-04
R002d	ATP-01 OR ATP-03 OR ATP-04
R002e	ATP-03
R002f	ATP-03
R002g	ATP-03
R002h	ATP-02 OR ATP-03
R002i	ATP-02 AND ATP-13
R002j	ATP-03
R002k	ATP-03
R002l	ATP-08
R002m	ATP-04
R003a(1)	Any ATP
R003a(2)	QA by GLV
R003a(3)	QA by GLV
R003a(4)	QA by GLV
R003a(5)	QA by GLV
R003a(5)	QA by GLV
R003a(6)	QA by GLV
R003a(7)	QA by GLV
R003a(8)	ATP-03
R003b	ATP-04
R003c	QA by GLV
R003d	ATP-03
R004a	ATP-01 AND ATP-03 AND ATP-04
R004b	QA by Interconnect
R005a	ATP-01 AND ATP-03
R005b	ATP-01 AND ATP-08
R005c	ATP-04
R005d	QA by TSI
R006	Any ATP
R007a	QA by Cooling
R007b	ATP-04
R007c	ATP-03
R007d	ATP-03
R007e	Waived
R007f	QA by Cooling
R007g	QA by Cooling

Waived or modified requirements and questions

Requirement	Reason
R003a(4)	Cannot tell if GLV is from the battery or 24VDC

R002h	Cannot tell if GLV is from the battery or 24VDC
R007e	Waived
R005d	We've changed the switches

ATP-01 intermediate steps

- 1. Packs provide 96V to motor
- 2. Throttle pedal controls motor
- 3. VSCADA is aware about drive mode
- 4. Remote computer is aware about drive mode
- 5. Cell phone is aware about drive mode

ATP-02 intermediate steps

- 1. Packs charge and perform safety checks
- 2. VSCADA is aware about charging
- 3. Remote computer is aware about charging
- 4. Cell phone is aware about charging

ATP-03 intermediate steps

Test	Seen on VSCADA	Seen on Remote	Seen on Cell
Cell Temperature			
Cell Voltage		// //	
Pack Current			
Pack SoC			
Pack Status			
Pack Voltage			
GLV Voltage			
GLV SoC			
GLV Current			
GLV Temperature			
Safety loop status			
RPM gauge			
(Dyno)			
Strain gauge			
Throttle position			
Brake status			
IMD status			
FWD/REV status			
Precharge status			
MC temp			
MC current			
Cooling temp in			
Cooling flow			
Cooling temp out			

TSI temp		
Speed		
Safety loop status		

ATP-04 intermediate steps

Fault	Safety	Seen on	Seen on	Seen on
	loop trip	VSCADA	Remote	Cell
Driver resettable BRB				
Non driver resettable BRB				
Crash protection				
Over temperature cooling				
Under flow cooling				
IMD fault				
Cell overtemp				
Cell overcurrent				
Cell overvoltage				
Cell undervoltage				
Brake overtravel				
VSCADA defined violation				

ATP-05 intermediate steps

- 1. TSI has the hardware to control the throttle
- 2. Physics model simulated
- 3. Physics model implemented on TSI
- 4. TSI can hold a throttle position

ATP-06 intermediate steps

1. Run system for 24h

ATP-07 intermediate steps

- 1. VSCADA reboots after unexpected shutdown
- 2. Packs disengage from the motor with loss of power

ATP-08 intermediate steps

- 1. VSCADA safely shuts down
- 2. Packs safely shutdown
- 3. Cooling safely shuts down
- 4. TSI safely shuts down
- 5. GLV safely shuts down