



CCSP 2019

International Conference
on
Communication Computing and Signal Processing
December 21-23, 2019

Department of Electronics & Communication Engineering
Dr. B. R. Ambedkar National Institute of Technology, Jalandhar

ABOUT NITJ

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Dr B R Ambedkar National Institute of Technology was established in the year 1987 as Regional Engineering College and was given the status of National Institute of Technology (Deemed University) by the Government of India on October 17, 2002 under the aegis of Ministry of Human Resource Development, New Delhi. Now the Ministry of Human Resource Development, Government of India has declared the Institute as “*Institute of National Importance*” under the act of Parliament-2007. A large number of reputed Industrial houses in the country visit the Institution and select the final year students as Engineers/ Management Trainees. The Institute has been placed amongst top 50 engineering institutions of the country as per the survey conducted by Mint-C Fore in the year 2008. As one of the National Institutes of Technology (NIT), the Institute has the responsibility of providing high quality education in Engineering, Technology and Sciences to produce competent technical and scientific manpower for the country. The Institute offers B. Tech programs in nine engineering disciplines. In addition to these programs, the institute offers M.Tech, PhD programs in several disciplines of Engineering, Technology and Science.

ABOUT DEPARTMENT OF ECE

The Department of Electronics & Communication Engineering, Dr. B. R. Ambedkar NIT Jalandhar has been offering B.Tech programme in Electronics & Communication Engineering since 1989, M.Tech programme in Electronics & Communication Engineering (Full-Time) and VLSI Design (Full Time) from year 2007. The Department also offers Ph.D. program in various specializations, such as Image Processing, Biomedical Engineering, VLSI Design, Communication Systems, Artificial Intelligence, Machine Learning etc. The Department has established state-of-the-art laboratories for B.Tech, M.Tech and Ph.D. research work. The Department has advanced Biomedical Signal Processing and Telemedicine Laboratory, VLSI Design Laboratory and Communication System Laboratory with sophisticated research equipments. The Department has faculty members with Ph.D degree from reputed Institutions and have approximately 250 publications in the last 5 year in SCI/Scopus/WOS/Reputed Journals. Faculty members and research scholars of the Department are actively involved in research activities and have various publications in journals of international repute like IEEE Transactions, ACM, Springer, Taylor & Francis, Elsevier Journals etc.

TRACKS

Signal Processing and Advanced Applications

This track emphasizes on the current ideas and trends in signal processing and application of techniques for the processing, understanding, learning, retrieval, mining, and extraction of information from signals. It will include information processing and the theory and application of filtering, coding, transmitting, estimating, detecting, analysing, recognizing, synthesizing, recording, and reproducing various audio, video, medical, speech, image, communication, geophysical, sonar, radar, musical signals and Plant signalling etc.

Image Processing and Its Applications

This track focuses on novel theories and algorithms for image processing with its advanced applications in various fields. It will include Computer Vision, pattern recognition, image/ video filtering, restoration, biomedical imaging, image and video communications, electronic imaging, image and video systems, motion sensing in videos, remote sensing, Biomedical and applications, etc.

Computing and Communication Networks

This track focuses on the topics that advance to the state-of-the-art of communications and networking research. It includes architecture, protocols, cross-layer, and cognition cycle design for cognitive networks, machine learning and artificial intelligence for cognitive communications and networks, end-to-end and distributed intelligence at and beyond all layers of communications, software-defined networking, cognitive radios, spectrum sharing, trading and relevant economical aspects of networks, security and privacy issues in communication networks, optical communication networks, 4G and 5G technologies, novel emerging services, Antenna and RF Design, applications enabled by such concepts etc.

Wireless Sensor Networks

This track focuses on wireless sensor networks and its key role in the execution of the applications. It will include coverage, connectivity, data aggregation, energy efficient MAC protocol, mobility management, underwater sensor networks, energy efficient routing protocols, deployment strategies, security attacks and attack detection, efficient flooding, congestion control and avoidance, bio inspired routing techniques, etc.

VLSI: Devices, Materials, Circuits and Systems

This track focuses on design and realization of microelectronic systems using VLSI/ULSI technologies. It will include systems architecture, chips and wafer fabrication, system design and partitioning, logic and memory design, digital and analog circuit design, layout synthesis, CAD tools, chips and wafer fabrication, testing and packaging, system level qualification, reliable VLSI/ULSI systems, MEMS, high performance computing, wafer scale integration and multichip modules (MCMs), high-speed interconnects in microelectronic systems, VLSI/ULSI neural networks, adaptive computing systems with FPGA components, mixed analog/digital systems, cost, performance tradeoffs of VLSI/ULSI Systems, adaptive computing using reconfigurable components (FPGAs), Nanoscale Semiconductor Devices, Modelling and Simulation, Bulk and nano materials, Quantum Physics, Device Physics, PV Cells.

Artificial Intelligence and Deep Learning

This track focuses on natural language processing, artificial intelligence and computational linguistics with respect to the interactions between computers and human languages. It includes deep learning, algorithmic game learning and computational mechanism design, neuromorphic computing genetic algorithms, grammatical evolution, differential evolution, probabilistic meta heuristic, swarm intelligence, ant colony algorithms, artificial immune systems, high performance computing and computational intelligence, fuzzy logic, Neural Network, Bayesian statistical methods, multi-agent systems, stochastic optimization etc.

Assistive Technologies

Emerging Technologies, IOT, IOE, Mechatronics, Machine Learning, Human Computer Interface, ICT, Industrial Automations, Autism, Computer Science and Information Technology.



SUBMISSION RULES

Author should submit their original unpublished work in pdf format to CCSPT 2019. Simultaneous submissions (Papers already submitted to other Conferences/ Journals) are not allowed. Submission should strictly follow recommended IEEE conference template, which can be found at: [WEBSITE](#)

The decision regarding the acceptance of paper is at the direction of Technical Program Committee. All the paper submission will be done on EDAS conference management software.

Paper Submission: [WEBSITE](#)

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computer Science**

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Berkley Wireless Research Centre

MIT, USA

Prof. Yuh-Jong Hu

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REGISTRATION FEES

Registration type	IEEE member	Non-IEEE member
Academician/ Researcher	Rs. 4000	Rs. 4500
Student	Rs. 1500	Rs. 2500
Participants	Rs. 1000	Rs. 1500
International Authors/ Participants	\$ 150	\$ 200

IMPORTANT DATES

Full Paper Submission Deadline	Sep. 30, 2019
Acceptance Notification Deadline	Oct. 08, 2019
Conference Registration and Camera Ready Paper	Oct. 18, 2019

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