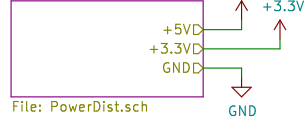


Sheet: Power Distribution



Isolated +5V and +3.3V.
Use GND as common gnd for electronic components.
Connect GNDPWR when using +12V.

Sheet: CAN



Sheet: Contactor Driver



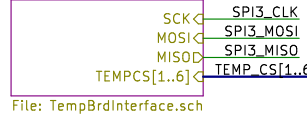
Sheet: EEPROM



Sheet: Voltage Board Interface



Sheet: Temperature Board Interface



Sheet: Current Board Interface



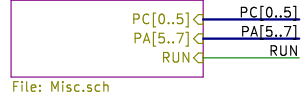
Sheet: Error Light



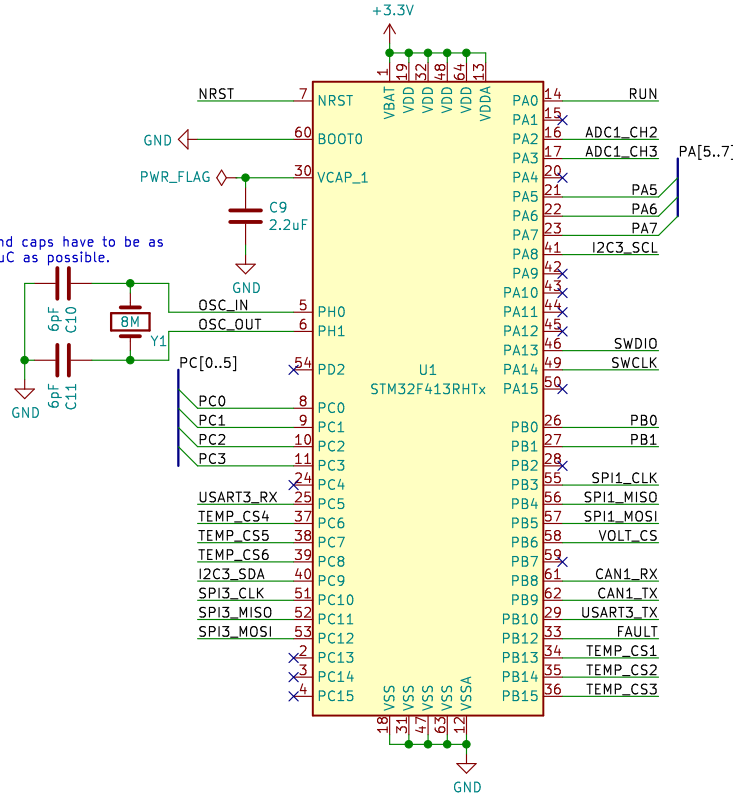
Sheet: USB



Sheet: Misc

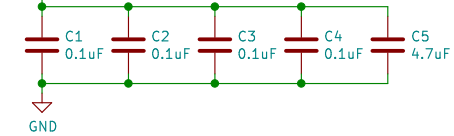


The crystal and caps have to be as close to the uC as possible.

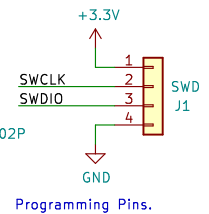
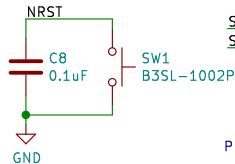
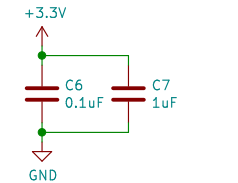


Bypass Capacitors

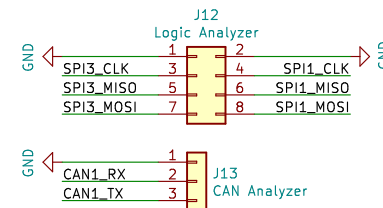
Place these capacitors as close to mcu as possible for correct operation.
VDD/VSS



VDDA/VSSA (VREF+/VREF-)



- MH1 MountingHole
- MH2 MountingHole
- MH3 MountingHole
- MH4 MountingHole



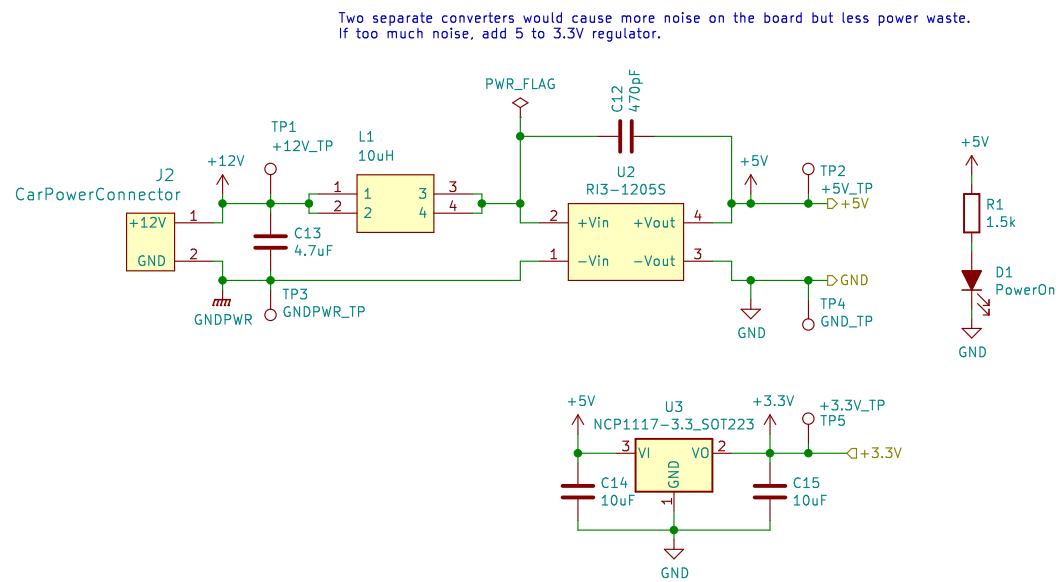
Sheet: /
File: BPSMaster.sch

Title:

Size: A4
KiCad E.D.A. kicad (5.1.4)-1

Date:

Rev:
Id: 1/11



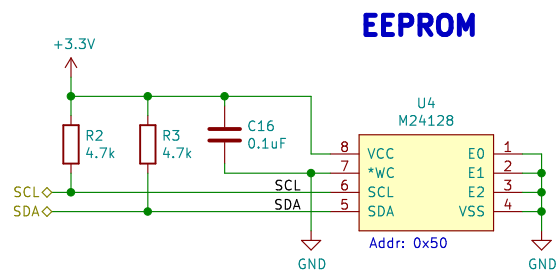
Sheet: /Power Distribution/
File: PowerDist.sch

Title:

Size: A4
KiCad E.D.A. kicad (5.1.4)-1

Date:

Rev:
Id: 2/11



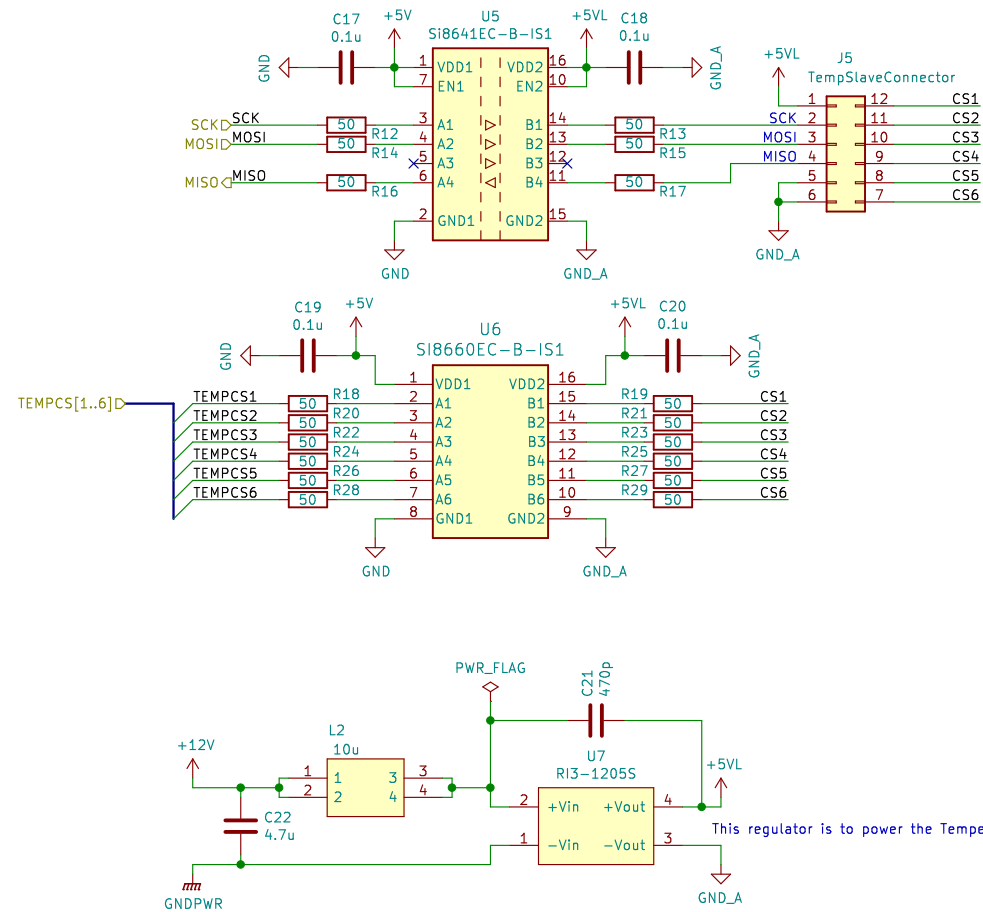
Sheet: /EEPROM/
File: EEPROM.sch

Title:

Size: A4
KiCad E.D.A. kicad (5.1.4)-1

Date:

Rev:
Id: 3/11



Sheet: /Temperature Board Interface/
File: TempBrdInterface.sch

Title:

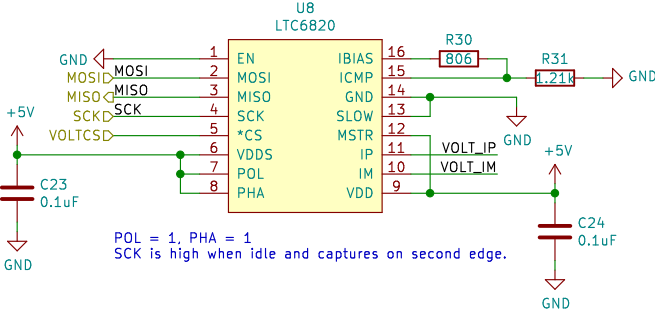
Size: A4
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Date:

Rev:
Id: 5/11

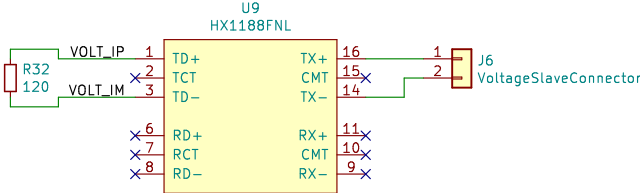
Isolated SPI

If the wires lengths are short and you want to save more power, change IBIAS resistor to be 2.8k. Look in LTC6820 datasheet for more information on calculating these BIAS resistors.



Connectors

Each connector is isolated from the master board components.



Sheet: /Voltage Board Interface/
File: VoltBrdInterface.sch

Title:

Size: A4

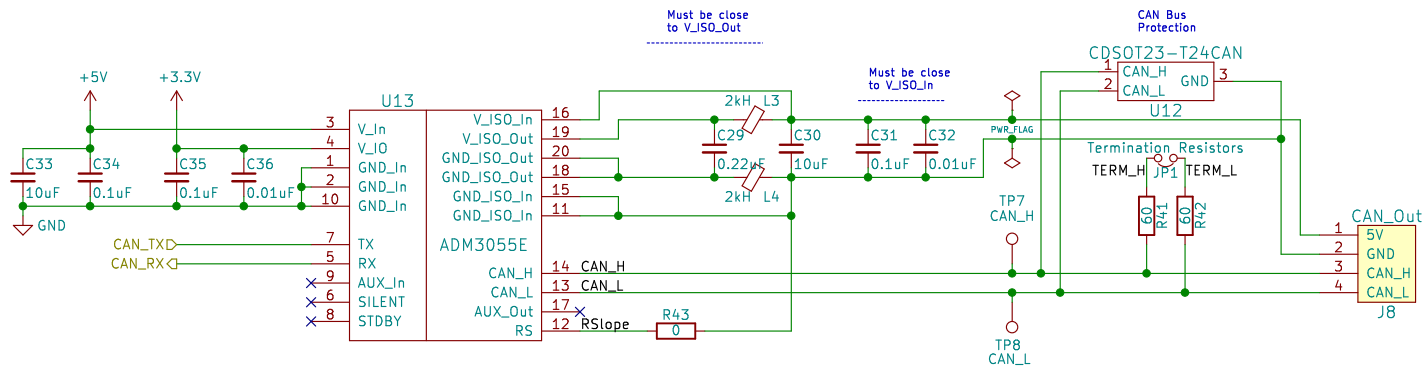
Date:

KiCad E.D.A. kicad (5.1.4)-1

Rev:

Id: 6/11





Sheet: /CAN/
File: CAN.sch

Title:

Size: A4
KiCad E.D.A. kicad (5.1.4)-1

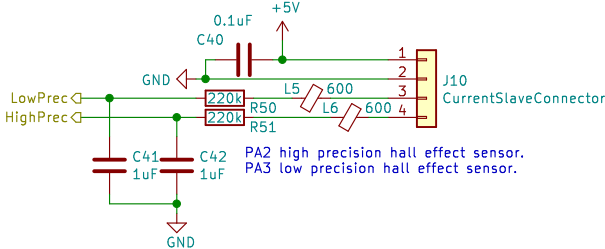
Date:

Rev:
Id: 8/11



Rev:
Id: 9/11

Current Board



Sheet: /Current Board Interface/
File: CurrentBrdInterface.sch

Title:

Size: A4	Date:
KiCad E.D.A. kicad (5.1.4)-1	

Rev: 10/11

Id: 11/11