

# ESC201T : Introduction to Electronics

MQ4 (06/11/2020)

Q. Plot the variation of output voltage  $V_o$  vs input voltage  $V_{IN}$  as it is increased from 0 to 8V. Assume Diodes are ideal in the sense that in forward bias, voltage drop across them is zero and in reverse bias, current through them is zero. Label (quantitatively) all threshold or corner points on x and y-axis. As usual, show steps of analysis, otherwise no marks will be awarded.

