

# Instruction Format

# Instruction Format

- To construct the binary codes for 8086 instructions, probably assembler is used.
- To code instructions for 8-bit processors such as 8085, is look up the hexa decimal code for each instruction on a one-page chart

# Instruction Format

## Indicators

W-bit  
D-bit  
V-bit  
S-bit  
Z-bit

Byte-1

Byte-2

OPCODE

D

W

MOD

REG

R/M

Byte-3

Byte-4

LOW DISPLACEMENT

HIGH DISPLACEMENT

# Instruction Format

## W-bit

1-word operation

0-Byte operation

## D-bit

0-then REG is Source

1-then REG is Destination

## V-bit

0-then no of rotations/shift=1

1- then no of rotations/shift=CL

## S-bit

0-No Sign extension

1- Sign extension

## Z-bit

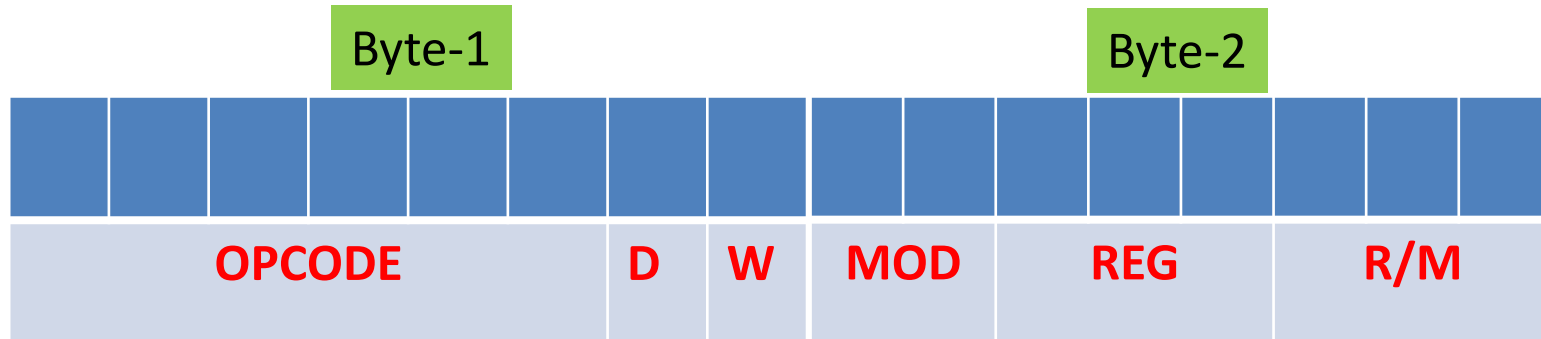
0-Repeat/loop while ZF=0

1- Repeat/loop while ZF=1

<div><div>MOD</div><div>REG/R/M</div></div>	11				
	00	01	10	W=0	W=1
000	[BX][SI] DS	[BX][SI]+d <sub>8</sub> DS	[BX][SI]+d <sub>16</sub> DS	AL	AX
001	[BX][DI] DS	[BX][DI]+d <sub>8</sub> DS	[BX][DI]+d <sub>16</sub> DS	CL	CX
010	[BP]+[SI] SS	[BP]+[SI] +d <sub>8</sub> SS	[BP]+[SI]+d <sub>16</sub> SS	DL	DX
011	[BP]+[DI] SS	[BP]+[DI] +d <sub>8</sub> SS	[BP]+[DI]+d <sub>16</sub> SS	BL	BX
100	[SI] DS	[SI]+d <sub>8</sub> DS	[SI]+d <sub>16</sub> DS	AH	SP
101	[DI] ES	[DI]+d <sub>8</sub> ES	[DI]+d <sub>16</sub> ES	CH	BP
110	d <sub>16</sub> DS	[BP]+d <sub>8</sub> SS	[BP]+d <sub>16</sub> SS	DH	SI
111	[BX] DS	[BX]+d <sub>8</sub> DS	[BX]+d <sub>16</sub> DS	BH	DI

# Instruction Format

Design Instruction template for MOV AX, [BX]



# Instruction Format

Design Instruction template for MOV AX, [BX]

