E.V.S.Lalita

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OBJECTIVE

Seeking a position to utilize my skills and abilities in an organization that offers professional growth while being resourceful, innovative and flexible.

EDUCATION

BIT MESRA, India
Master of Engineering in POWER SYSTEMS 2002-2004
CGPA: 9.10/10

B.I.T.DURG, PT.Ravishankar Shukla University,India
Bachelor of Engineering in Electrical Engg 1997-2001
Percentage:82.55%

HSSCE (12^{TH}), M.P.BOARD, BHOPAL 1997
Percentage: 76%

HSCE (10^{TH}), M.P.BOARD, BHOPAL, 1995
Percentage:75.8%

Other Courses: Attended training on Novel Technologies for Certified Novel Engineer Netware 5.1

Skill Set: Matlab 6.1, C, Testing Tools, Manual Testing and Automated TestingTools(Load Runner,WinRunner)

PROJECT DETAILS :

Final Semester Project as Part of the M.E. Curriculum.

Project Name **:**"Reliability evaluation of committed units in conventional and fuzzy approach"

Software **:** Matlab 6.1

Description **:** In this project , a program in Matlab is developed for a probabilistic approach for hierarchical level-I (HL-I) reliability of unit commitment problem (UC). With the proposed methods, uncertainties embedded

in generation side are taken into account and then the reliability indices such as loss-of-load- probability (LOLP) and fuzzy loss-of-load-probability (FLOLP) have been evaluated. A short-term commitment period with a 24-hour time horizon is considered.

Final Semester Project as Part of the B.E Curriculum.

Project Name **:** "Voltage regulator for contactor ridethrough"

Description **:**In this project a voltage regulator is designed that allows the contactors to ride through the power line disturbances such as sags and dropouts. The voltage regulator protects critical equipments in continuous process industries and prevent loss caused by downtime. The voltage regulator is used for sag sensitive components such as contactors, some of which dropout at 70% of normal voltage or even higher.

AWARDS RECEIVED

Received EXCEL GOLD MEDAL from B.I.T.,DURG for securing highest percentage in ELECTRICAL ENGG.BRANCH for session 97-2001.

TECHNICAL PAPER

SOLUTION OF UNIT COMMITMENT USING MODIFIED GENETIC ALGORITHM CONSIDERING THE EFFECT OF UNIT OUTAGE UNCERTAINTY-*Journal* ***(203) International Journal of Power and Energy Systems - 2008***

PROFESSIONAL EXPERIENCE

SNSIT (Hyderabad, India) Aug-2009 – ToDate

- Lecturer in Electrical and Electronics Engineering Department.
- Taught subjects : Electric Circuits, Power Systems Analysis, Electrical Machines, Basic Electrical engineering
- Taken Laboratory course in Electrical Machines, Electrical Circuits, Networks
- Guided students in presenting Technical Reports

ICFAITECH (Hyderabad, India) May-2005 – August-2006

- Lecturer in Electrical and Electronics Engineering Department.
- Taught subjects : Power Systems Analysis, Switchgear And Protection, Electrical Machines, Basic Electrical engineering
- Taken Laboratory course in Power System Analysis based on power world Simulator
- Guided students in presenting Technical Reports
- Handled Projects both short and long term in Matlab6.1.

Rungta College of Engg. And Technology (Bhilai, India) March-2002 –July-2002

- Lecturer in Electrical Engineering Department.
- Taught subjects : Electrical Machines, Basic Electrical engineering, Network Synthesis
- Handled labs in Electrical Machines
- Short term projects in C.