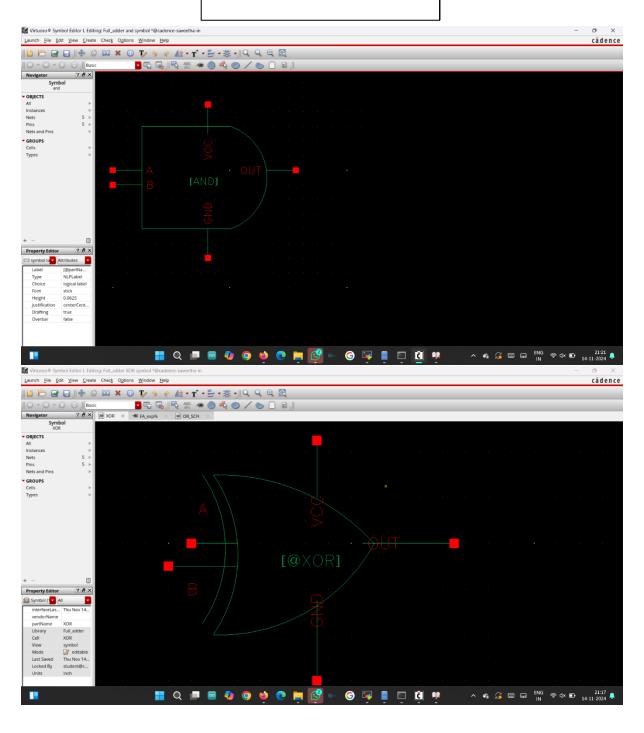
## **FULL ADDER**

## AND GATE & XOR GATE



## **FULL ADDER**

## SCHEMATIC AND OUTPUT

