Nuwan Bandara	Best support for BIT
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ONLINE LESSION	
-CS-UNIT-2-	
Fractional Numbers Representation	•

1. Fractional Converting.

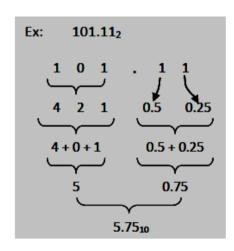
a. Decimal fractions to Binary Conversions.

21.25, relevant binary number?

b. Binary fraction.

2	1	0	-1	-2	-3	-4
2 ²	2 ¹	2 ⁰	2-1	2 ⁻²	2 ⁻³	2 ⁻⁴
4	2	1	1/21	1/2 ²	1/2 ³	1/24
			1/2	1/4	1/8	1/16
			0.5 1	0.25 ↓ 2	0.125 ↓ 3	0.0625 ↓ 4

 $\begin{array}{ccccccc} 0.1_2 & = & 0.5_{10} \\ 0.01_2 & = & 0.25_{10} \\ 0.11_2 & = & 0.75_{10} \\ 0.001_2 & = & 0.125_{10} \\ 0.101_2 & = & 0.625_{10} \\ 0.111_2 & = & 0.875_{10} \end{array}$



IMPORTANT.



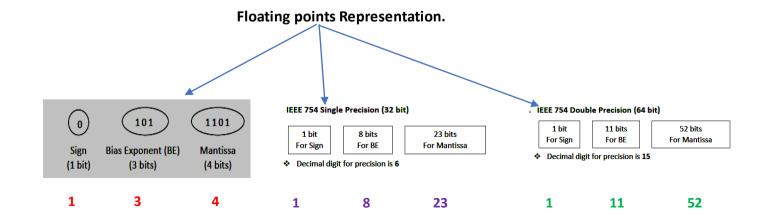
Method	7	, 8
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2

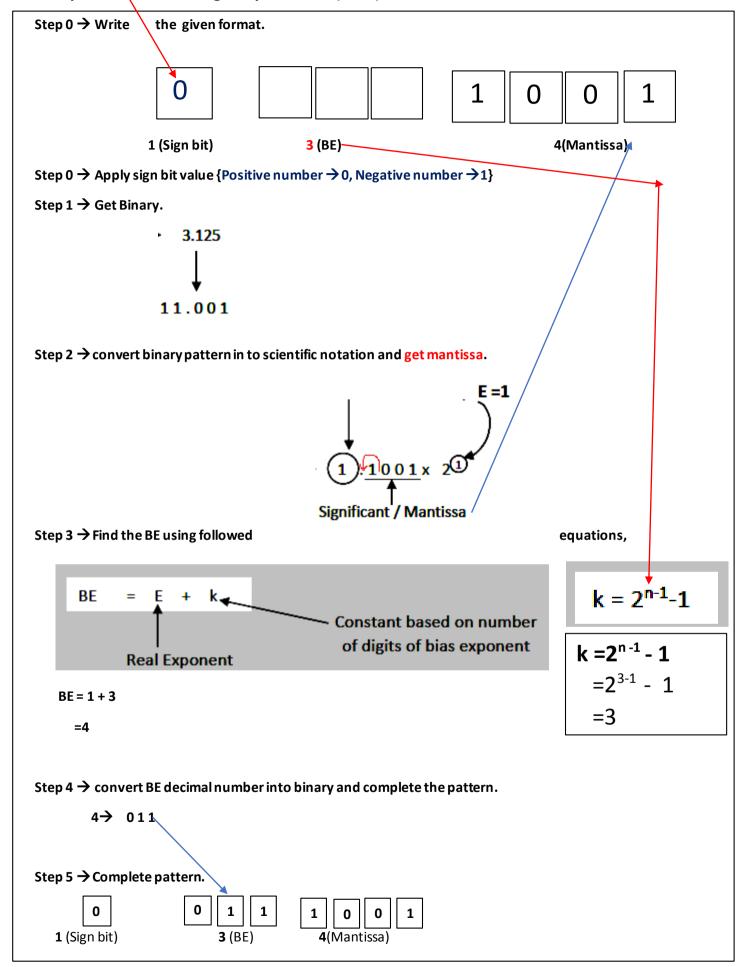
2018-07

7) Convert the decimal number 0.171875 to binary

(a) 0.010111	(b) 0.001101	(c) 0.001011	
(d) 0.001110	(e) 0.000111		



Samples → 3.125 using Simple model (8 bit).



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FORWORD QUESTIONS.

2014-11

11) The IEEE standard 32-bit floating point representation of the binary number 32.5 is

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2015-11

11) The IEEE standard 32-bit floating point representation of the binary number +42.625 is

BACKWORD QUESTIONS.

2012-8

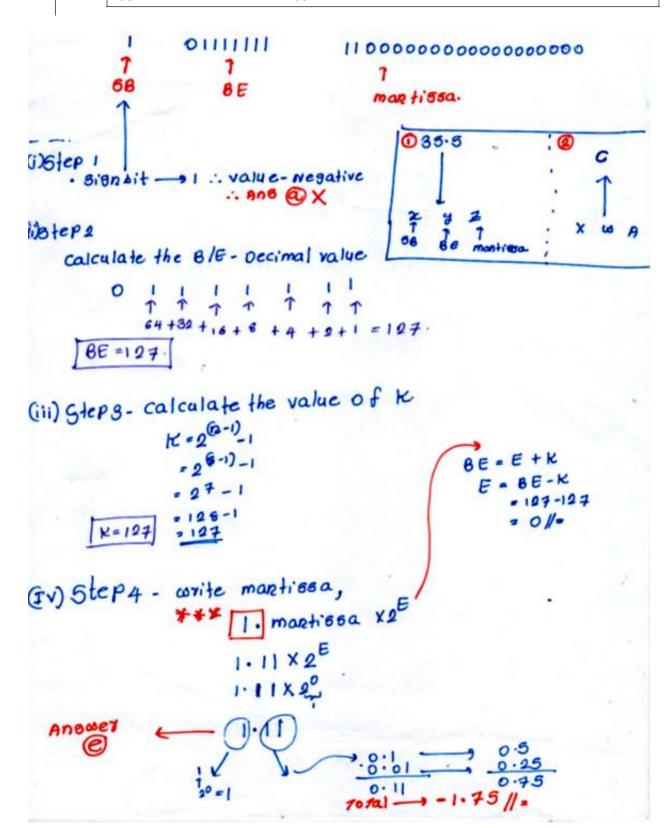
(a) +1.1

(b) -1.0

(c) +1.11

(d) -0.1

(e) -1.11



2017-10

Which of the following is the correct decimal number of the 16-bit floating point representation 010101 0101011010 with a sign bit, 5-bit exponent and 10-bit mantissa?

(a) +87.625 (b) +85.625

(c) +89.625

(d) +43.625

(e) +47.625

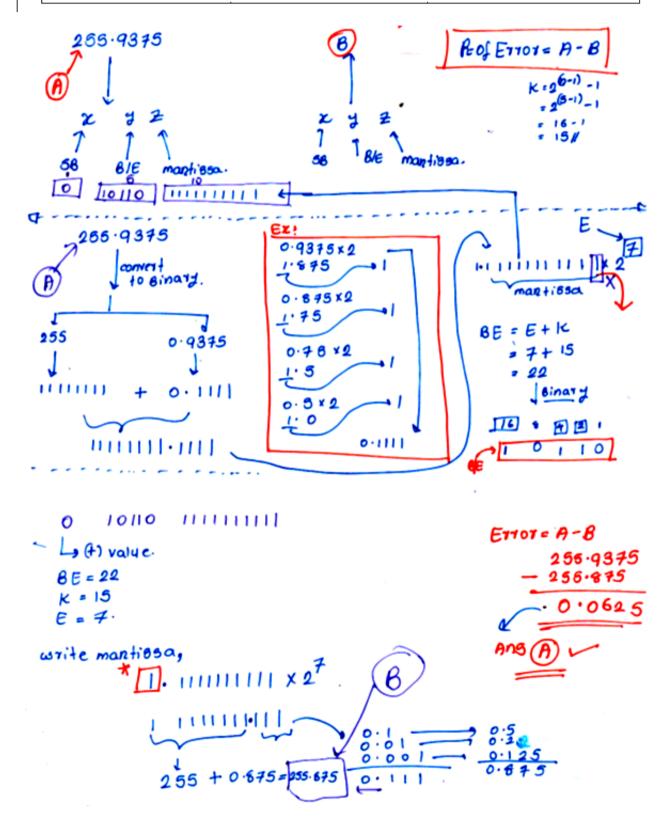
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Floating points - Round-Off-Error

2014-09

9) What is the loss of accuracy (round-off-error) when converting the decimal value +255.9375 to 16-bit floating point representation with a sign bit, 5-bit exponent and a 10-bit mantissa?

(a) 0.0625 (b) 0.125 (c) 0.1875 (d) 0.25 (e) 0.5

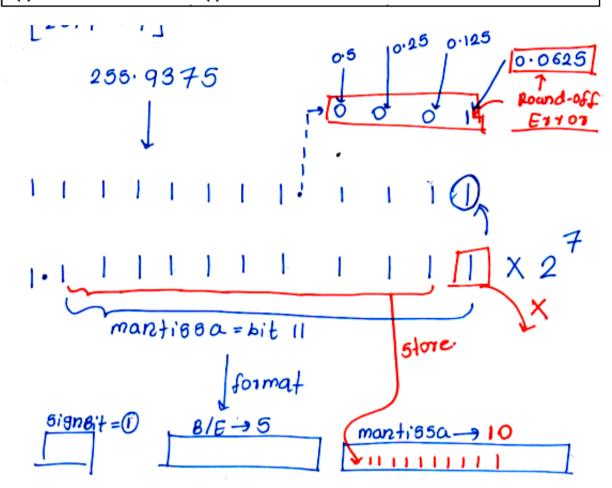


Round-Off-Error-Short Method

2014-09

9) What is the loss of accuracy (round-off-error) when converting the decimal value +255.9375 to 16-bit floating point representation with a sign bit, 5-bit exponent and a 10-bit mantissa?

(a) 0.0625 (b) 0.125 (c) 0.1875 (d) 0.25 (e) 0.5



2015-09

9) What is the loss of accuracy (round-off-error) when converting the decimal value +511.875 to 16-bit floating point representation with a sign bit, 5-bit exponent and a 10-bit mantissa?

(a) 0.0625	(b) 0.125	(c) 0.1875	
(d) 0.25	(e) 0.5		

2016-10

What is the loss of accuracy (round-off-error) when converting the decimal value +1000.875 to 16-bit floating point representation with a sign bit, 5-bit exponent and a 10-bit mantissa?

(a) 0.125	(b) 0.25	(c) 0.375
(d) 0.625	(e) 0.75	