

DivergenceMeter CTL

A work from lamadaemon | Opensource under license Anti-996

For more details about Anti-996 and 996.icu please visit: <https://996.icu>

Change Log

2023/07/14 Rev1.0: Initial version

2023/08/05 Rev1.1: Support controlling power board directly

2023/09/09 Rev1.2: Fix minor bugs

Default Unit

Resistor Ohms

Capacitor Faras

Inductor Henry

TOC

P1: TOC

P2: Power and connector

P3: ESP32-S3 MCU and connections

P4: IO 1 and 2

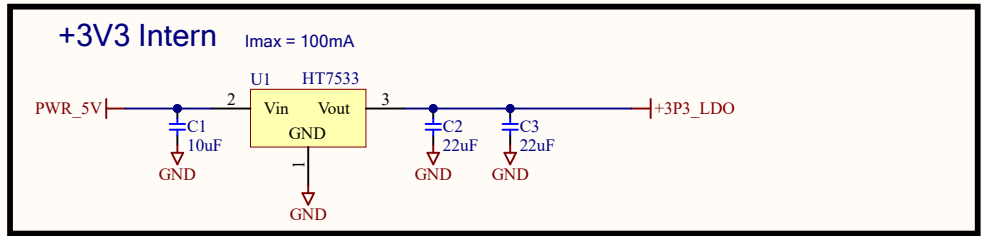
P5: IO 3 and 4

P6: Fan

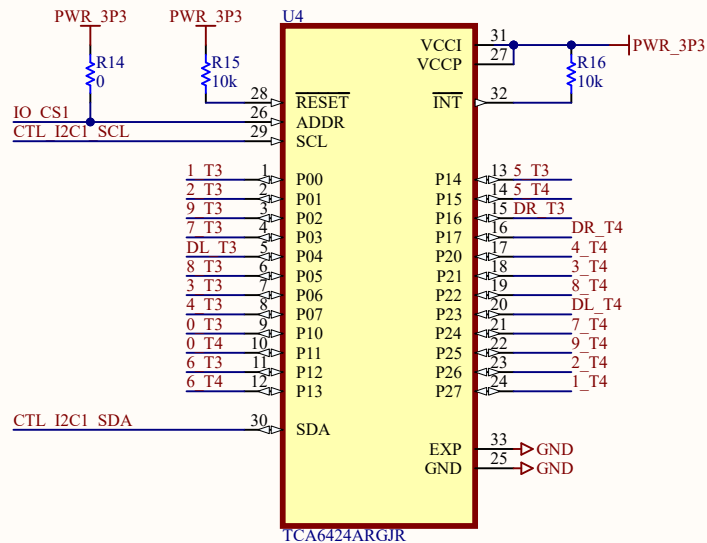
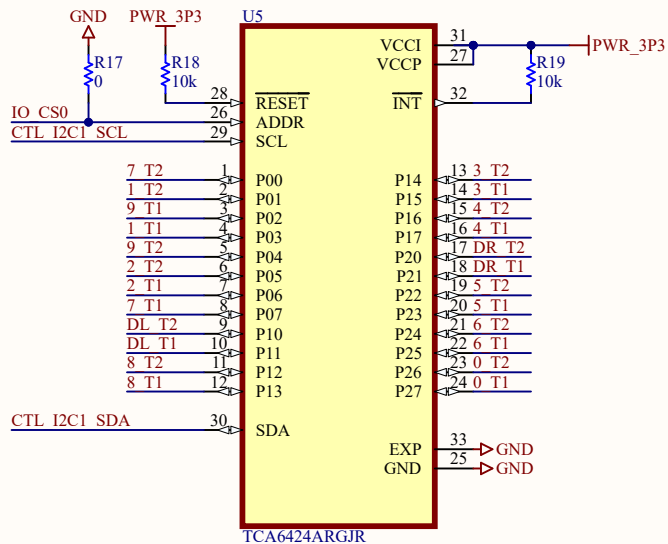
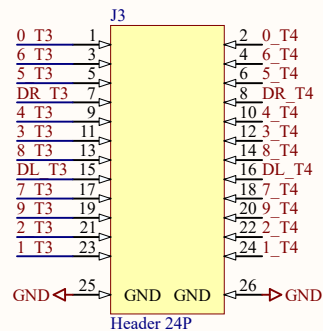
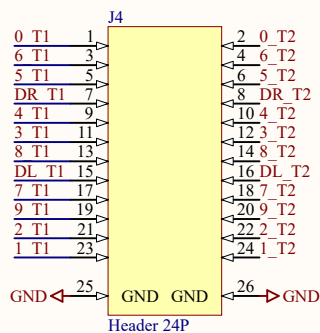
Design: 5th Gen

Rev: 1.2

Title		
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File:	E:\Projects\...\TOC.SchDoc	Drawn By:

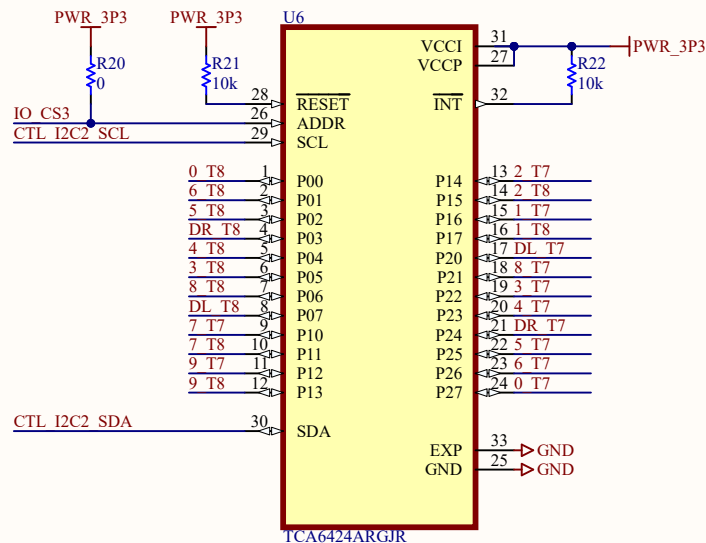
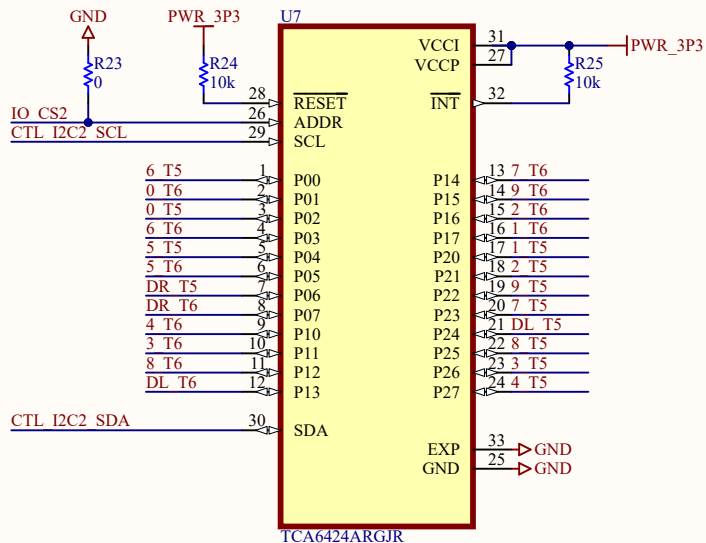
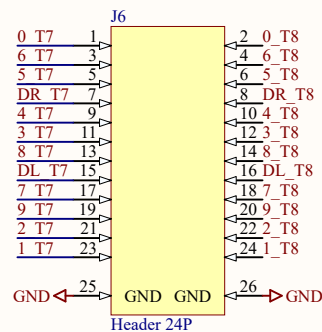
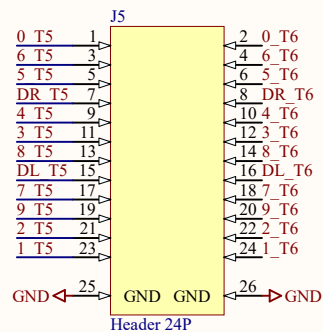


No pull down resistors is needed. Because all of these control pins will be set to LOW before HV is powered to nixie tubes



Title		
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No pull down resistors is needed. Because all of these control pins will be set to LOW before HV is powered to nixie tubes



Title		
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A4		
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