* 1. Instruction Set Summary
     1. Format Summary

The AMO instruction set formats are shown below.

텍스트, 스크린샷, 번호, 디자인이(가) 표시된 사진

자동 생성된 설명

* + 1. Instruction Summary

**DEFAULT**

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| Mnemonic | Example | Instruction |
| MOV | mov Rd, Rs/imm21  mov Rd, imm32/symbol | Rd ← Rs/imm21  literal pool (pseudo) |
| LDR | ldr Rd, [base, offset]  ldr Rd, [relative] | Rd ← mem[base + offset]  Rd ← mem[pc + relative] |
| STR | str [base, offset], Rs  str [relative], Rs | mem[base + offset] ← Rs  mem[pc + relative] ← Rs |
| LDRB | ldrb Rd, [base, offset]  ldrb Rd, [relative] | Rd ← mem[base + offset]  Rd ← mem[pc + relative] |
| STRB | strb [base, offset], Rs  strb [relative], Rs | mem[base + offset] ← Rs  mem[pc + relative] ← Rs |
| LDRH | ldrh Rd, [base, offset]  ldrh Rd, [relative] | Rd ← mem[base + offset]  Rd ← mem[pc + relative] |
| STRH | strh [base, offset], Rs  strh [relative], Rs | mem[base + offset] ← Rs  mem[pc + relative] ← Rs |
| ADD | add Rd, Rn, Rs/imm16 | Rd ← Rn + Rs/imm16 |
| ADC | adc Rd, Rn, Rs/imm16 | Rd ← Rn + Rs/imm16 + Carry |
| SUB | sub Rd, Rn, Rs/imm16 | Rd ← Rn - Rs/imm16 |
| AND | and Rd, Rn, Rs/imm16 | Rd ← Rn AND Rs/imm16 |
| OR | or Rd, Rn, Rs/imm16 | Rd ← Rn OR Rs/imm16 |
| XOR | xor Rd, Rn, Rs/imm16 | Rd ← Rn XOR Rs/imm16 |
| NOT | not Rd, Rs/imm16 | Rd ← NOT Rs/imm16 |
| LSL | lsl Rd, Rn, Rs/imm16 | Rd ← Rn << Rs/imm16 |
| LSR | lsr Rd, Rn, Rs/imm16 | Rd ← Rn >> Rs/imm16 |
| ASR | asr Rd, Rn, Rs/imm16 | Rd ← Rn >>> Rs/imm16 |
| BEQ | beq Rd, Rs, imm16 | PC ← PC + (imm16 << 2) if Rd == Rs |
| BNE | bne Rd, Rs, imm16 | PC ← PC + (imm16 << 2) if Rd != Rs |
| BLT | blt Rd, Rs, imm16 | PC ← PC + (imm16 << 2) if Rd < Rs |
| BLE | ble Rd, Rs, imm16 | PC ← PC + (imm16 << 2) if Rd <= Rs |
| BLTU | bltu Rd, Rs, imm16 | PC ← PC + (imm16 << 2) if Rs < Rn |
| BLEU | bleu Rd, Rs, imm16 | PC ← PC + (imm16 << 2) if Rs <= Rn |
| JMP | jmp Rs/imm26 | PC = Rs/(imm26 << 2) |
| JAL | jal Rs/imm26 | PC = (imm26 << 2)  LR = PC |
| SWI | swi imm6 | Jump to Interrupt Vector (Trap) |
| EXT | ext Rd, Rs, Opt | Rd ← [Sign/Unsign]Extend (Rs) |
| SETVT | setvt Rs, Type | Set the Vector Table  Type 0: Interrupt Vector Table |
| RET | ret Rs | PC = Rs/(imm26 << 2)  InterruptBlocking = false |
| LOCK | lock | InterruptBlocking = !InterruptBlocking  (for atomic operation) |