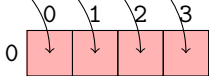
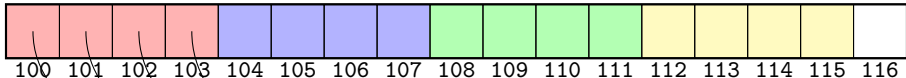


Memory address



Vector register