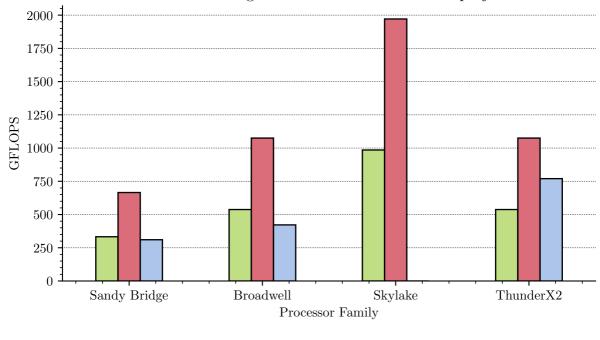Peak theoretical performance and HPL measured performance of
the node configurations used in this research project