類比積體電路設計概論 HW7, due date: 12 Dec.

HW7.1 (20 points)

Calculate the close-loop input and output resistance ($R_{in,CL}$ and $R_{out,CL}$) using small-signal parameters and assuming that $\gamma = 0$.

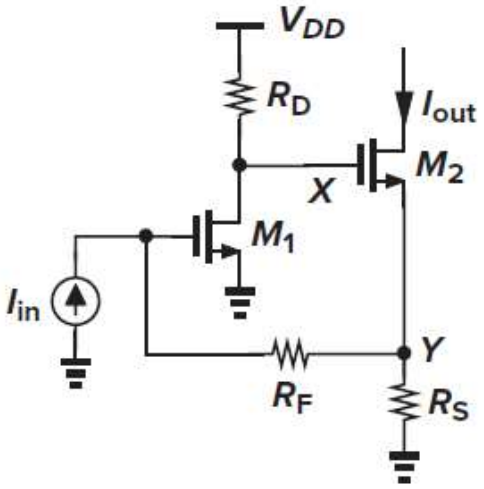
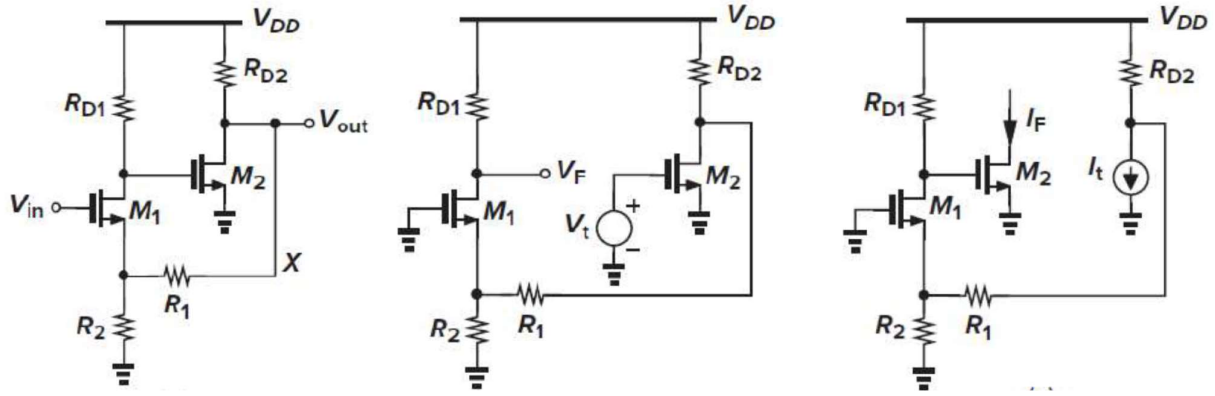


Fig. 7.1

HW7.2 (20 points)

As shown in Fig. 7.2(a), please prove that the loop gain, I_F/I_t (shown in Fig. 7.2(c)), is the same as that using V_F/V_t (shown in Fig. 7.2(b)).



(a) (b) (c)

Fig. 7.2

類比積體電路設計概論 HW7, due date: 12 Dec.

HW7.3 (40 points)

Using feedback techniques, calculate the input and output impedance and voltage gain of each circuit in Fig. 7.3. Using small-signal parameters to represent your solutions.

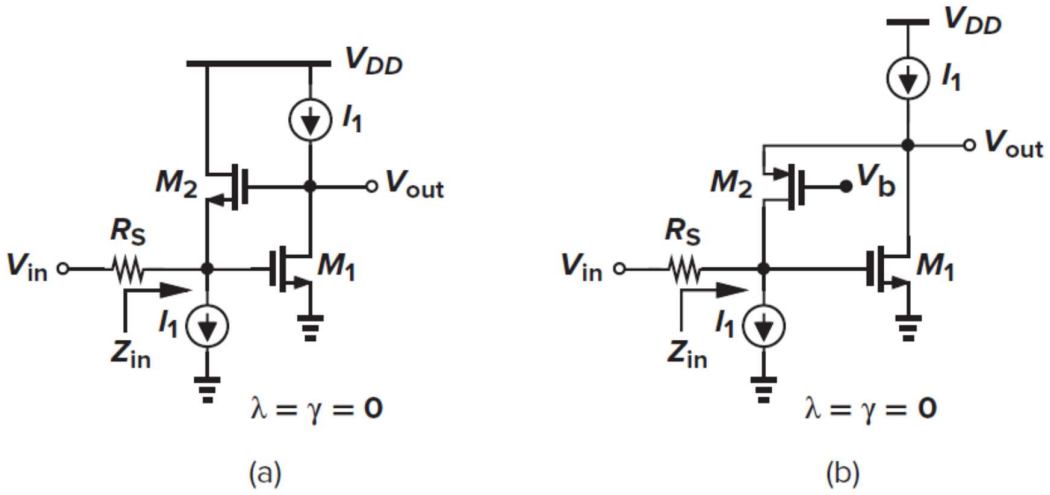


Fig. 7.3

HW7.4 (20 points)

In the circuit of Fig. 7.4, assuming that $\lambda = \gamma = 0$, calculate the closed-loop gain and output impedance. Using small-signal parameters to represent your solutions.

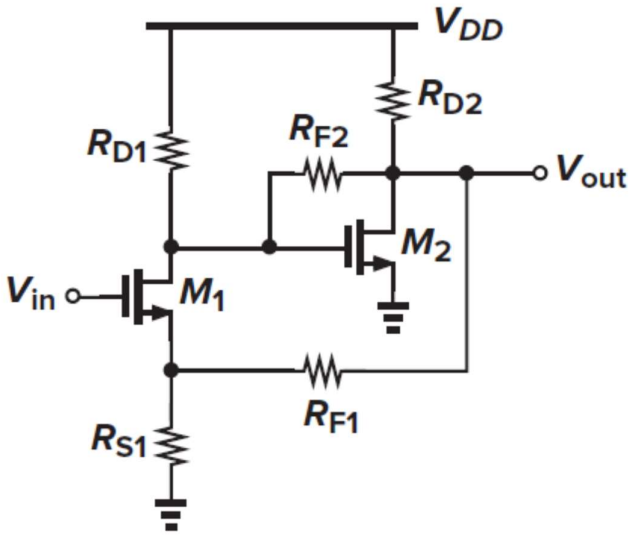


Fig. 7.4