Job Posting:165546 - Position: S25 Firmware Engineer (Co-op) 165546B E1

Co-op Work Term Posted: 2025 - Summer

App Deadline 02/05/2025 09:00 AM

Application Method: Through Employer Website

Posting Goes Live: 01/22/2025 11:06 AM

Job Posting Status: Expired

ORGANIZATION INFORMATION

Organization Astera Labs

Address Line 1 4370 Dominion St

Address Line 2

City

Postal Code / Zip Code

Province / State

Country

Suite 240

Burnaby

V5G 4L7

BC

Canada

JOB POSTING INFORMATION

Placement Term 2025 - Summer

 S25 Firmware Engineer (Co-op) 165546B E1

Position Type Co-op Position
Job Location Vancouver, BC

CountryCanadaDuration4 monthsSalary CurrencyCAD

Salary 40.0 per hour for 0 Major List

Job Description

Astera Labs is a global leader in purpose-built connectivity solutions that unlock the full potential of AI and cloud infrastructure. Our Intelligent Connectivity Platform integrates PCIe®, CXL®, and Ethernet semiconductor-based solutions and the COSMOS software suite of system management and optimization tools to deliver a software-defined architecture that is both scalable and customizable. Inspired by trusted relationships with hyperscalers and the data center ecosystem, we are an innovation leader delivering products that are flexible and interoperable. Discover how we are transforming modern data-driven applications at

www.asteralabs.com.

Job Requirements

Basic qualifications:

- Strong academic and technical background taking Computer Science, Electrical/Electronics/Software Engineering or equivalent.
- •Completed one or more Co-op terms of firmware or embedded software development.
- •Completed one or more courses in Real Time Operating Systems (RTOS) or detailed use of an RTOS during a prior coop term or project.
- •Professional attitude with the ability to formulate theories and well thought out questions as part of helping debug/develop complex systems.

Required experience:

- Proficiency in C (preferred) or C++, including development of firmware.
- •Proficient in Python for scripting.
- •Experience with developer workflows, SCM (preferably git), code reviews, CI.
- •Experience developing firmware to execute in on-chip microcontrollers.
- •Experience debugging firmware/hardware issues using JTAG, trace logs, or other lab tools.

Preferred experience:

- Knowledge of tools like PyTest or GoogleTest is an asset.
- •Knowledge of software build environments, CI/CD, gcc/make is an asset
- •Experience with lab hardware/firmware debug is an asset.
- •Experience developing firmware for PCle/CXL/Ethernet is an asset.

We know that creativity and innovation happen more often when teams include diverse ideas, backgrounds, and experiences, and we actively encourage everyone with relevant experience to apply, including people of color, LGBTQ+ and non-binary people, veterans, parents, and individuals with disabilities.

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website

Cover Letter Required? Optional

Special Application Instructions

Application Link: https://job-boards.greenhouse.io/asteraearlycareer/jobs/4489085005

Please click the "I intend to apply to this position" button on SCOPE and also submit your application via the employer's website.

Applications are accepted on a rolling basis and the posting may be expired at any time by the employer as submissions are received.

Students should submit their applications as soon as they are ready.