

Job Posting:164059 - Position: S25 Test Engineer Intern - Bachelors 164059B E1

Co-op Work Term Posted:	2025 - Summer
App Deadline	02/19/2025 09:00 AM
Application Method:	Through Employer Website
Posting Goes Live:	02/04/2025 12:49 PM
Job Posting Status:	Expired

ORGANIZATION INFORMATION

Organization	Marvell Technology Group Ltd.
Address Line 1	Canon's Court 22 Victoria Street
Country	Canada

JOB POSTING INFORMATION

Placement Term	2025 - Summer
 Job Title 	S25 Test Engineer Intern - Bachelors 164059B E1
Position Type	Co-op Position
Job Location	Irvine, CA
Country	USA
Duration	4 months
Work Mode	Hybrid
Salary Currency	US
Salary	0.0 per hour for 0 Major List
Salary Range \$	\$24- \$47

Job Description

About Marvell

Marvell's semiconductor solutions are the essential building blocks of the data infrastructure that connects our world. Across enterprise, cloud and AI, automotive, and carrier architectures, our innovative technology is enabling new possibilities.

At Marvell, you can affect the arc of individual lives, lift the trajectory of entire industries, and fuel the transformative potential of tomorrow. For those looking to make their mark on purposeful and enduring innovation, above and beyond fleeting trends, Marvell is a place to thrive, learn, and lead.

Your Team, Your Impact

This team owns all test engineering activities related to the NPI plan including new silicon bring-up, release of sampling test programs, release of qualification and characterization test programs, release of production test programs, and optimization of test time and yield.

What You Can Expect

- Designing, debugging and correlating ATE HW and SW
- Working with senior engineers on making SW and HW improvements
- Optimizing software utilized in production on ATE
- Working on cutting edge high speed optical PHY products

What We're Looking For

Minimum Requirements:

- Candidate **MUST** be currently pursuing a BS degree in CS/EE or related technical field(s)
- 0-1 years of previous experience

Preferred Requirements:

- Experience with Digital/Analog circuit fundamentals gathered from Digital communication, Analog circuit design, Algorithm and Programming coursework, research and projects.
- Software development experience and coding in a general-purpose programming language
- Proficient in C, C++

Expected Base Pay Range (USD)

24 - 47, \$ per hour.

The successful candidate's starting base pay will be determined based on job-related skills, experience, qualifications, work location and market conditions. The expected base pay range for this role may be modified based on market conditions.

Additional Compensation and Benefit Elements

For Internship roles, we are proud to offer the following benefits package during the internship - medical, dental and vision coverage, perks and discount programs, wellness & mental health support including coaching and therapy, paid holidays, paid volunteer days and paid sick time. Additional compensation may be available for intern PhD candidates.

This role is eligible for our hybrid work model in which you will be able to split time between working from home and on-site in a Marvell office.

All qualified applicants will receive consideration for employment without regard to race, color, religion, sex, national origin, sexual orientation, gender identity, disability or protected veteran status.

Any applicant who requires a reasonable accommodation during the selection process should contact Marvell HR Helpdesk at TAOps@marvell.com.

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website

Cover Letter Required? Optional

Special Application Instructions

Application Link: https://marvell.wd1.myworkdayjobs.com/en-US/MarvellCareers2/job/Irvine-CA/Test-Engineer-Intern---Bachelors_2401943

Indicate your interest to apply to this position in **SCOPE** and also submit your application package via the online website portal.

Applications are accepted on a rolling basis and the posting may expire at any time. Students should submit their applications as soon as they are ready.