

Job Posting:164105 - Position: S25 Embedded Software Engineer Intern 164105B E1

Co-op Work Term Posted: 2025 - Summer
App Deadline 03/21/2025 09:00 AM
Application Method: Through Employer Website
Posting Goes Live: 02/28/2025 03:23 PM
Job Posting Status: Approved

ORGANIZATION INFORMATION

Organization Neuralink
City Fremont
Province / State CA

JOB POSTING INFORMATION

Placement Term 2025 - Summer
** Job Title ** S25 Embedded Software Engineer Intern 164105B E1
Position Type Co-op Position
Job Location Fremont, CA
Country USA
Duration 4 months
Work Mode In-Person
Salary Currency US
Salary 35.0 per hour for 0 Major List
Job Description

Team Description:

The Brain Interfaces Embedded Systems Team, a division of the Brain Interfaces Hardware Department, owns the development of high-performance, safety-critical embedded systems for the next-generation of brain-computer interfaces. As a key member of our team, you will gain hands-on training, work alongside accomplished mentors, and contribute to real-world projects.

Job Description and Responsibilities:

We are looking for experienced, hands-on engineers who are interested in exploring what's possible in highly powered and latency-constrained environments. Firmware is the kernel of operation for the implant and you have the chance to determine and implement features that have a massive impact on the product's capabilities. You will have the opportunity to collaborate closely with chip designers, electrical engineers, algorithms engineers, and software engineers on a small, agile team. The job responsibilities will include:

- Building on your ability to work cross functionally, working with engineers who deliver safety-critical systems while considering power, radio, and thermal requirements
- Gaining an understanding of delivering products and/or core features from planning and design to execution and testing
- Growing your fundamental understanding of hardware while contributing to bare-metal firmware in C
- Contributing to codebase organization, prioritizing modular formats that allow for elegant hardware bifurcation in the field
- Developing the ability to contribute to system improvements that decrease latency and increase battery life, reliability, and safety

Pay Transparency:

Based on California law, the following details are for California individuals only.

California Hourly Rate:

\$35/hr USD

For Full-Time Employees, your compensation package will include two major components: salary and equity. Guidance on salary for this role will be determined according to the level at which you enter the organization, with the ability to gain more over time as you contribute. In addition, Full-Time Employees are eligible for the following benefits listed below.

What We Offer:

- An opportunity to change the world and work with some of the smartest and most talented experts from different fields
- Growth potential; we rapidly advance team members who have an outsized impact
- Excellent medical, dental, and vision insurance through a PPO plan
- Paid holidays
- Commuter benefits
- Meals provided
- Equity + 401(k) plan *Temporary Employees & Interns excluded
- Parental leave *Temporary Employees & Interns excluded
- Flexible time off *Temporary Employees & Interns excluded

Multiple studies have found that a higher percentage of women and BIPOC candidates won't apply if they don't meet every listed qualification. Neuralink values candidates of all backgrounds. If you find yourself excited by our mission but you don't check every box in the description, we encourage you to apply anyway!

Neuralink provides equal opportunity in all of our employment practices to all qualified employees and applicants without regard to race, color, religion, gender, national origin, age, disability, marital status, military status, genetic information or any other category protected by federal, state and local laws. This policy applies to all aspects of the employment relationship, including recruitment, hiring, compensation, promotion, transfer, disciplinary action, layoff, return from layoff, training and social, and recreational programs. All such employment decisions will be made without unlawfully discriminating on any prohibited basis. If you need a reasonable accommodation at any point in the interview process, please let us know. Reasonable accommodations are modifications or adjustments to the application or hiring process that would enable you to fully participate in those processes. Examples of reasonable accommodations include but are not limited to:

- Documents in alternate formats or read aloud to you
- Having interviews in an accessible location
- Being accompanied by a service dog
- Having a sign language interpreter present for the interview

Job Requirements

Required Qualifications:

- 2+ years of experience with embedded systems
- Proficient in C/C++, and Python
- Knowledge of MCU architectures, peripherals, and development environments
- Comfortable reading electrical schematics and datasheets
- Deep knowledge of the embedded toolchains and workflow
- Strong EE & DSP fundamentals
- HW/SW debugging skills with lab equipment

Preferred Qualifications:

- Experience with embedded stacks for ARM cores
- Experience with safety-critical systems
- Experience with FPGA development and testing
- Experience with RF, BLE, and TCP/IP stacks from a practical embedded perspective
- Prior work or experience with applied cryptography and embedded systems security

Citizenship Requirement N/A

APPLICATION INFORMATION

Application Procedure Through Employer Website

Cover Letter Required? Optional

Special Application Instructions

Please apply through this link: <https://grnh.se/5b2544f03us>