/tb/input_file -2147483641

/tb/output_file -2147483640

/tb/in

/tb/out

/tb/i

/tb/clk

/tb/reset_n

/tb/start

/tb/done

/tb/a_in 3435

/tb/b_in 2342340

/tb/result

/tb/gcd_0/a_in 3435

/tb/gcd_0/b_in 2342340

/tb/gcd_0/start

/tb/gcd_0/reset_n

/tb/gcd_0/clk

/tb/gcd_0/result

/tb/gcd_0/done

/tb/gcd_0/reg_a

/tb/gcd_0/reg_b 3435

/tb/gcd_0/mux_sig_a a_sub_b

/tb/gcd_0/mux_sig_b a_sub_b

/tb/gcd_0/a_lt_b

/tb/gcd_0/b_neq_zero

+15   11500 ns   12000 ns   12500 ns   13000 ns   13500 ns

Entity:tb Architecture: Date: Mon May 11 11:17:29 PM PDT 2015 Row: 1 Page: 1