/tb/input_file -2147483641
/tb/output_file -2147483640
/tb/in
/tb/out
/tb/i
/tb/clk
/tb/reset_n
/tb/start
/tb/done
/tb/a_in 3430
/tb/b_in 2060
/tb/result 3... 1... 2... 690 1... 680 690 10 680 670 660 650 640 630 620 610 600 590 580 570 560 550 540 530 ...
/tb/gcd_0/a_in 3430
/tb/gcd_0/b_in 2060
/tb/gcd_0/start
/tb/gcd_0/reset_n
/tb/gcd_0/clk
/tb/gcd_0/result 3... 1... 2... 690 1... 680 690 10 680 670 660 650 640 630 620 610 600 590 580 570 560 550 540 530 ...
/tb/gcd_0/done
/tb/gcd_0/reg_a 3... 1... 2... 690 1... 680 690 10 680 670 660 650 640 630 620 610 600 590 580 570 560 550 540 530 ...
/tb/gcd_0/reg_b 2060 1370 690 680 10
/tb/gcd_0/mux_sig_a hold ld... a... a... a... a... a... a_sub_b
/tb/gcd_0/mux_sig_b hold ld... a... a... a... a... a... a_sub_b
/tb/gcd_0/a_lt_b
/tb/gcd_0/b_neq_zero

+15   500 ns   1000 ns   1500 ns   2000 ns   2500 ns   3000 ns

Entity:tb Architecture: Date: Mon May 11 11:14:50 PM PDT 2015   Row: 1 Page: 1

/tb/input_file -2147483641
/tb/output_file -2147483640
/tb/in
/tb/out
/tb/i
/tb/clk
/tb/reset_n
/tb/start
/tb/done
/tb/a_in 3430
/tb/b_in 2060
/tb/result ...510 500 490 480 470 460 450 440 430 420 410 400 390 380 370 360 350 340 330 320 310 300 290 280 270 260 ...
/tb/gcd_0/a_in 3430
/tb/gcd_0/b_in 2060
/tb/gcd_0/start
/tb/gcd_0/reset_n
/tb/gcd_0/clk
/tb/gcd_0/result ...510 500 490 480 470 460 450 440 430 420 410 400 390 380 370 360 350 340 330 320 310 300 290 280 270 260 ...
/tb/gcd_0/done
/tb/gcd_0/reg_a ...510 500 490 480 470 460 450 440 430 420 410 400 390 380 370 360 350 340 330 320 310 300 290 280 270 260 ...
/tb/gcd_0/reg_b 10
/tb/gcd_0/mux_sig_a a_sub_b
/tb/gcd_0/mux_sig_b a_sub_b
/tb/gcd_0/a_lt_b
/tb/gcd_0/b_neq_zero

+15   3500 ns   4000 ns   4500 ns   5000 ns   5500 ns

Entity:tb Architecture: Date: Mon May 11 11:16:34 PM PDT 2015   Row: 1 Page: 1

/tb/input_file -2147483641
/tb/output_file -2147483640
/tb/in
/tb/out
/tb/i
/tb/clk
/tb/reset_n
/tb/start
/tb/done
/tb/a_in 3435
/tb/b_in 2342340
/tb/result
/tb/gcd_0/a_in 3435
/tb/gcd_0/b_in 2342340
/tb/gcd_0/start
/tb/gcd_0/reset_n
/tb/gcd_0/clk
/tb/gcd_0/result
/tb/gcd_0/done
/tb/gcd_0/reg_a
/tb/gcd_0/reg_b 3435
/tb/gcd_0/mux_sig_a a_sub_b
/tb/gcd_0/mux_sig_b a_sub_b
/tb/gcd_0/a_lt_b
/tb/gcd_0/b_neq_zero

11500 ns   12000 ns   12500 ns   13000 ns   13500 ns

Entity:tb Architecture: Date: Mon May 11 11:17:29 PM PDT 2015   Row: 1 Page: 1

/tb/input_file -2147483641
/tb/output_file -2147483640
/tb/in
/tb/out
/tb/i
/tb/clk
/tb/reset_n
/tb/start
/tb/done
/tb/a_in 3... 3435
/tb/b_in 2... 23423340
/tb/result 10
/tb/gcd_0/a_in 3... 3435
/tb/gcd_0/b_in 2... 23423340
/tb/gcd_0/start
/tb/gcd_0/reset_n
/tb/gcd_0/clk
/tb/gcd_0/result 10
/tb/gcd_0/done
/tb/gcd_0/reg_a 10
/tb/gcd_0/reg_b 0 3435
/tb/gcd_0/mux_sig_a hold a_sub_b
/tb/gcd_0/mux_sig_b hold a_sub_b
/tb/gcd_0/a_lt_b
/tb/gcd_0/b_neq_zero

+8500 ns   9000 ns   9500 ns   10000 ns   10500 ns   11000 ns

Entity:tb Architecture: Date: Mon May 11 11:17:13 PM PDT 2015 Row: 1 Page: 1

/tb/input_file -2147483641
/tb/output_file -2147483640
/tb/in
/tb/out
/tb/i
/tb/clk
/tb/reset_n
/tb/start
/tb/done
/tb/a_in 3435
/tb/b_in 2342340
/tb/result 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2...
/tb/gcd_0/a_in 3435
/tb/gcd_0/b_in 2342340
/tb/gcd_0/start
/tb/gcd_0/reset_n
/tb/gcd_0/clk
/tb/gcd_0/result 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2...
/tb/gcd_0/done
/tb/gcd_0/reg_a 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2... 2...
/tb/gcd_0/reg_b 3435
/tb/gcd_0/mux_sig_a a_sub_b
/tb/gcd_0/mux_sig_b a_sub_b
/tb/gcd_0/a_lt_b
/tb/gcd_0/b_neq_zero

+15 14000 ns   14500 ns   15000 ns   15500 ns   16000 ns   16500 ns

Entity:tb Architecture: Date: Mon May 11 11:17:54 PM PDT 2015   Row: 1 Page: 1

/tb/input_file -2147483641
/tb/output_file -2147483640
/tb/in
/tb/out
/tb/i
/tb/clk
/tb/reset_n
/tb/start
/tb/done
/tb/a_in 3435
/tb/b_in 2342340
/tb/result
/tb/gcd_0/a_in 3435
/tb/gcd_0/b_in 2342340
/tb/gcd_0/start
/tb/gcd_0/reset_n
/tb/gcd_0/clk
/tb/gcd_0/result
/tb/gcd_0/done
/tb/gcd_0/reg_a
/tb/gcd_0/reg_b 3435
/tb/gcd_0/mux_sig_a a_sub_b
/tb/gcd_0/mux_sig_b a_sub_b
/tb/gcd_0/a_lt_b
/tb/gcd_0/b_neq_zero

17000 ns   17500 ns   18000 ns   18500 ns   19000 ns
+15

Entity:tb Architecture: Date: Mon May 11 11:18:12 PM PDT 2015   Row: 1 Page: 1

/tb/input_file -2147483641
/tb/output_file -2147483640
/tb/in
/tb/out
/tb/i
/tb/clk
/tb/reset_n
/tb/start
/tb/done
/tb/a_in 3435
/tb/b_in 2342340
/tb/result 1...
/tb/gcd_0/a_in 3435
/tb/gcd_0/b_in 2342340
/tb/gcd_0/start
/tb/gcd_0/reset_n
/tb/gcd_0/clk
/tb/gcd_0/result 1...
/tb/gcd_0/done
/tb/gcd_0/reg_a 1...
/tb/gcd_0/reg_b 3435
/tb/gcd_0/mux_sig_a a_sub_b
/tb/gcd_0/mux_sig_b a_sub_b
/tb/gcd_0/a_lt_b
/tb/gcd_0/b_neq_zero

19500 ns   20000 ns   20500 ns   21000 ns   21500 ns   22000 ns

Entity:tb Architecture: Date: Mon May 11 11:18:33 PM PDT 2015   Row: 1 Page: 1