Gen 1, 2 and 3 Jedge install with no BRX power switch lockout switch - with Amazon MP1584EN voltage regulator Gen 2 and 3 JST plug J4 mates with "PWR" JST connector on BRX board next to BRX power switch Gen 1 Pin 2 is "lower" pin — closest to bottom edge of BRX board. regulator enable wire is soldered to left-hand pad - exposed when Amazon MP1584EN surface-mount resistor is Remove resistor Adjust output of regulator for removed from regulator 3.3 volts BEFORE connecting (see photo) (see photo) to ESP32 regulator enable wire is soldered to left-hand pad - exposed when Amazon MP1584EN VOUT-VOUT+ surface-mount resistor is NINremoved from regulator Remove resistor Adjust output of regulator for (see photo) (see photo) 3.3 volts BEFORE connecting to ESP32 If ESP32 board has an EN input, It must be grounded VIN-VOUT-VOUT+ BEFORE applying 3.3 volts to 3.3 volt power input enable wire is soldered pad of BRX power switch ide of BRX board (see photo) Alternate power connections Caution: Separate for regulator input: Pin 1 of J12 from J13 BEFORE "PWR" JST (battery positive) If ESP32 board has an EN connecting USB to to VIN+, "lower" pin of "5 volts" ESP32 input, It must be grounded ESP32. J12 J13 JST (battery ground) to VIN-BEFORE applying 3.3 volts to 3.3 volt power input 3.3 volts GND Caution: Separate J6 from J7 BEFORE 1017 1016 connecting USB to ESP32. ESP32 3.3 volts GND Regulator of to middle on undersi 1017 **BRX Power Battery** 1016 Switch Disconnect SW2 Rx (See photo) 1N4148 diode is for Gen 3 only observe polarity **BRX Bluetooth** Battery **BRX Battery BRX Power Switch** Module Disconnect **⊥BRX** Ground Tx Rx (See photo) **BRX Bluetooth BRX Battery** Module **⊥**BRX Ground Sheet: / File: Austin.sch Title: BRX Gen 1,2,3 Jedge install with MP1584EN voltage regulator Size: A4 Date: 7-18-2021 Rev: 1.0 KiCad E.D.A. eeschema 4.0.7 ld: 1/1