



FPGA book for absolute beginners

Max Maxfield - March 04, 2008

OK, here's the deal. A publisher contacted me recently asking me to review a proposal for a forthcoming book on FPGAs. This book is targeted toward absolute beginners, using simple examples to walk the reader through the various phases of design, synthesis, simulation, and place-and-route.

During our conversations, I mentioned that I often received questions from newcomers to Programmable Logic DesignLine asking about books of this ilk. In response, the publisher asked me to ask if anyone was interested in acting as a reviewer for this forthcoming book. You don't have to review the book itself, just a detailed description as to the table of contents and the topics that will be covered by each chapter.

Best of all, you actually get paid for it (although not handsomely) in either cold, hard cash or in books that you can select from their current portfolio.

The publisher is interested in hearing both from experts and absolute beginners (if you fall in the latter category, you should be knowledgeable about electronics in general, just not so familiar with using FPGAs). If you are interested, send me an email and I'll pass it on to the publisher.

Questions? Comments? Feel free to email me - Clive "Max" Maxfield - at max@techbites.com). And, of course, if you haven't already done so, don't forget to [Sign Up](#) for our weekly Programmable Logic DesignLine Newsletter.