/Users/hbm/Dropbox/DTU-Amigos/0...ngMachine/src/clock_manager.vhd Page 2 of 2 Saved: 5/5/13 7:51:45 PM Printed For: Hjalte Bested Møller

```
49
       process(clk_50, count, count3)
50
       begin
51
            if rising_edge(clk_50) then
52
                count <= count_next;</pre>
53
                count3 <= count3_next;</pre>
54
55
            end if;
       end process;
56
57
  end structure;
58
59
```