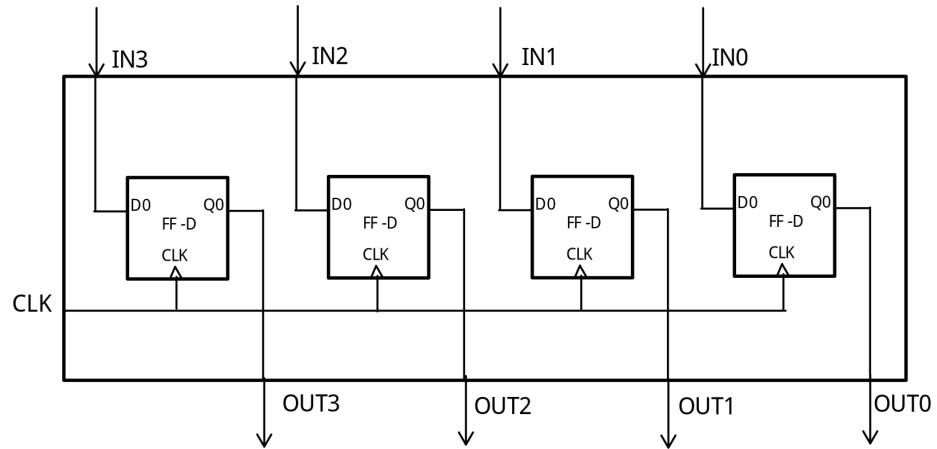
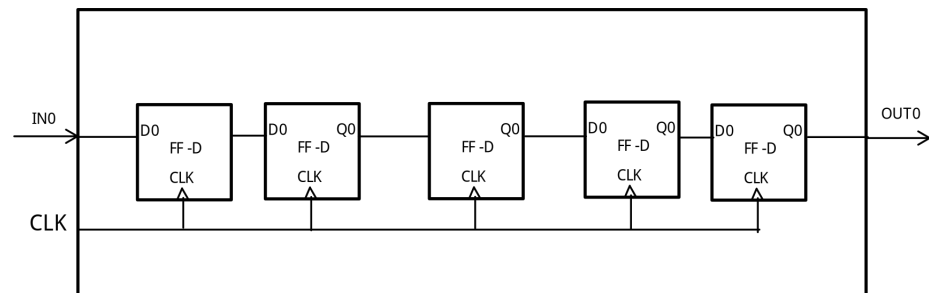


Practico 5 OdC

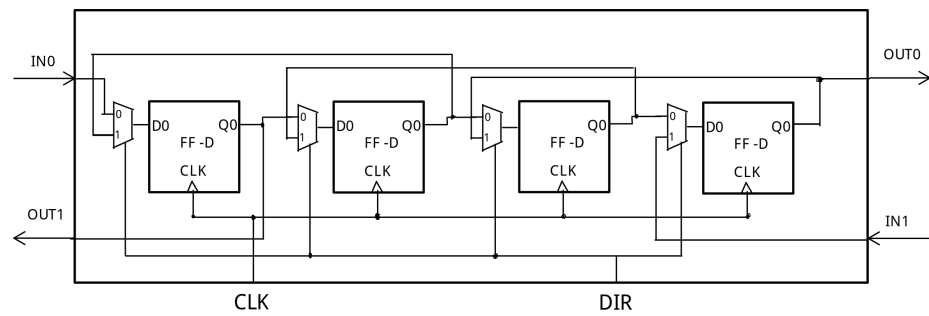
1)



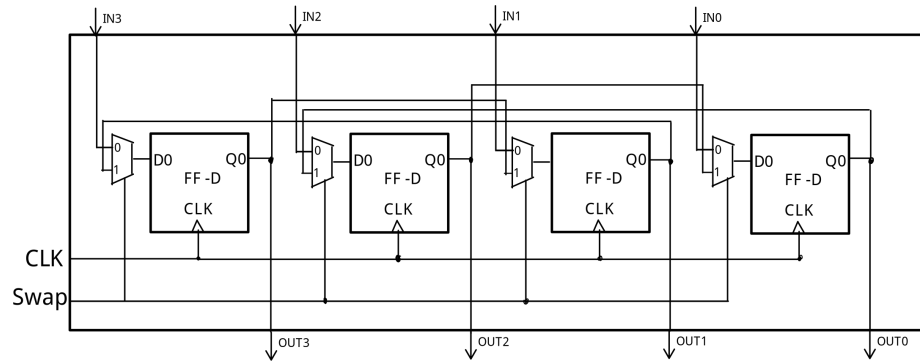
2)



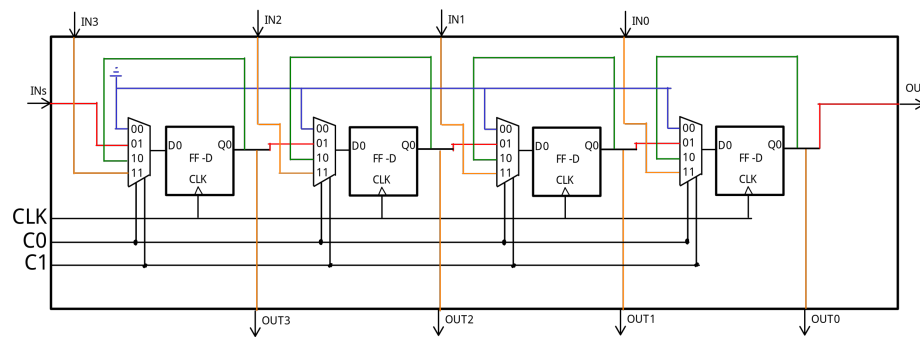
3)



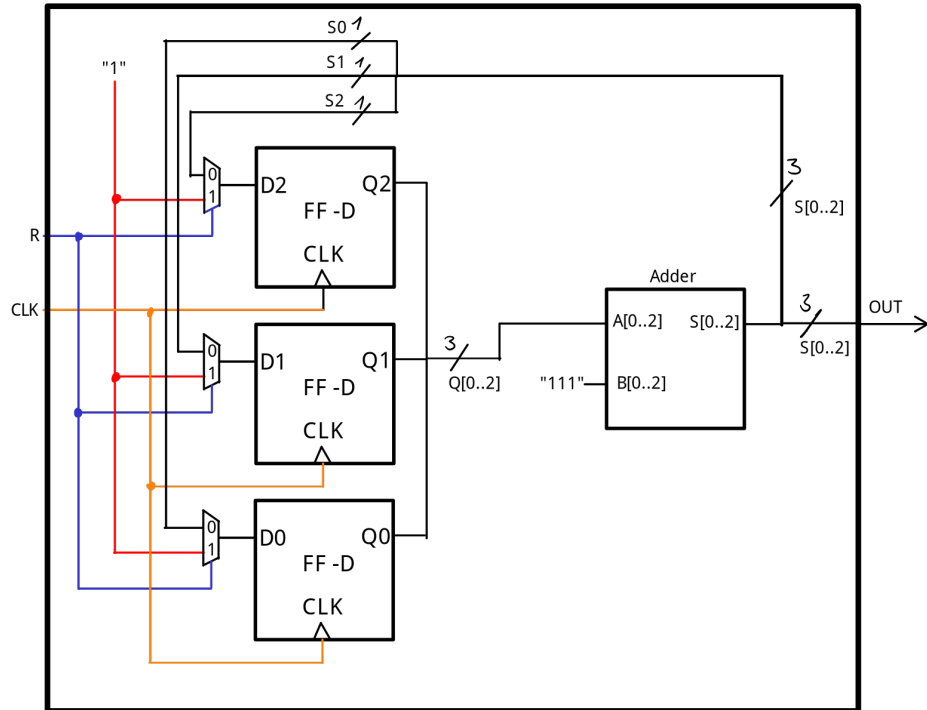
4)



5)

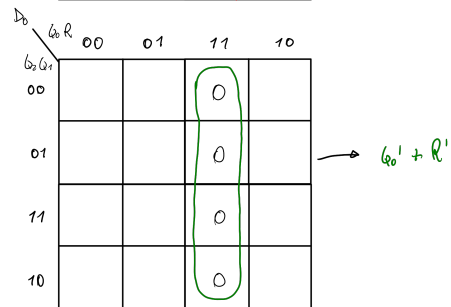
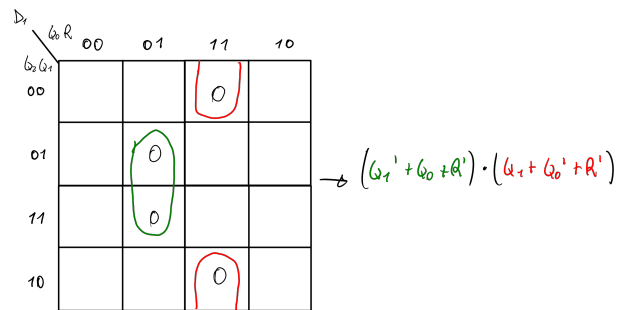
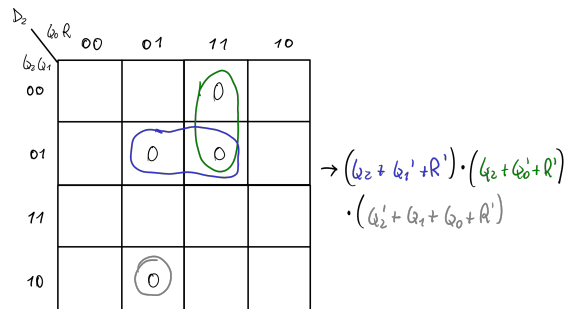


6)

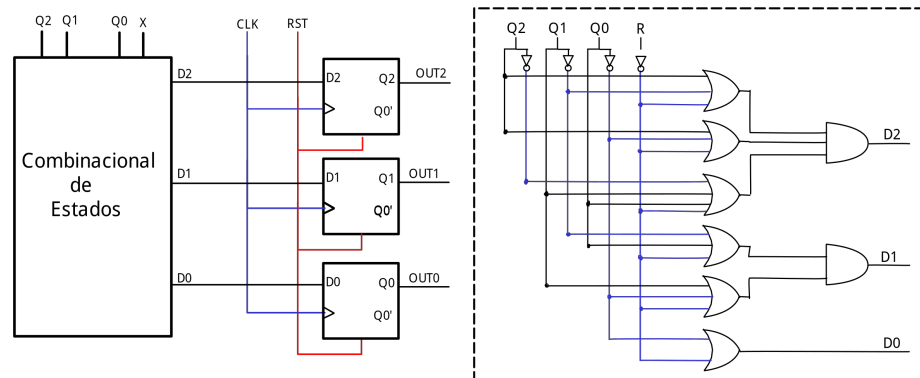


Entradas Combinacional de estados				Salidas combinacional de estados		
Estado actual			Entradas	Estado siguiente		
Q2	Q1	Q0	R	D2	D1	D0
1	1	1	0	1	1	1
1	1	1	1	1	1	0
1	1	0	0	1	1	0
1	1	0	1	1	0	1
1	0	1	0	1	1	1
1	0	1	1	1	0	0
1	0	0	0	1	1	1
1	0	0	1	0	1	1
0	1	1	0	1	1	1
0	1	1	1	0	1	0
0	1	0	0	1	1	1
0	1	0	1	0	0	1
0	0	1	0	1	1	1
0	0	1	1	0	0	0
0	0	0	0	1	1	1
0	0	0	1	1	1	1

Karnaugh:



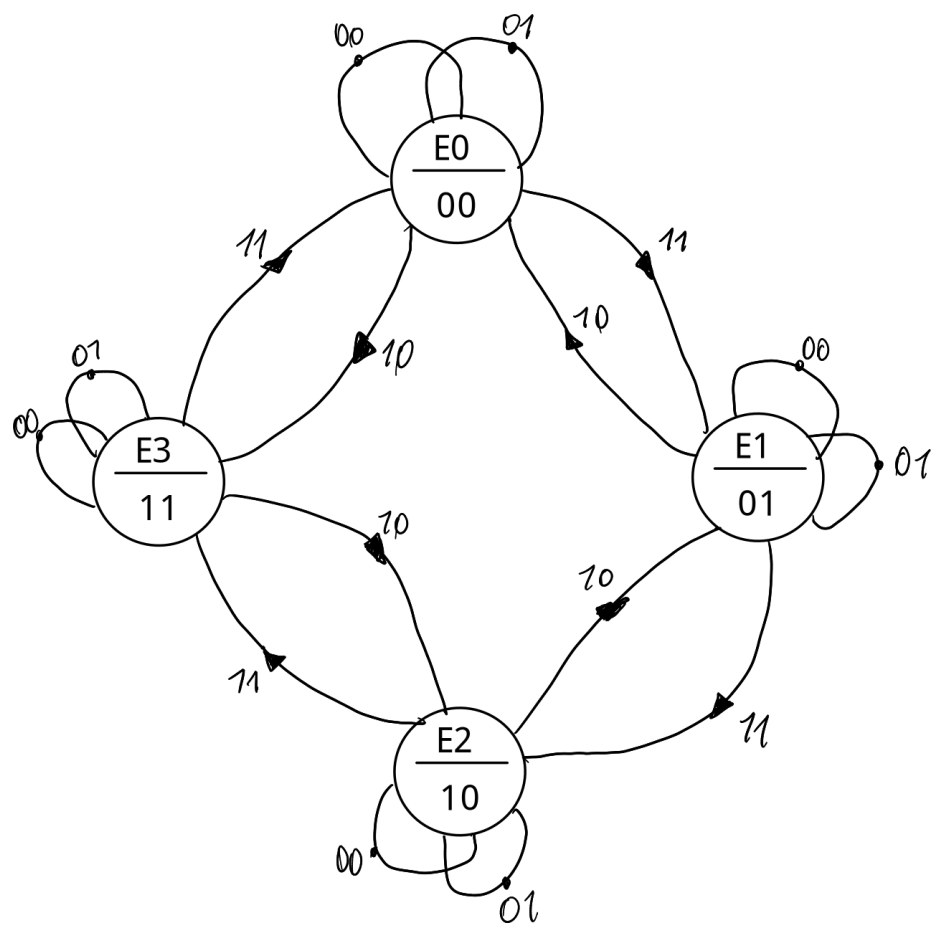
Implementacion



7)

Entradas Combinacional de estados				Salidas combinacional De estados	
Estado actual		Entradas		Estado siguiente	
Q1	Q0	E	X	D1	D0
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	1	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	0	1
0	1	1	0	0	0
0	1	1	1	1	0
1	0	0	0	1	0
1	0	0	1	1	0
1	0	1	0	0	1
1	0	1	1	1	1
1	1	0	0	1	1
1	1	0	1	1	1
1	1	1	0	1	0
1	1	1	1	0	0

Diagrama de estados:



Karnaugh:

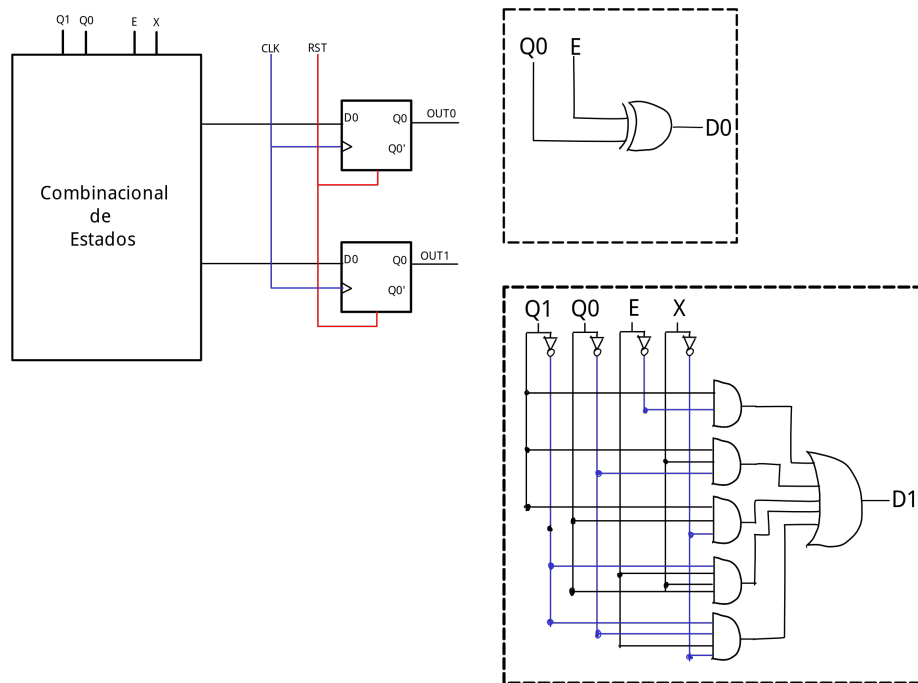
D_0	Ex	00	01	11	10
$Q_1 Q_0$	00				1
	01			1	
	11	1	1		1
	10	1	1	1	

$$\rightarrow Q_1 E' + Q_1 Q_0' X + Q_1 Q_0 X' + Q_1' Q_0 E X + Q_1' Q_0' E X'$$

D_1	Ex	00	01	11	10
$Q_1 Q_0$	00			1	1
	01	1	1		
	11	1	1		
	10			1	1

$$\rightarrow Q_0 E' + Q_0' E$$

Implementacion:



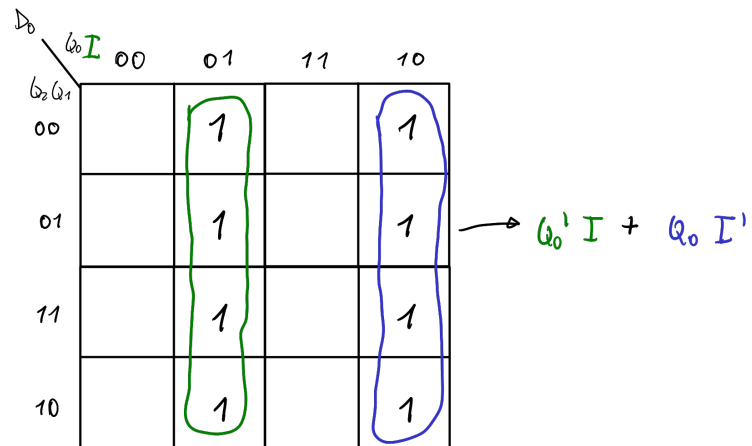
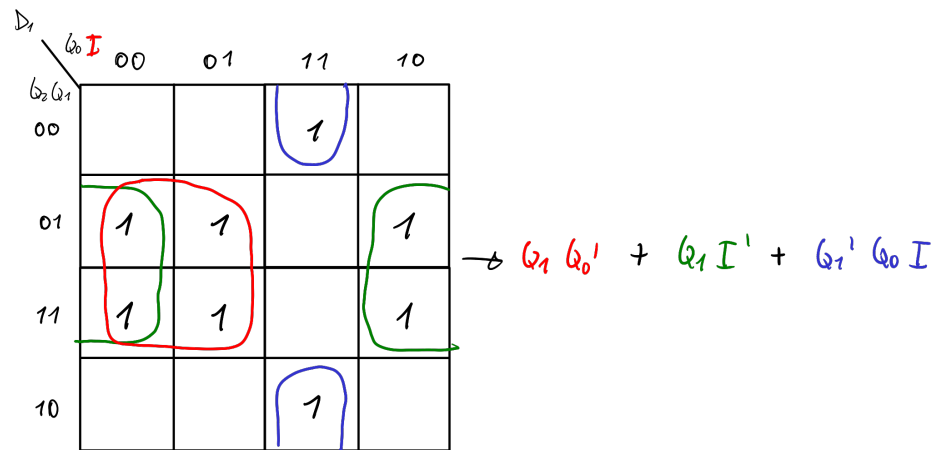
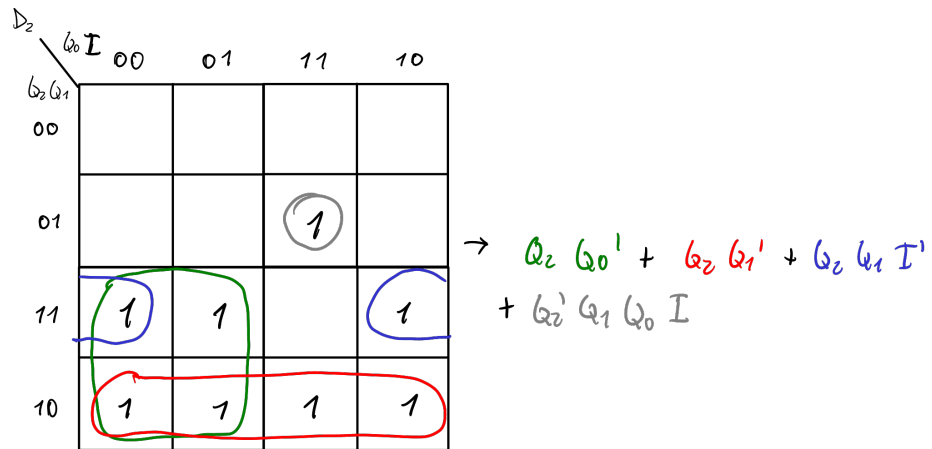
8)

Combinacional de entrada:

Tabla

Entradas Combinacional de estados				Salidas combinacional de estados		
Estado actual			Entradas	Estado siguiente		
Q2	Q1	Q0	INC	D2	D1	D0
0	0	0	0	0	0	0
0	0	0	1	0	0	1
0	0	1	0	0	0	1
0	0	1	1	0	1	0
0	1	0	0	0	1	0
0	1	0	1	0	1	1
0	1	1	0	0	1	1
0	1	1	1	1	0	0
1	0	0	0	1	0	0
1	0	0	1	1	0	1
1	0	1	0	1	0	1
1	0	1	1	1	1	0
1	1	0	0	1	1	0
1	1	0	1	1	1	1
1	1	1	0	1	1	1
1	1	1	1	0	0	0

karnaugh:

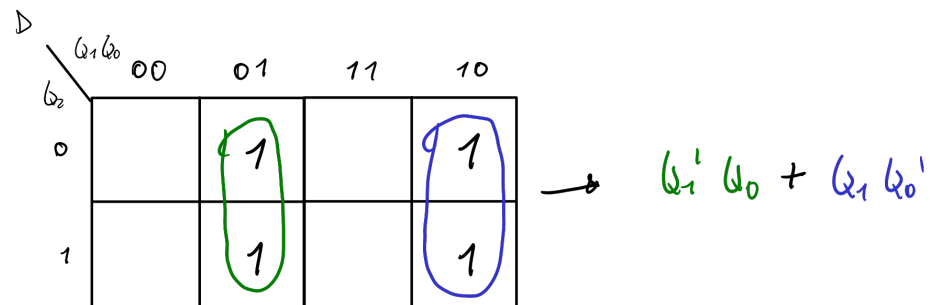
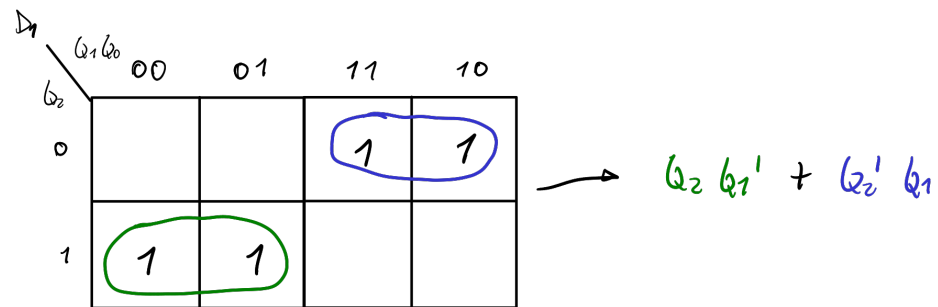
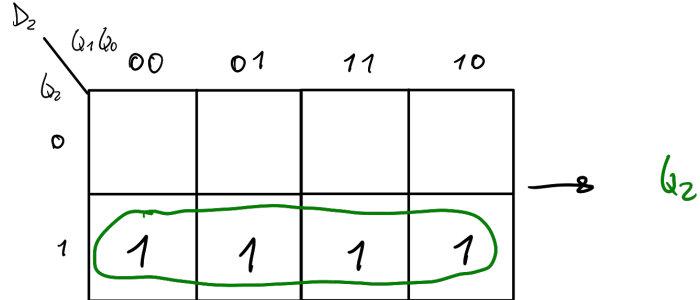


Combinacional de salida:

Tabla

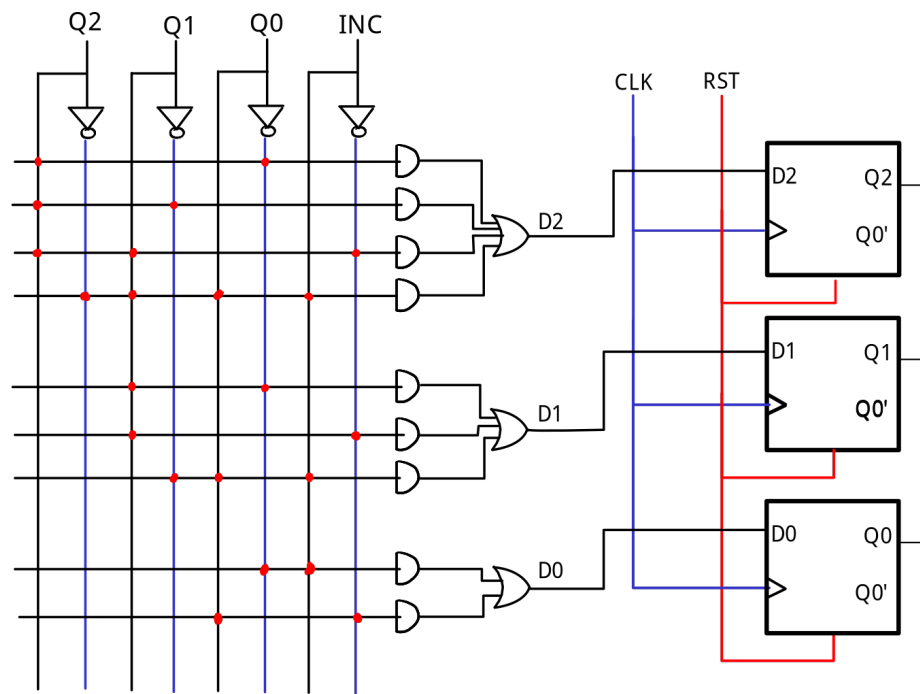
Estado Actual				Salida		
Codif	Q2	Q1	Q0	D2	D1	D0
E0	0	0	0	0	0	0
E1	0	0	1	0	0	1
E2	0	1	0	0	1	1
E3	0	1	1	0	1	0
E4	1	0	0	1	1	0
E5	1	0	1	1	1	1
E6	1	1	0	1	0	1
E7	1	1	1	1	0	0

karnaugh:

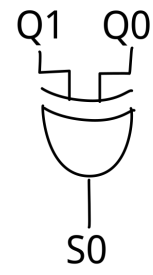
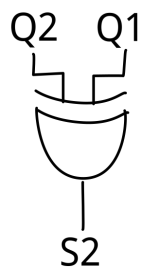


Implementacion

Combinacional de entrada:



Combinacional de salida



11)

