Aliaksei Laurynovich

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I need a visa to work in the UK

Objective

To be able to develop my professional skills and contribute my knowledge in electronics and software applications, such as designing, testing, troubleshooting, and modifying embedded systems.

Skills

- Deep understanding of MIPS computer architecture
- Hardware programing skills in Verilog
- High- and low-level programing skills in C, C++, Python, MatLab
- Team-worker, good work ethics
- Fluent in English and Russian

Work history

Summer Engineering Intern **May 2018 – Aug 2018**

The University of Edinburgh

Designed and built a Python GUI for plant-phenotyping platform Phenotiki under supervision of Dr. Tsaftaris. Was using object-oriented approach. Developed programing and team-communication skills.

Summer Engineering Intern **May 2017 – July 2017**

The University of Edinburgh

Designed and built a communication system between multiple RaspberryPi computers for plant-phenotyping platform Phenotiki. Used Bush scripting and SSH port for communication. Presented my work to the project supervisor Dr. Tsaftaris and his team. This system is now being used by Valerio Giuffrida, PhD student at the University of Edinburgh, for his PhD research.

Education

The University of Essex **Oct 2018 – Current**

- Master of Science: Internet of Things

The University of Edinburgh **Sep 2015 – May 2018**

- Bachelor of Engineering with Honours: Electrical Engineering and Electronics, 2:1 degree
- Thesis topic: Development of intelligent and robust camera sensor for agriculture (A, 72%). Fixed bugs and developed the web-interface for plant phenotyping platform, improved the system by building a hardware-trigger, tested and simulated the system.

Projects

- IoT Challenge 2017: setup PyCom microcontroller to process and exchange sound data via IoT. **Feb 2017**
- Cisco Switch Up Challenge Final: tested proximity sensors for smart gym and presented my work. **Dec 2016 – Jan 2017**
- Engineering Software 3 Project: built the Traffic Light State Machine on MicroBlaze embedded processor in C. **Oct 2016 – Nov 2016**
- Digital Systems Laboratory 3 Project: programmed the Snake Game on FPGA board using Verilog. **Oct 2016 – Nov 2016**

References available on the request
Dr. Sotirios Tsaftaris
Chancellor's Fellow (Senior Lecturer) @ University of Edinburgh
Alexander Graham Bell building