Birzeit University logo

Faculty of Engineering and Technology

Electrical and Computer Engineering Department

Digital Design ENCS2340

**VERILOG PROJECT**

For the following circuit, Use QUARTUS tool to:

1. Write a Verilog description for the Quad\_Mux2x1.
2. Write a Verilog description for the BCD\_7Segment\_driver.
3. Write Verilog Description for the Decoder2x4
4. Generate block for each module in part “a” to part “c” above, and connect them using QUARTUS.
5. Generate simulation for each component in parts “a” to “d”.

The report should include the code, the Quartus diagram, and screen shots for simulation.

