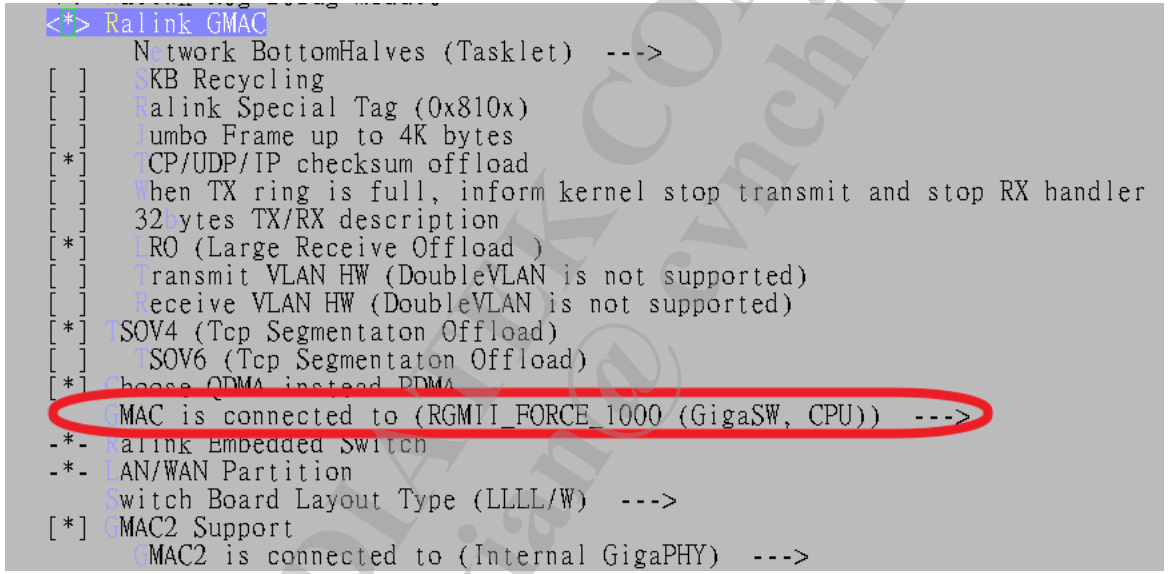
ISSUE TITLE	Improve RGMII timing and Electrical Characteristics				
SDK VERSION	4.2.0.0	CHIP	MT7621	Date	2013/12/25

Description:

We found TRGMII mode cannot work properly in some situations. Please **MUST** change Linux setting from Turbo RGMII mode to RGMII mode. In addition, we have new RGMII setting to improve timing and electrical characteristics. Please apply below patch as well.

How to Fix:

1. Change Linux setting from TRGMII to RGMII.



2. *Modify RT288x_SDK/source/linux-2.6.36.x/drivers/net/raeth/raether.c*

```
void setup_internal_gsw(void)
{
.....
 #if defined (CONFIG_GE1_TRGMII_FORCE_1200) && defined (CONFIG_MT7621_ASIC)
        mii_mgr_write(0, 14, 0x3); /*TRGMII*/
#else
        mii_mgr_write(0, 14, 0x1);   /*RGMII*/
        /* set MT7530 central align */
        mii_mgr_read(31, 0x7830, &regValue);
        regValue &= ~1;
        regValue |= 1<<1;
        mii_mgr_write(31, 0x7830, regValue);

        mii_mgr_read(31, 0x7a40, &regValue);
        regValue &= ~(1<<30);
        mii_mgr_write(31, 0x7a40, regValue);

        regValue = 0x855;
        mii_mgr_write(31, 0x7a78, regValue);
.....
}
```