

ISSUE TITLE	MT7601 module initialization conditional fail Patch			
Driver VERSION	Linux v3.0.0.3 /Android JB	CHIP	MT7601U/MT7601E	Date 2014/02/10
Description: The patch optimizes Power Management Unit (PMU) setting to prevent initialization failures.				
How to Fix: <pre>diff -urN WIFI_MT7601_bak/common/rtmp_init_inf.c WIFI_MT7601/common/rtmp_init_inf.c --- WIFI_MT7601_bak/common/rtmp_init_inf.c 2014-01-28 19:35:41.077303375 +0800 +++ WIFI_MT7601/common/rtmp_init_inf.c 2014-01-28 18:56:41.446716514 +0800 @@ -149,6 +149,18 @@ #ifdef MT7601 if (IS_MT7601(pAd) && (pAd->WlanFunCtrl.field.WLAN_EN == 0)) MT7601_WLAN_ChipOnOff(pAd, TRUE, FALSE); + + /* Offset 0x006c <5:1> PMU_OCLEVEL<5:1> default <2'b01100> change to <2'b00110> */ + if (IS_MT7601(pAd)) + { + LDO_CTRL0_STRUC ldo_ctr0; + + RTMP_IO_READ32(pAd, LDO_CTRL0, &ldo_ctr0.word); + printk("1. LDO_CTRL0(%x) = %x, PMU_OCLEVEL %x\n", LDO_CTRL0, ldo_ctr0.word, ldo_ctr0.field.PMU_OCLEVEL);</pre>				

```
+      ldo_ctr0.field.PMU_OCLEVEL=0x6;
+      RTMP_IO_WRITE32(pAd, LDO_CTRL0, ldo_ctr0.word);
+      printk("2. LDO_CTRL0(%x) = %x, PMU_OCLEVEL %x\n", LDO_CTRL0, ldo_ctr0.word, ldo_ctr0.field.PMU_OCLEVEL);
+  }
#endif /* MT7601U */
//---

diff -urN WIFI_MT7601_bak/include/mac_ral/nmac/ral_nmac.h WIFI_MT7601/include/mac_ral/nmac/ral_nmac.h
--- WIFI_MT7601_bak/include/mac_ral/nmac/ral_nmac.h 2014-01-28 19:36:28.316709496 +0800
+++ WIFI_MT7601/include/mac_ral/nmac/ral_nmac.h 2014-01-28 17:42:01.735034118 +0800
@@ -410,6 +410,39 @@
/* Register format */
/* ===== */

#define LDO_CTRL0          0x6C
#ifdef RT_BIG_ENDIAN
typedef union _LDO_CTRL0_STRUC{
+  struct{
+      UINT32 Rsv:8;
+      UINT32 PMU_VTUNE:8;
+      UINT32 PMU_DLY:3;
+      UINT32 PMU_OCDLY:3;
```

```
+    UINT32 PMU_SSCPERI:1;
+    UINT32 PMU_SSCGEN:1;
+    UINT32 PMU_SSC:2;
+    UINT32 PMU_OCLEVEL:5;
+    UINT32 PMU_OCDISB:1;
+    }field;
+    UINT32 word;
+}LDO_CTRL0_STRUC, *PLDO_CTRL0_STRUC;
+#else
+typedef union _LDO_CTRL0_STRUC{
+    struct{
+        UINT32 PMU_OCDISB:1;
+        UINT32 PMU_OCLEVEL:5;
+        UINT32 PMU_SSC:2;
+        UINT32 PMU_SSCGEN:1;
+        UINT32 PMU_SSCPERI:1;
+        UINT32 PMU_OCDLY:3;
+        UINT32 PMU_DLY:3;
+        UINT32 PMU_VTUNE:8;
+        UINT32 Rsv:8;
+    }field;
+    UINT32 word;
```

```
+}LDO_CTRL0_STRUC, *PLDO_CTRL0_STRUC;  
+#endif /* RT_BIG_ENDIAN */  
+
```

```
#define WLAN_FUN_CTRL      0x80  
#ifdef RT_BIG_ENDIAN
```